

Description

The SX100P10T uses advanced technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V.

This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = -95V$ $I_D = -100A$

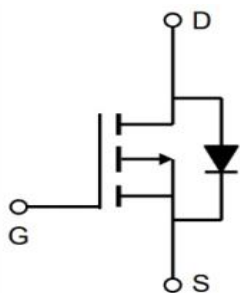
$R_{DS(ON)} < 16m\Omega$ @ $V_{GS} = 10V$

Application

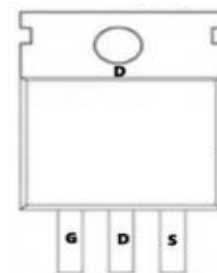
Brushless motor

Load switch

Uninterruptible power supply



TO-263-3L



Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-95	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_c = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-100	A
$I_D @ T_c = 100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V^1$	-66	A
I_{DM}	Pulsed Drain Current ²	-300	A
EAS	Single Pulse Avalanche Energy ³	174	mJ
I_{AS}	Avalanche Current	-50	A
$P_D @ T_c = 25^\circ C$	Total Power Dissipation ⁴	280	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	1.1	$^\circ C/W$

P-Channel Electrical Characteristics (T_J =25 °C, unless otherwise noted)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-95	-102	-	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} =-95V, V _{GS} =0V,	-	-	-1.0	μA
IGSS	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
VGS(th)	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.0	-1.6	-2.5	V
RDS(on)	Static Drain-Source on-Resistance	V _{GS} =-10V, I _D =-20A	-	12	16	mΩ
		V _{GS} =-4.5V, I _D =-10A	-	15	20	
Ciss	Input Capacitance	V _{DS} =-50V, V _{GS} =0V, f=1.0MHz	-	4200	-	pF
Coss	Output Capacitance		-	910	-	pF
Crss	Reverse Transfer Capacitance		-	55	-	pF
Q _g	Total Gate Charge	V _{DS} =-50V, I _D =-20A, V _{GS} =-10V	-	58	-	nC
Q _{gs}	Gate-Source Charge		-	12.9	-	nC
Q _{gd}	Gate-Drain("Miller") Charge		-	10.2	-	nC
td(on)	Turn-on Delay Time	V _{DD} =-50V, I _D =-5A, R _G =6Ω, V _{GS} =-10V	-	15	-	ns
t _r	Turn-on Rise Time		-	17	-	ns
td(off)	Turn-off Delay Time		-	70	-	ns
t _f	Turn-off Fall Time		-	15	-	ns
IS	Maximum Continuous Drain to Source Diode Forward Current		-	-	-100	A
ISM	Maximum Pulsed Drain to Source Diode Forward Current		-	-	-300	A
VSD	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =-6.2A	-	-	-1.2	V
t _{rr}	Body Diode Reverse Recovery Time	T _J =25°C, I _F =-10A, dI/dt=100A/μs	-	42	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	20	-	nC

Note :

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、The EAS data shows Max. rating . The test condition is VDD =-72V,VGS =-10V,L=0.1mH,IAS =-50A
- 4、The power dissipation is limited by 150°Cjunction temperature
- 5、The data is theoretically the same as I D and I DM , in real applications , should be limited by total power dissipation.

Typical Characteristics

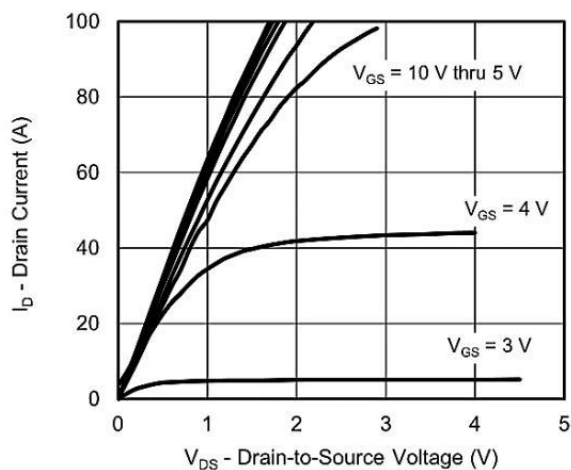


Figure 1: Output Characteristics

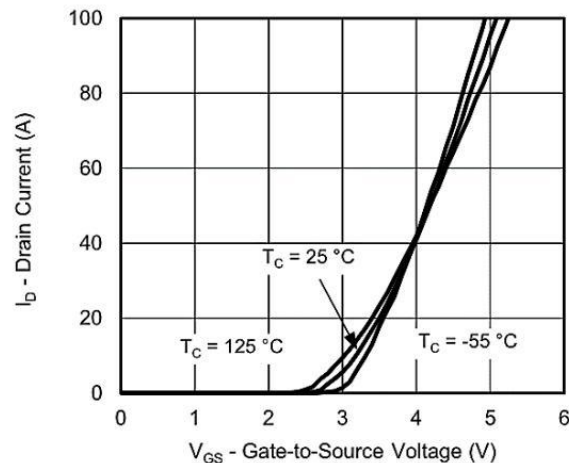


Figure 2: Transfer Characteristics

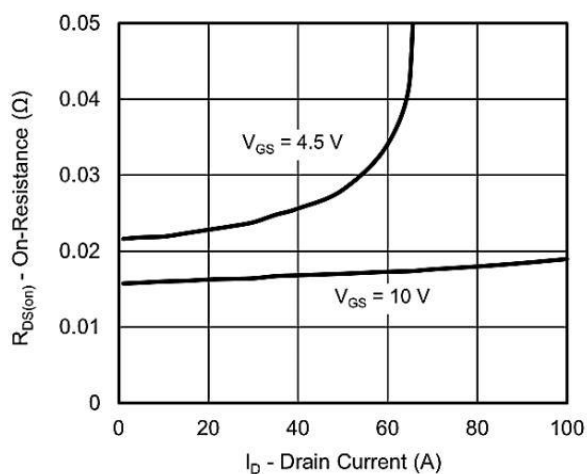


Figure 3: On-Resistance vs. Drain Current

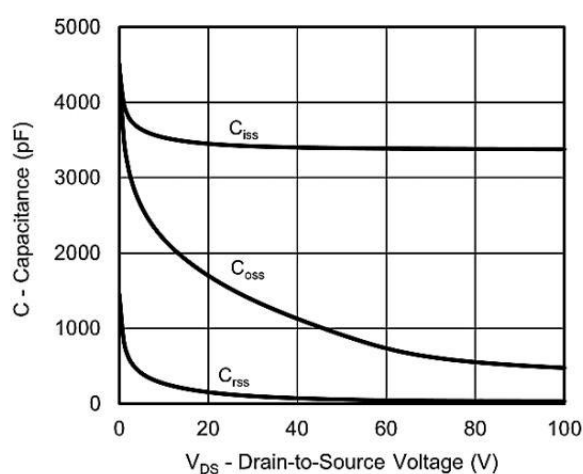


Figure 4: Capacitance

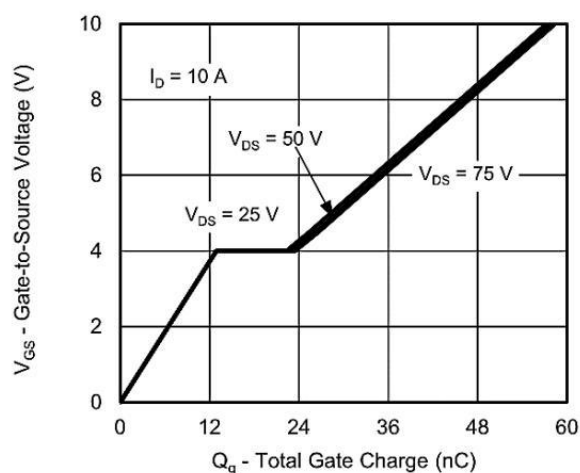


Figure 5: Gate Charge

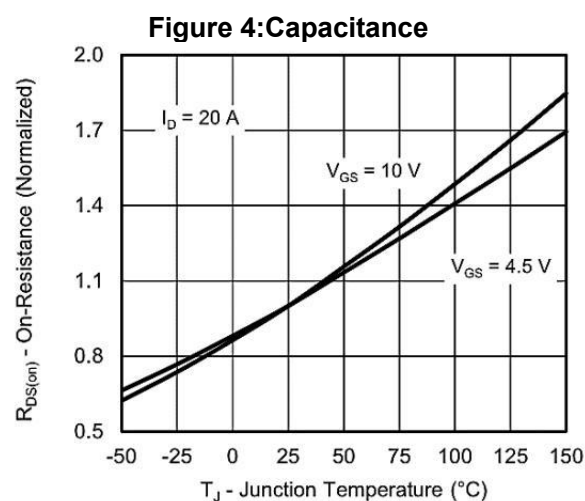


Figure 6: On-Resistance vs. Junction Temperature

Typical Characteristics

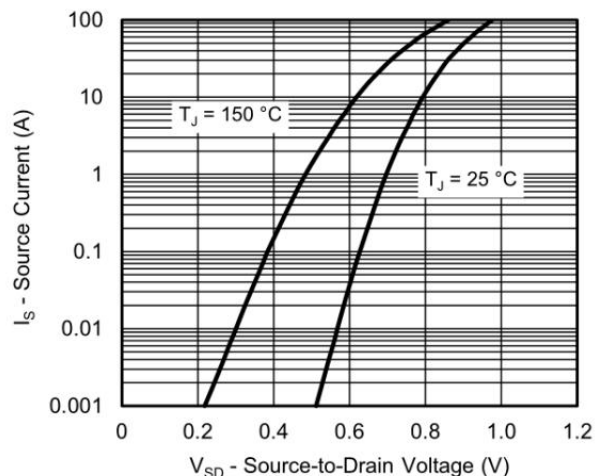


Figure 6: Source-Drain Diode Forward Voltage

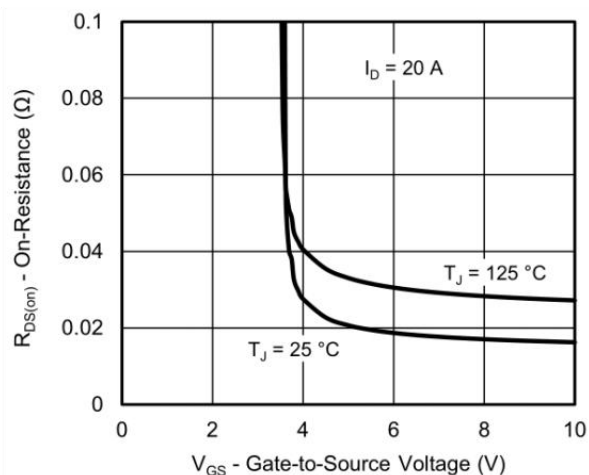


Figure 7: On-Resistance vs. Gate-to-Source Voltage

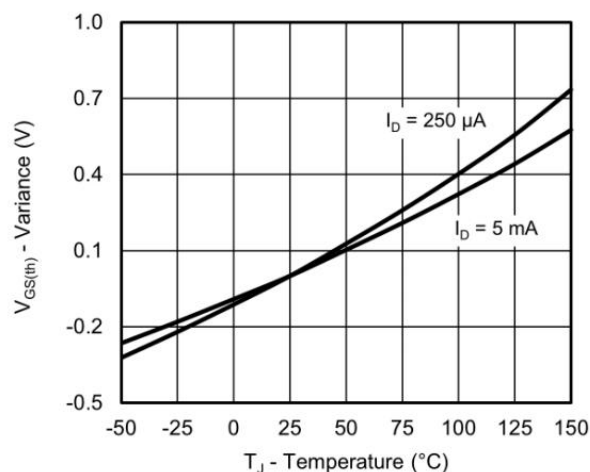


Figure 8: Threshold Voltage

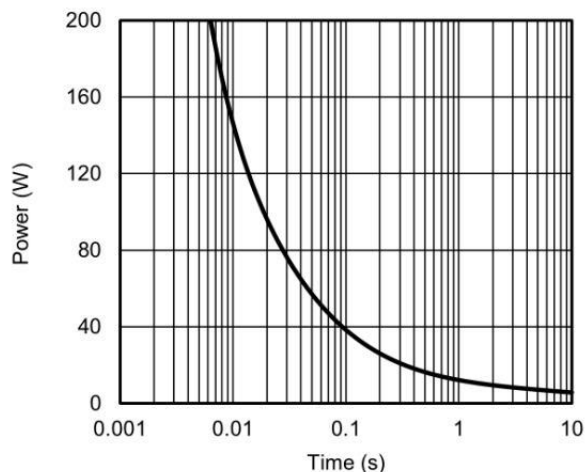


Figure 9: Single Pulse Power, Junction-to-Ambient

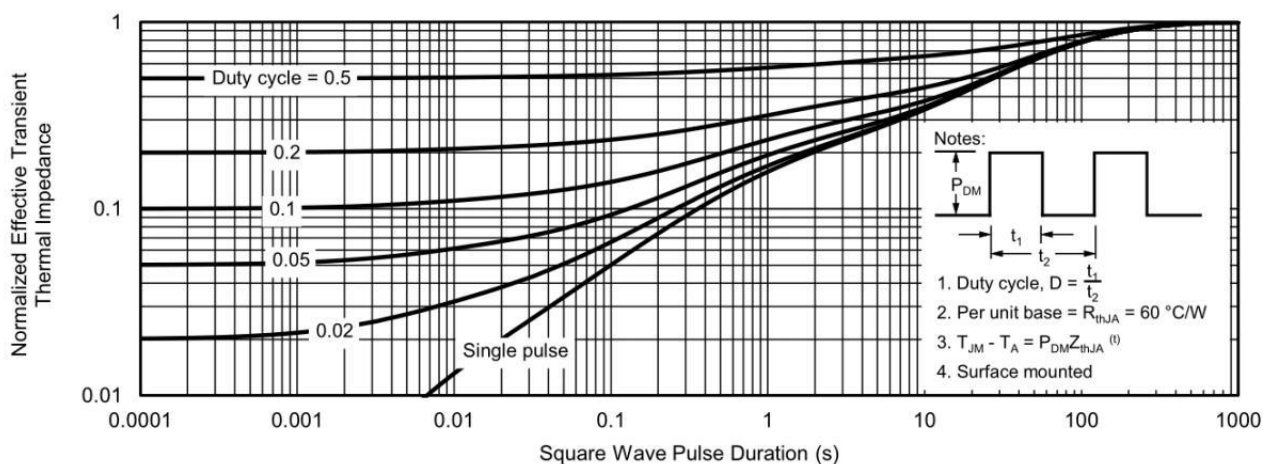
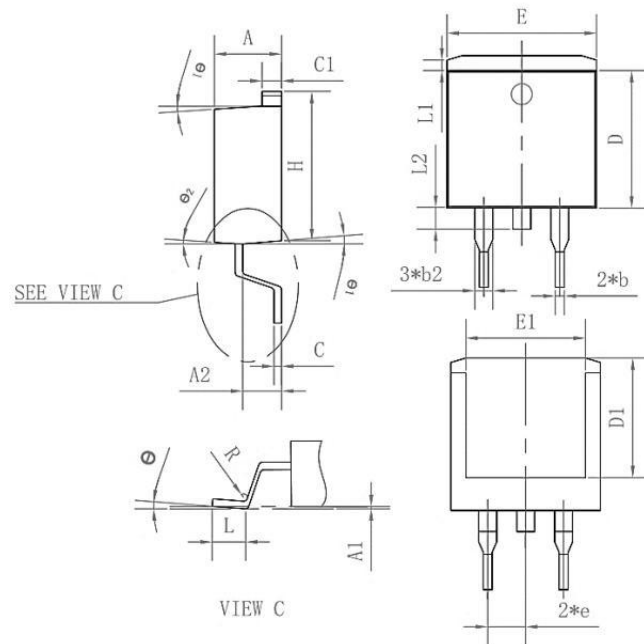


Figure 11: Normalized Thermal Transient Impedance, Junction-to-Ambient

Package Mechanical Data-TO-263-3L-SLK



Symbol	Common		
	mm		
	Mim	Nom	Max
A	4.35	4.47	4.60
A1	0.09	0.10	0.11
A2	2.30	2.40	2.70
b	0.70	0.80	1.00
b2	1.25	1.36	1.50
C	0.45	0.50	0.55
C1	1.29	1.30	9.40
D	9.10	9.20	9.30
D1	7.90	8.00	8.10
E	9.85	10.00	10.20
E1	7.90	8.00	8.10
H	15.30	15.50	15.70
e	-	2.54	-
L	2.34	2.54	2.74
L1	1.00	1.10	1.20
L2	1.30	1.40	1.50
R	0.24	0.25	0.26
θ	0°	4°	8°
Θ1	4°	7°	10°
Θ2	0°	3°	6°

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TO-263-3L		800