

IRLHM620TRPBF-VB Datasheet N-Channel 30 V (D-S) MOSFET

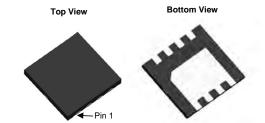
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
30	0.0018 at V _{GS} = 10 V	70	22.5 nC		
30	0.0025 at V _{GS} = 4.5 V	60	22.3110		

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Gen IV Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



DFN 3x3 EP

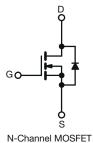


APPLICATIONS

- Switch Mode Power Supplies
- Personal Computers and Servers
- Telecom Bricks
- VRM's and POL

Top View





ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter	Symbol	Limit	Unit			
Drain-Source Voltage		V _{DS}	30	V		
Gate-Source Voltage		V _{GS}	+ 20, - 16	V		
	T _C = 25 °C		70			
Continuous Drain Current (T = 150 °C)	T _C = 70 °C	I_	50			
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	30.9 ^{a, b}			
	T _A = 70 °C		28.3 ^{a, b}	^		
Pulsed Drain Current (t = 300 μs)		I _{DM}	80	A		
Continuous Source-Drain Diode Current	T _C = 25 °C	1-	40 ^g			
Continuous Source-Diain Diode Current	T _A = 25 °C	I _S	3.3 ^{a, b}			
Single Pulse Avalanche Current L = 0.1		I _{AS}	20			
Single Pulse Avalanche Energy	L=0.11IIII	E _{AS}	20	mJ		
	T _C = 25 °C		52			
Maximum Power Dissipation	T _C = 70 °C	P _D	43	w		
Maximum Fower Dissipation	T _A = 25 °C	LD	3.7 ^{a, b}	VV		
	T _A = 70 °C		3.1 ^{a, b}			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperature) ^{c, d}			260			

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{a, e}	t ≤ 10 s	R _{thJA}	24	33	°C/W		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	1.9	2.4	O/ VV		

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. The DFN 3 x 3 is a leadless package. The end of the lead terminal is exposed pper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- d. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components. e. Maximum under steady state conditions is 81 °C/W.
- f. Based on $T_C = 25$ °C. g. Package limited.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	<u>'</u>			ı			
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			٧	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Γ _J		14		m\//°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 5.5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1.1		2.2	V	
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = + 20 V, - 16 V			± 100	nA	
Zana Oaka Walka wa Durin Oamani		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	40			Α	
	Б	V _{GS} = 10 V, I _D = 15 A		0.0018		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.0025			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		105		S	
Dynamic ^b	<u>'</u>			ı			
Input Capacitance	C _{iss}			3050		pF	
Output Capacitance	C _{oss}	V 45 V V 0 V (4 MU-		1040			
Reverse Transfer Capacitance	C _{rss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		79			
C _{rss} /C _{iss} Ratio				0.022	0.044		
	Q _g	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$		51	77	77	
Total Gate Charge				22.5	34		
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		8.6		nC	
Gate-Drain Charge	Q _{gd}			4			
Output Charge	Q _{oss}	V _{DS} = 15 V, V _{GS} = 0 V		30.5		1	
Gate Resistance	R_{g}	f = 1 MHz	0.3	1.25	2.5	Ω	
Turn-On Delay Time	t _{d(on)}			24	48		
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		17	34		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		25	50		
Fall Time	t _f			10	20	1	
Turn-On Delay Time	t _{d(on)}			12	24	ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 1.5 Ω		10	20	- - -	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		30	60		
Fall Time	t _f			8	16		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			40		
Pulse Diode Forward Current	I _{SM}				80	A	
Body Diode Voltage	V _{SD}	I _S = 5 A, V _{GS} = 0 V		0.73	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}			36	70	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 40 A 41/44 400 A/1- T 07.00		24	48	nC	
Reverse Recovery Fall Time	ta	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		16		1	
Reverse Recovery Rise Time	t _b			20		ns	

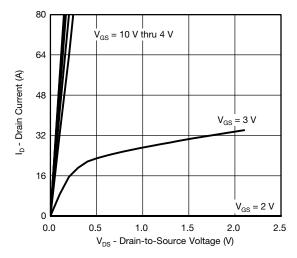
Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

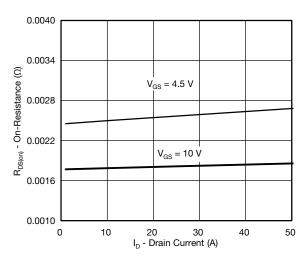
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



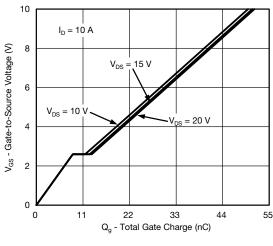
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



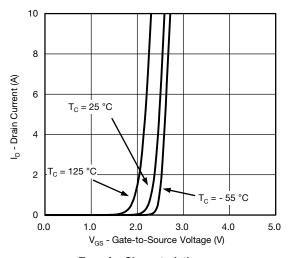
Output Characteristics



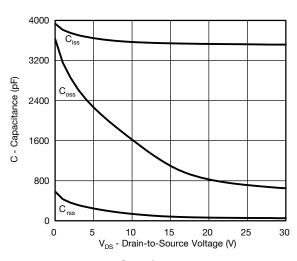
On-Resistance vs. Drain Current and Gate Voltage



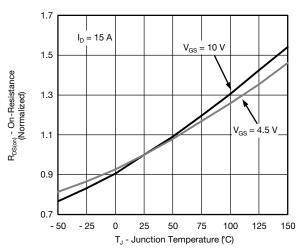
Gate Charge



Transfer Characteristics



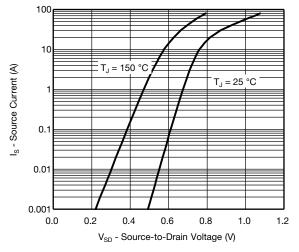
Capacitance



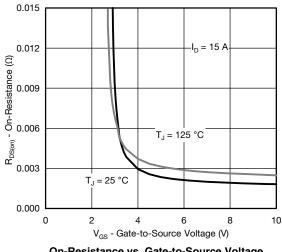
On-Resistance vs. Junction Temperature



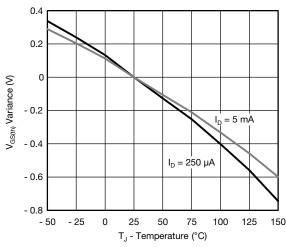
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



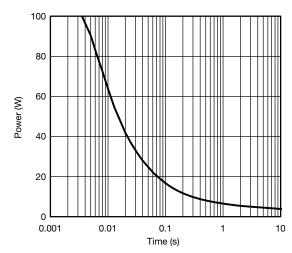
Source-Drain Diode Forward Voltage



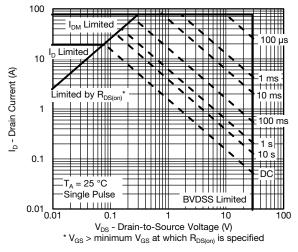
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



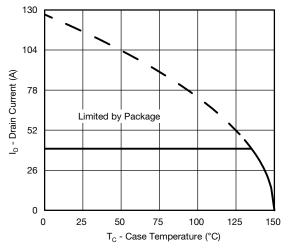
Single Pulse Power, Junction-to-Ambient



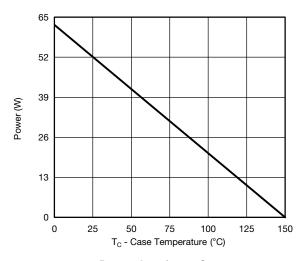
Safe Operating Area, Junction-to-Ambient



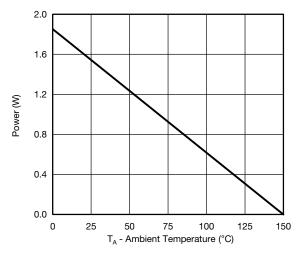
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*







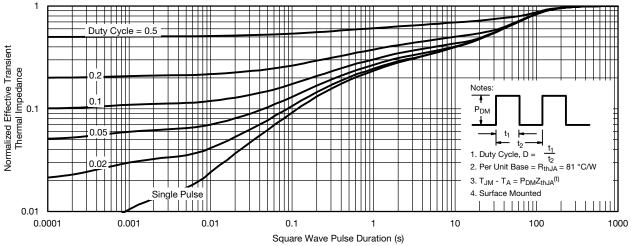
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

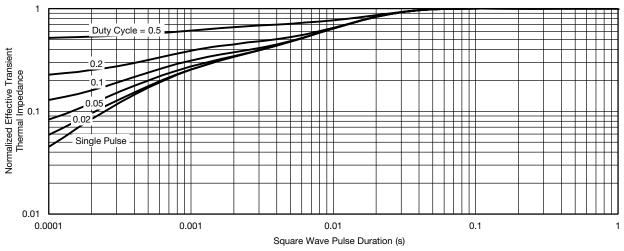
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

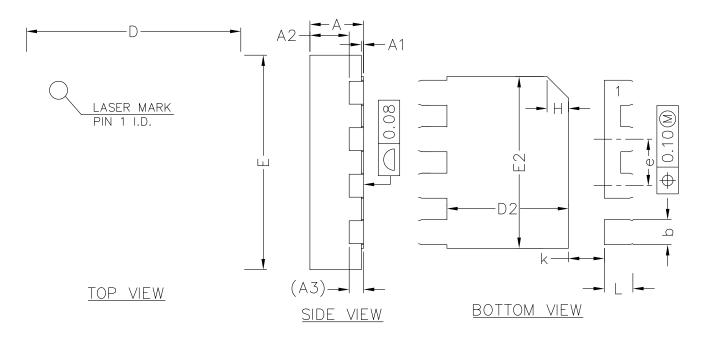


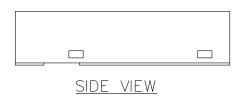
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case







COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX		
А	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.50	0.55	0.60		
A3	0.20REF				
Ь	0.30	0.35	0.40		
D	2.90	3.00	3.10		
E	2.90	3.00	3.10		
D2	1.60	1.70	1.80		
E2	2.30	2.40	2.50		
е	0.55	0.65	0.75		
K	0.40	0.50	0.60		
L	0.35	0.40	0.45		



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