

## **Description**

The IRFH9310TRPBF uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

 $V_{DS} = -30V I_{D} = -90A$ 

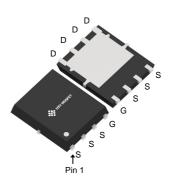
 $R_{DS(ON)}$  < 4.5 m $\Omega$   $V_{GS}$ =-10V

## **Application**

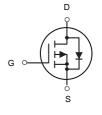
Battery protection

Load switch

Uninterruptible power supply



DFN5X6-8L (PQFN-8(5x6))



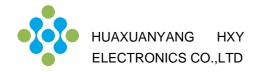
P-Channel MOSFET

### **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)	
IRFH9310TRPBF	DFN5X6-8L(PQFN-8(5x6))	9310 XXXX	5000	

## Absolute Maximum Ratings (Tc=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	-30	V	
Vgs	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-90	А	
Io@Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	-57	Α	
Ірм	Pulsed Drain Current <sup>2</sup>	-360	Α	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	125	mJ	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	60	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	
Reja	Thermal Resistance Junction-Ambient <sup>1</sup>	55	°C/W	
Rыс	Thermal Resistance Junction-Case <sup>1</sup>	2.08	°C/W	



## Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250μA	-30	-	-	V	
Gate-body Leakage current		Igss	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
Zero Gate Voltage Drain Current	TJ=25°C	lass	\\ 20\\\\\ 0\\	-	-	-1	μА	
	T <sub>J</sub> =100°C	IDSS	$V_{DS} = -30V, V_{GS} = 0V$	-	-	-100		
Gate-Threshold Voltage		V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	-1.0	-1.6	-2.5	V	
Drain-Source On-Resistance <sup>4</sup>		D	V <sub>GS</sub> = -10V, I <sub>D</sub> = -30A	-	3.5	4.5	- mΩ	
		R <sub>DS(on)</sub>	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -15A	-	4.8	6.2		
Forward Transconductance <sup>4</sup>		<b>g</b> fs	V <sub>DS</sub> = -10V, I <sub>D</sub> = -30A	-	90	-	S	
Input Capacitance		C <sub>iss</sub>		-	5070	-	pF	
Output Capacitance		Coss	V <sub>DS</sub> = -15V, V <sub>GS</sub> =0V, f =1MHz	-	695	-		
Reverse Transfer Capacitance		Crss		-	580	-		
Gate resistance	Gate resistance		f=1MHz	-	4	-	Ω	
Total Gate Charge		Qg		-	146	-		
Gate-Source Charge		Q <sub>gs</sub>	$V_{GS} = -10V, V_{DS} = -15V,$ $I_{D} = -30A$	-	21.5	-	nC	
Gate-Drain Charge		Q <sub>gd</sub>		-	39	-		
Turn-On Delay Time		t <sub>d(on)</sub>		-	23	-	ns ns	
Rise Time		<b>t</b> r	$V_{GS} = -10V, V_{DD} = -15V,$	-	15	-		
Turn-Off Delay Time		t <sub>d(off)</sub>	$R_G = 3\Omega$ , $I_D = -30A$	-	129	-		
Fall Time		<b>t</b> f		-	28	-		
Diode Forward Voltage <sup>4</sup>		V <sub>SD</sub>	I <sub>S</sub> = -30A, V <sub>GS</sub> = 0V	-	-	-1.2	V	
Continuous Source Current	T <sub>C</sub> =25°C	Is	-	-	-	-90	Α	

#### Note:

- 1. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}$ =150°C
- 2. The EAS data shows Max. rating . The test condition is  $V_{DD}$ = -25V,  $V_{GS}$ = -10V, L= 0.1mH,  $I_{AS}$ = -50A
- 3. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.



# **Typical Characteristics**

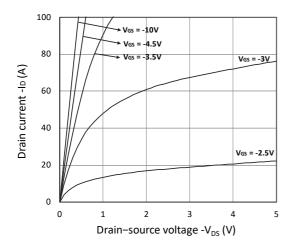


Figure 1. Output Characteristics

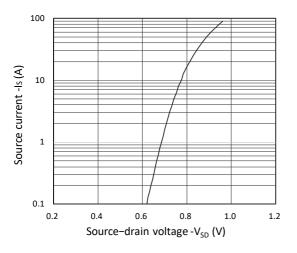


Figure 3. Forward Characteristics of Reverse

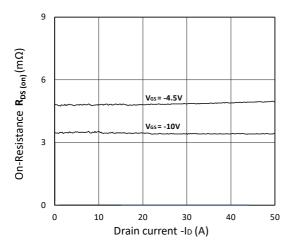


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$ 

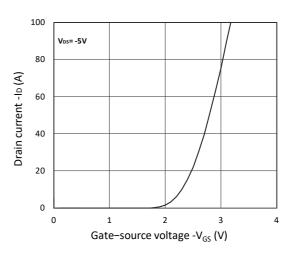


Figure 2. Transfer Characteristics

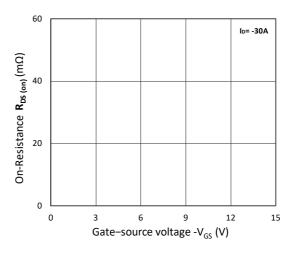


Figure 4.  $R_{DS(ON)}\ vs.\ V_{GS}$ 

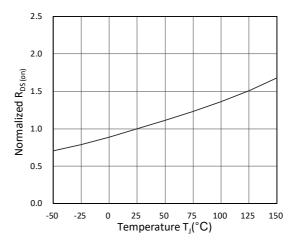
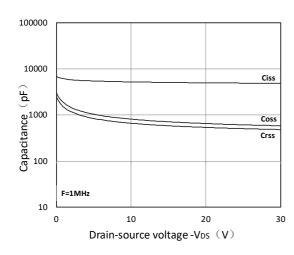


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature





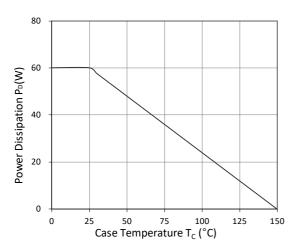


Figure 9. Power Dissipation

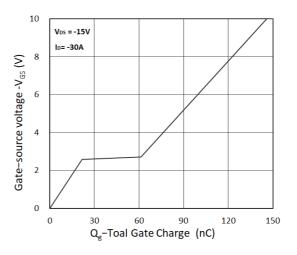


Figure 8. Gate Charge Characteristics

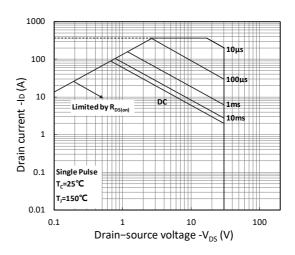


Figure 10. Safe Operating Area

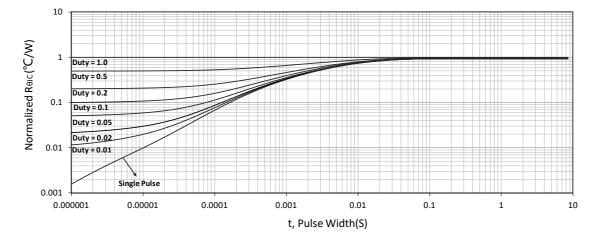


Figure 11. Normalized Maximum Transient Thermal Impedance



## **Test Circuit**

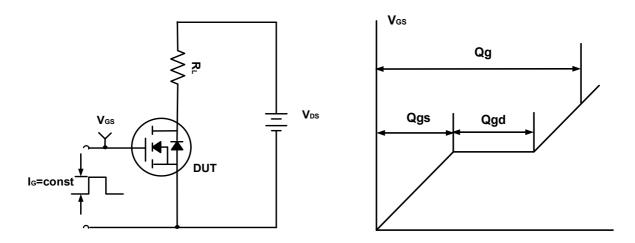


Figure A. Gate Charge Test Circuit & Waveforms

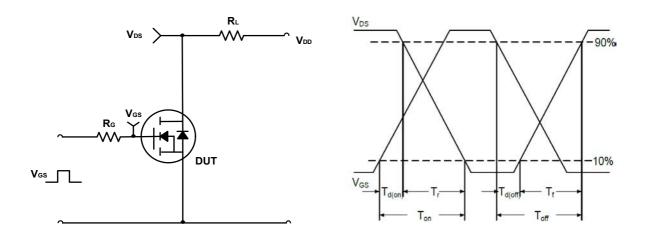


Figure B. Switching Test Circuit & Waveforms

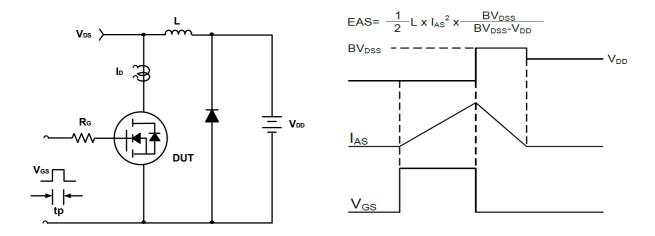
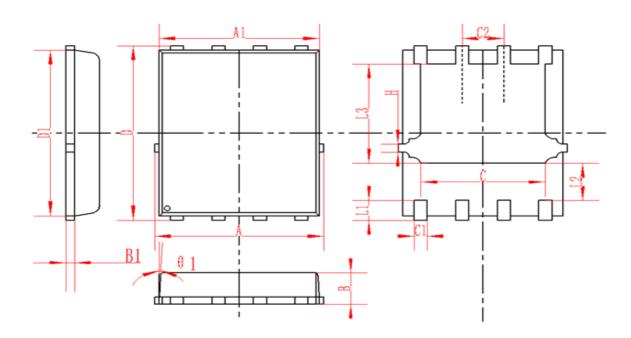


Figure C. Unclamped Inductive Switching Circuit & Waveforms



# DFN5X6-8L(PQFN-8(5x6)) Package Information



SYMBOL	MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX
Α	4.95	5	5.05	0.195	0.197	0.199
A1	4.82	4.9	4.98	0.190	0.193	0.196
D	5.98	6	6.02	0.235	0.236	0.237
D1	5.67	5.75	5.83	0.223	0.226	0.230
В	0.9	0.95	1	0.035	0.037	0.039
B1		0.254REF			0.010REF	
С	3.95	4	4.05	0.156	0.157	0.159
C1	0.35	0.4	0.45	0.014	0.016	0.018
C2		1.27TYP			0.5TYP	
θ1	8°	10°	12°	8°	10°	12°
L1	0.63	0.64	0.65	0.025	0.025	0.026
L2	1.2	1.3	1.4	0.047	0.051	0.055
L3	3.415	3.42	3.425	0.134	0.135	0.135
Н	0.24	0.25	0.26	0.009	0.010	0.010



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