

Features

- Ideal for printed circuit board mounting
- This series is UL listed under the Recognized Component Index, file number E142814
- The plastic material used carries Underwriters Laboratory flammability recognition 94V-0
- Built-in printed circuit board stand-offs
- High case dielectric strength
- High temperature soldering guaranteed 265°C/10 seconds at 5 lbs (2.3kg) tension



KBL

Ordering Information

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Product ID	Pack	Qty(PCS)
KBL10-E4/51	KBL	500



Maximum Ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	KBL10-E4/51	Unit
Maximum repetitive peak reverse voltage	VRRM	1000	V
Maximum RMS bridge input voltage	VRMS	700	V
Maximum DC blocking voltage	VDC	1000	V
Maximum average forward rectified output current at TA=50 °C	IF(AV)	4.0	А
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	IFSM	200	А
Rating for fusing (t<8.3ms)	l ² t	166	A ² sec
Typical thermal resistance per element (1)	ReJA	10.0	°C / W
Operating junction and storage temperature range	TJ, TSTG	-55 to + 150	°C

Electrical Characteristics

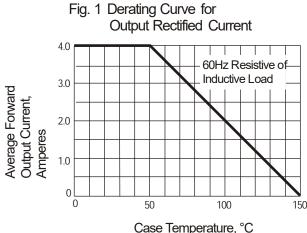
Rating at 25°C ambient temperature unless otherwise specified. Resistive or Inductive load, 60Hz. For Capacitive load derate by 20 %.

Parameter	Symbol	KBL10-E4/51	Unit
Maximum instantaneous forward voltage drop per leg at 4.0A	VF	1.1	V
Maximum DC reverse current at rated TA =25°C DC blocking voltage per element TA =125°C	IR	10 1000	μΑ

Notes: (1)Thermal resistance from Junction to Ambemt on P.C.board mounting.



Rating and Characteristic Curves(Ta=25°C unless otherwise noted)



150 Case Temperature, °C

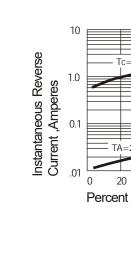
Fig. 3 Typical Instantaneous

200 8.3ms 175 Single half-sine-Wa Peak Forward Surge [JEDEC Method] 150 Current, Amperes 125 100 75 50 40 10 100 Number of Cycles at 60HZ

Fig. 2 Maximum Non-repetitive Peak

Forward Surge Current

Forward Characteristics 100 10 Instantaneous Forward Current, Amperes 1.0 0.1 Tj=25°C Pulse Width=300us 2% duty cycle .01 . 0.4 0.6 0.8 1.2 1.0 Instantaneous Forward Voltage, Volts



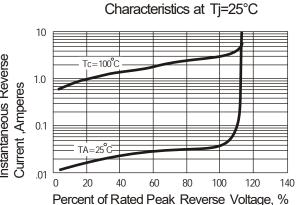
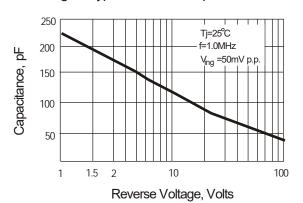


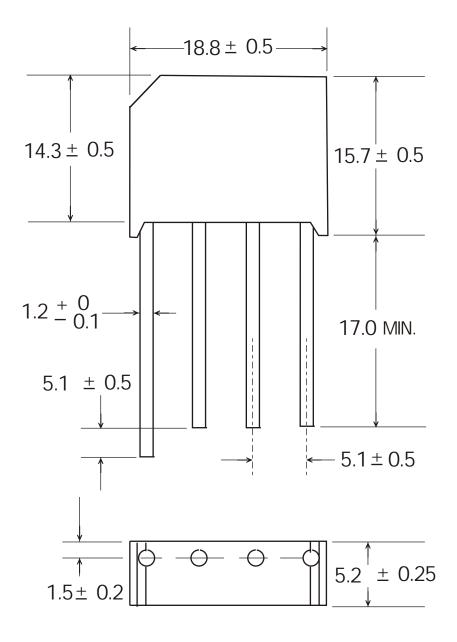
Fig. 4 Typical Reverse







KBL Package Outline Dimensions





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