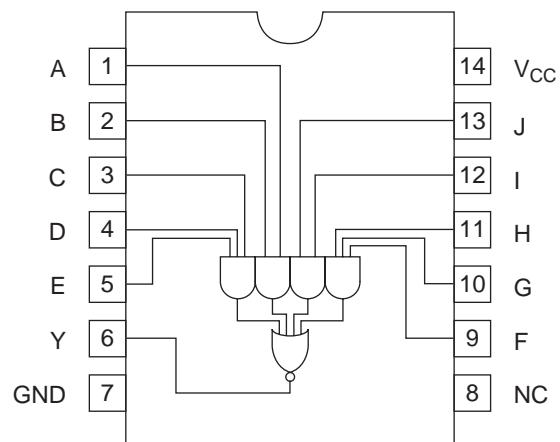
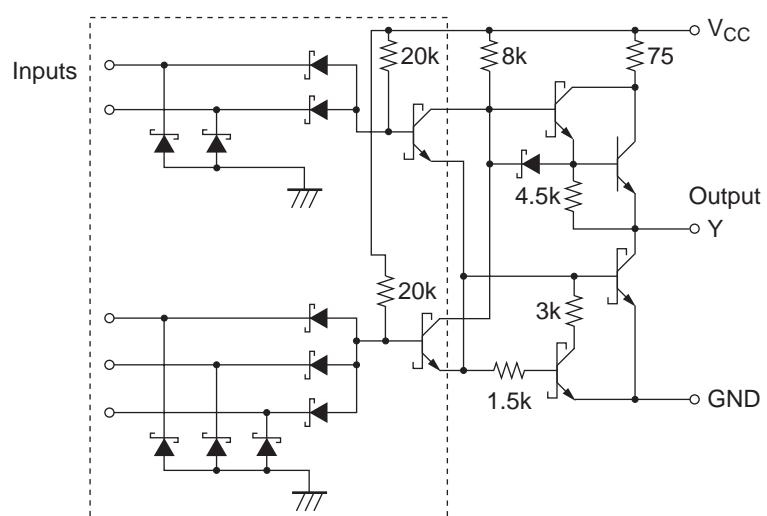


Pin Arrangement



(Top view)

Circuit Schematic



Note: The schematic within the dashed line is included the half input terminals of HD74LS54.

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	T _{STG}	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Typ	Max	Unit
Supply voltage	V _{CC}	4.75	5.00	5.25	V
Output current	I _{OH}	—	—	-400	μA
	I _{OL}	—	—	8	mA
Operating temperature	T _{OPR}	-20	25	75	°C

Electrical Characteristics

(Ta = -20 to +75 °C)

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	—	—	V	
	V _{IL}	—	—	0.8	V	
Output voltage	V _{OH}	2.7	—	—	V	V _{CC} = 4.75 V, V _{IL} = 0.8 V, I _{OH} = -400 μA
	V _{OL}	—	—	0.5	V	I _{OL} = 8 mA
		—	—	0.4		V _{CC} = 4.75 V, V _{IH} = 2 V I _{OL} = 4 mA
Input current	I _{IH}	—	—	20	μA	V _{CC} = 5.25 V, V _I = 2.7 V
	I _{IL}	—	—	-0.4	mA	V _{CC} = 5.25 V, V _I = 0.4 V
	I _I	—	—	0.1	mA	V _{CC} = 5.25 V, V _I = 7 V
Short-circuit output current	I _{OS}	-20	—	-100	mA	V _{CC} = 5.25 V
Supply current	I _{CCH}	—	0.8	1.6	mA	V _{CC} = 5.25 V
	I _{CCL}	—	1.0	2.0	mA	V _{CC} = 5.25 V
Input clamp voltage	V _{IR}	—	—	-1.5	V	V _{CC} = 4.75 V, I _{IN} = -18 mA

Note: * V_{CC} = 5 V, Ta = 25°C

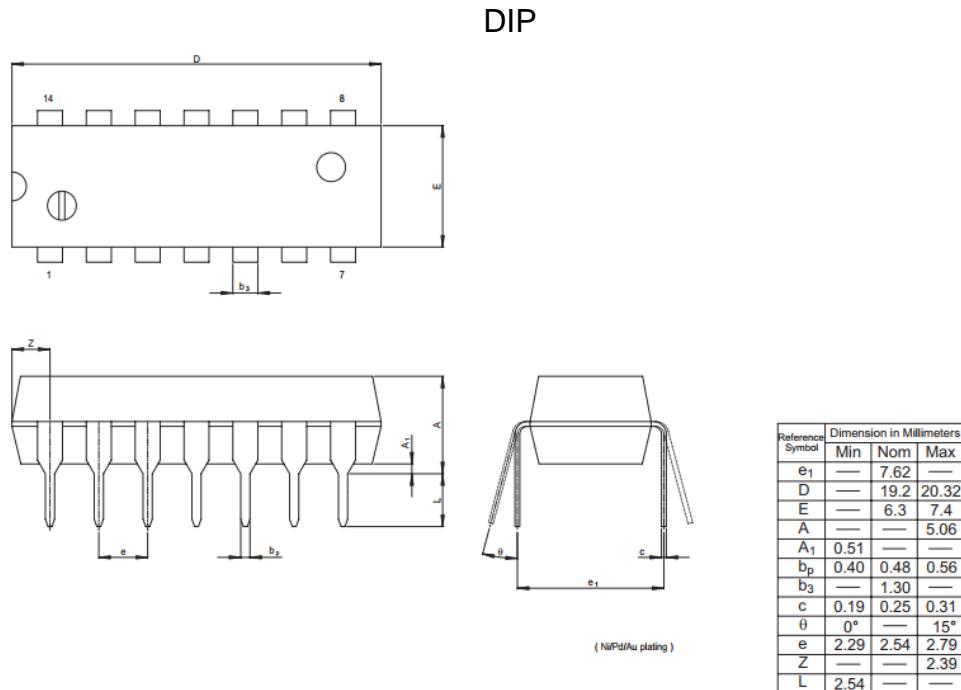
Switching Characteristics

(V_{CC} = 5 V, Ta = 25°C)

Item	Symbol	min.	typ.	max.	Unit	Condition
Propagation delay time	t _{PLH}	—	12	20	ns	C _L = 15 pF, R _L = 2 kΩ
	t _{PHL}	—	12.5	20	ns	

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

Package Dimensions



以上信息仅供参考. 如需帮助联系客服人员。谢谢 XINLUDA