

JLPI35B120RN2E7SN

LN2 PACK module with NCE Gen.7 Trench/Fieldstop IGBT and Emitter Controlled diode and NTC

RoHS

Features

- Electrical features
- VCES = 1200 V
- IC nom = 35 A / ICRM = 70 A
- Low V_{CEsat}
- Overload operation up to 175°C
- Mechanical features
- High power and thermal cycling capability
- Integrated NTC temperature sensor
- Copper base plate
- Al₂O₃ substrate with low thermal resistance
- Solder contact technology

LN2 Pack

MARKING DIAGRAM



Typical Applications

- · Auxiliary inverters
- Motor drives
- Servo drives

JINLAN = Company Name

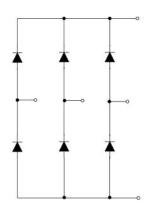
JLPI35B120RN2E7SN = Specific Device Code

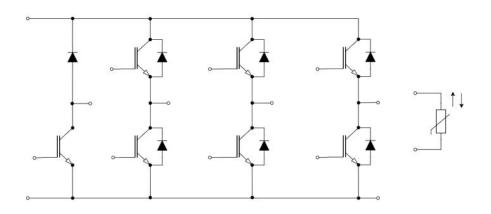
YYWW = Year and Work Week Code

XXXXX = Serial Number

QR code = Custom Assembly Information

Description







Package Insulation coordination

Parameter Symbol		Note or test condition	Values	Unit
Isolation test voltage	V _{ISOL}	RMS,f=50Hz,t=60s	2.5	kV
Internal isolation		basic insulation(class 1,IEC 61140)	Al ₂ O ₃	
Creepage distance	d _{creep}	terminal to heatsink	10.0	mm
Clearance	d _{clear}	terminal to heatsink	7.5	mm
Comparative tracking index (electrical)	СТІ		>200	
RTI Elec.	RTI	housing	140	$^{\circ}$

Package Characteristic values

			Values			
Parameter	Symbol	Note or test condition	Min.	Тур.	Max.	Unit
Stray Inductance	LCE			35		nH
Module Lead Resistance, Terminal to Chip	Raa'+cc'	T _C =25°C, per switch		6.9		mΩ
Module Lead Resistance, Terminal to Chip	R _{CC'+EE'}	T_{C} =25 $^{\circ}$ C, per switch		5.9		mΩ
Storage Temperature Range	T _{STG}		-40		125	$^{\circ}$
Mounting Torque, Screw M5	М	M5, Screw	3		6	N.m
Weight	G			180		g



IGBT, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit		
V _{CES}	Collector-Emitter Voltage	1200	V		
V _{GES}	Gate-Emitter Voltage ±30				
	Collector Current @ T _C =25 ℃	70	Α		
Ic	Collector Current @ Tc=80 ℃	35	Α		
Ісм	Pulsed Collector Current, t _p =1S	105	Α		

Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Cond	Min	Тур	Max	Unit	
			T _{vj} =25°C		1.50	2.10	
$V_{\text{CE}(\text{sat})}$	Collector-Emitter Saturation Voltage	I _C =35A, V _{GE} =15V	T _{vj} = 175 °C		1.75		V
$V_{\text{GE(th)}}$	Gate Threshold Voltage	I _C =1mA,V ₀	_{CE} =V _{GE}	5.5		7.0	V
I _{CES}	Collector-Emitter Leakage Current	V _{GE} =0V,V _{CE}	=1200V			10	uA
I_{GES}	Gate-Emitter Leakage Current	V _{GE} =30V,\	/ _{CE} =0V			100	nA
R _{Gint}	Internal Gate Resistance	f=1MI	-lz		0.6		Ω
Cies	Input Capacitance	V _{CE} =25V,V	/ _{GE} =0V	3500	4203	4900	рF
Coes	Out Capacitance		MHz		107	-	pF
Cres	Reverse Transfer				24	-	pF
$Q_{\rm G}$	Gate Charge	V _{CE} =960V, I _C =35A,V _{GE} =15V			147	1	nC
t _{d(on)}	Turn-On Delay Time				71		
t_{r}	Rise Time	V_{CE} =600V, I_{C} =35A, V_{GE} =0/15V, R_{g} =25 Ω , Inductive Load			53		-
$t_{\text{d(off)}} \\$	Turn-off Delay Time				261		ns
t _f	Fall Time				66.3		
E _{on}	Turn-On Switching Loss per Pulse				2.34		m I
E _{off}	Turn Off Switching Loss per Pulse				1.44		mJ
$t_{\text{d(on)}}$	Turn-On Delay Time				TBD		
t _r	Rise Time				TBD		ns
$t_{\text{d(off)}} \\$	Turn-off Delay Time	V _{CE} =600V,	I _C =35A,		TBD		113
t_{f}	Fall Time	V _{GE} =0/15V,l Inductive Load	$R_g=25\Omega$,		TBD		
E _{on}	Turn-on Switching Loss per Pulse	inductive Load	u, 1j-175 C		3.64		
E _{off}	Turn Off Switching Loss per Pulse				2.00		mJ
Isc	SC Data	V _{GE} =15V,V _{CC} ≤600V, t _{SC} ≤10us,T _J ≤150°C			175		А
R _{thJC}	Thermal resistance	Junction-to-Cas	e (per IGBT)		0.66		K/W
T _{vj op}		Temperature under sv	vitching conditions	-40		175 ¹⁾	$^{\circ}$

¹¹T_{vj op} > 150 °C is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.



Diode, Inverter

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage	1200	V
I _F	Diode Continuous Forward Current	35	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	105	Α

Characteristics (Tc=25°C unless otherwise noted)

	Diode Forward Voltage I _F =35A	T _j =25°C		T _j =25°C		1.80	2.70	
V _F		I _F =35A	T _j =175°C		1.75		V	
Qr	Recovered Charge	I _F =35A,R _g =25Ω, T _j =25°C			1.01		μC	
I _{RM}	Peak Reverse Recovery Current				37.7		Α	
Trr	Reverse Recovery Time				120		ns	
Erec	Reverse Recovery Energy				0.42		mJ	
Qr	Recovered Charge				TBD		μC	
I _{RM}	Peak Reverse Recovery Current	I _F =35A,I	I _F =35A,R _g =25Ω,		TBD		Α	
Trr	Reverse Recovery Time	T _j =1	75°C		TBD		ns	
E _{rec}	Reverse Recovery Energy				TBD		mJ	
R _{thJC}	Thermal resistance	Junction-to-Case (per diode)			1.03		K/W	
T _{vj op}		Temperature under switching conditions		-40		175 ²⁾	$^{\circ}$	

 $^{^{2)}}T_{vj \, op} > 150\,^{\circ}\text{C}$ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode, Rectifier

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V _{RRM}	Repetitive Peak Reverse Voltage	1600	V
I _F	Diode Continuous Forward Current	35	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	70	Α

 $\textbf{Characteristics} \quad (\texttt{Tc=25}\% \text{ unless otherwise noted})$

V _F	Diode Forward Voltage	I _F = 35 A, T _j = 150 °C		0.9		V
I _R	Reverse Current	T _j =175 °C, V _R =1600V		1		mA
R _{thJC}	Thermal resistance	Junction-to-Case (per diode)		0.65		K/W
T _{vj op}		Temperature under switching conditions	-40		175 ³⁾	$^{\circ}$

 $^{^{3)}}T_{vj\,op} > 150\,^{\circ}\text{C}$ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

IGBT, Chopper

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

ADSOIGLE I	MAXIII UIII Natiligs (1c=25 C unless otherwise noted)				
Symbol	Description	Value	Unit		
V _{CES}	Collector-Emitter Voltage	1200	V		
V _{GES}	Gate-Emitter Voltage ±30				
	Collector Current @ T _C =25 °C	70	Α		
Ic	Collector Current @ T _C =80 °C	35	Α		
I _{CM}	Pulsed Collector Current, t _p =1S	105	Α		

Aug, 2024-Rev.00 4 / 9 JLPI35B120RN2E7SN



Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Cond	ition	Min	Тур	Max	Unit
			T _{vj} =25°C		1.50	2.10	
$V_{\text{CE}(\text{sat})}$	Collector-Emitter Saturation Voltage	I _C =35A, V _{GE} =15V	T _{vj} = 175 °C		1.75		V
V _{GE(th)}	Gate Threshold Voltage	I _C =1mA,V _C	:E=V _{GE}	5.5		7.0	V
I _{CES}	Collector-Emitter Leakage Current	V _{GE} =0V,V _{CE}	=1200V			10	uA
I _{GES}	Gate-Emitter Leakage Current	V _{GE} =30V,V	_{CE} =0V			100	nA
R _{Gint}	Internal Gate Resistance	f=1MH	łz		0.6	-	Ω
C _{ies}	Input Capacitance	V _{CE} =25V,V	GE=0V	3500	4203	4900	nF
Coes	Out Capacitance	f=1N			107	-	nF
Cres	Reverse Transfer				24		nF
$Q_{\rm G}$	Gate Charge	V _{CE} =960V, I _C =35A,V _{GE} =15V			147	-	μC
t _{d(on)}	Turn-On Delay Time			71			
t _r	Rise Time	V_{CE} =600V, I_{C} =35A, V_{GE} =0/15V, R_{g} =25 Ω , Inductive Load			53		ne
$t_{\text{d(off)}} \\$	Turn−off Delay Time				261		ns
t _f	Fall Time				66.3		
Eon	Turn-On Switching Loss per Pulse				2.34		
E _{off}	Turn Off Switching Loss per Pulse				1.44		m
$t_{\text{d(on)}} \\$	Turn-On Delay Time				TBD		
t_{r}	Rise Time				TBD		ns
$t_{\text{d(off)}} \\$	Turn−off Delay Time	V _{CE} =600V,I	c=35A.		TBD		118
t _f	Fall Time	V _{GE} =0/15V,F	$R_g = 25\Omega$,		TBD		
E _{on}	Turn-on Switching Loss per Pulse	Inductive Load	ı, ı _j =175 C		3.64		
E _{off}	Turn Off Switching Loss per Pulse				2.00		m
Isc	SC Data	V _{GE} =15V,V _{CC} ≤600V, t _{SC} ≤10us,T _J ≤150°C			175		А
R _{thJC}	Thermal resistance	Junction-to-Cas	e (per IGBT)		0.66		K/V
T _{vj op}		Temperature under sw	vitching conditions	-40		175 ⁴⁾	°C
	I .			<u> </u>	l		l

 $^{^{4)}}$ T_{vj op} > 150 $^{\circ}$ C is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode, Chopper

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	1200	V
I _F	Diode Continuous Forward Current	15	Α
I _{FM}	Diode Maximum Forward Current t _p =1ms	30	Α

Characteristics (Tc=25℃ unless otherwise noted)

.,	V _F Diode Forward Voltage Q _r Recovered Charge	Diode Forward Voltage I _F =15A	T _j =25°C	1.50	2.3	.,
VF			T _j =175°C	1.45		V
Qr	Recovered Charge			0.43		μC
I _{RM}	Peak Reverse Recovery Current	I _F =15A,	R _q =25Ω,	16.3		Α
T _{rr}	Reverse Recovery Time	I _F =15A,R _g =25Ω, T _j =25°C		52		ns
Erec	Reverse Recovery Energy			0.18		mJ

Aug, 2024-Rev.00 5 / 9 JLPI35B120RN2E7SN



Qr	Recovered Charge			TBD		μC
I _{RM}	Peak Reverse Recovery Current	$I_F=15A,R_g=25\Omega,$		TBD		Α
T _{rr}	Reverse Recovery Time	T _j =175°C		TBD		ns
E _{rec}	Reverse Recovery Energy			TBD		mJ
R _{thJC}	Thermal resistance	Junction-to-Case (per diode)		0.97		K/W
T _{vj op}		Temperature under switching conditions	-40		175 ⁵⁾	$^{\circ}$

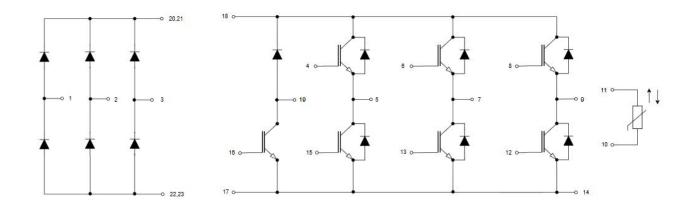
 $^{^{5)}}T_{Vjop} > 150^{\circ}C$ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

NTC Characteristics (Tc = 25°C unless otherwise noted)

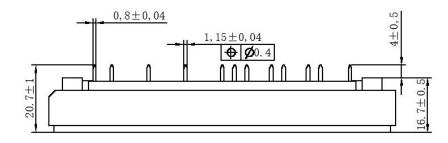
Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
R ₂₅	Rated Resistance			5.0		kΩ
ΔR/R	Deviation of R100	Tc=100 ℃,R100=493.3Ω	-5		5	%
P ₂₅	Power Dissipation				20.0	mW
B _{25/50}	B-value	R ₂ =R ₂₅ exp[B _{25/50} (1/T ₂ - 1/(298.15K))]		3375		К
B _{25/80}	B-value	R ₂ =R ₂₅ exp[B _{25/80} (1/T ₂ - 1/(298.15K))]		3411		K
B _{25/100}	B-value	R ₂ =R ₂₅ exp[B _{25/100} (1/T ₂ - 1/(298.15K))]		3433		К

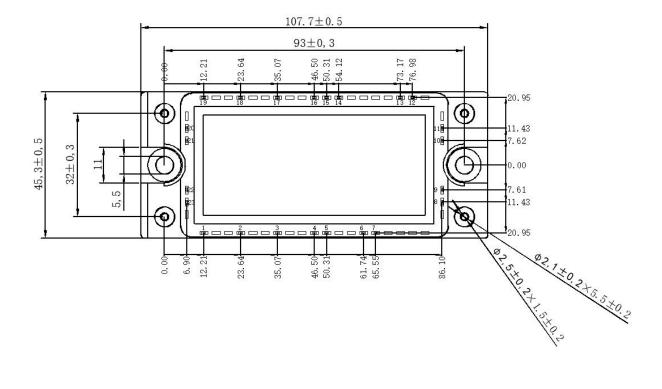


CIRCUIT DIAGRAM



PACKAGE DIMENSION







REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2024-08-23	Preview



ATTENTION

- Any and all Jinlan power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Jinlan Power Semiconductor representative nearest you before using any Jinlan power products described or contained herein in such applications.
- Jinlan Power Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Jinlan power modules described or contained herein.
- Specifications of any and all Jinlan power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- Jinlan Power Semiconductor (Wuxi).co.,LTD. strives to supply high-quality high-reliability products. However,any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all Jinlan power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Jinlan Power Semiconductor (Wuxi).co.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Jinlan Power Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Jinlan power product that you intend to use.
- This catalog provides information as of Aug.2024. specifications and information herein are subject to change without notice.