



PRODUCT DATA SHEET



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Datasheet

es Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

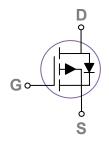


General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT -23	Pin	Configuration	





BVDSS	RDSON	ID
-20V	95m Ω	-2.0A

Features

- -20V,-2.0A, RDS(ON) =95mΩ@VGS =-4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Notebook
- Load Switch
- Hand-Held Instruments

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Parameter	Rating	Units
Drain-Source Voltage	-20	V
Gate-Source Voltage	±12	V
Drain Current – Continuous (T _C =25°C)	-2.0	Α
Drain Current – Continuous (T _C =100°C)	-1.6	Α
Drain Current – Pulsed ¹	-8.0	Α
Power Dissipation (T _C =25°C)	1.56	W
Power Dissipation – Derate above 25°C	0.012	W/°C
Storage Temperature Range	-55 to 150	°C
Operating Junction Temperature Range	-55 to 150	°C
	Drain-Source Voltage Gate-Source Voltage Drain Current – Continuous (T _C =25°C) Drain Current – Continuous (T _C =100°C) Drain Current – Pulsed¹ Power Dissipation (T _C =25°C) Power Dissipation – Derate above 25°C Storage Temperature Range	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		80	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	bol Parameter Conditions		Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage V _{GS} =0V , I _D =-250uA		-20		-	٧
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient Reference to 25°C , I _D =-1mA			-0.01		V/°C
I _{DSS}	Drain-Source Leakage Current	V_{DS} =-20V , V_{GS} =0V , T_J =25 $^{\circ}$ C			-1	uA
		V _{DS} =-16V , V _{GS} =0V , T _J =125°C			-10	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 12V$, $V_{DS}=0V$			±100	nA

On Characteristics

D		V _{GS} =-4.5V , I _D =-2.0A		95	125	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-2.5V , I _D =-1.0A		125	180	11122
$V_{GS(th)}$	Gate Threshold Voltage	\\ -\\ - 250\	-0.3	-0.7	-1.0	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=-250uA$		3		mV/°C

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{2, 3}		 3.0		
Qg	-		 3.0		
Q_gs	Gate-Source Charge ^{2,3}	V_{DS} =-10V , V_{GS} =-4.5V , I_{D} =-1A	 0.5		nC
Q_gd	Gate-Drain Charge ^{2, 3}		 0.8		
$T_{d(on)}$	Turn-On Delay Time ^{2 , 3}		 10		
Tr	Rise Time ^{2,3}	V_{DD} =-10V , V_{GS} =-4.5V , R_{G} =3 Ω	 5.5		nS
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}	I _D =-1A	 20		110
T_f	Fall Time ^{2, 3}		 6.5	-	
C _{iss}	Input Capacitance		 180	-	
Coss	Output Capacitance	V_{DS} =-10V , V_{GS} =0V , F=1MHz	 35		pF
C _{rss}	Reverse Transfer Capacitance		 25	-	

Drai

Symb	symbource Diode parageteristics and Maximum Patings				Max.	Unit
Is	Continuous Source Current	V =V =0V =5 Comment			-2.0	Α
I _{SM}	Pulsed Source Current	V _G =V _D =0V , Force Current			-4.0	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V

Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.



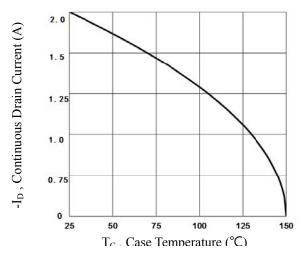


Fig.1 Continuous Drain Current vs. T_c

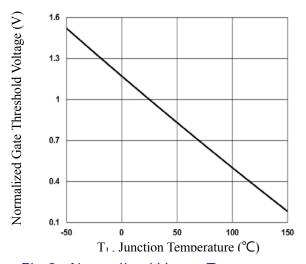


Fig.3 Normalized V_{th} vs. T_J

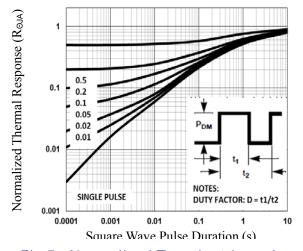


Fig.5 Normalized Transient Impedance

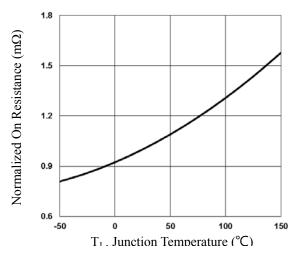


Fig. 2 Normalized RDSON vs. T_J

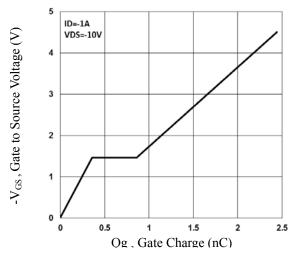


Fig.4 Gate Charge Waveform

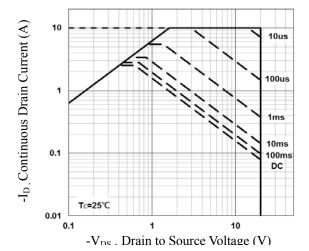
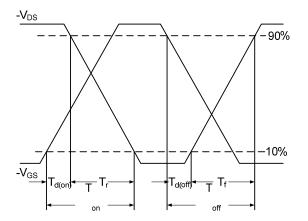


Fig.6 Maximum Safe Operation Area





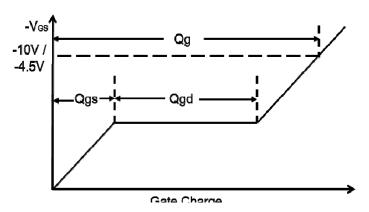
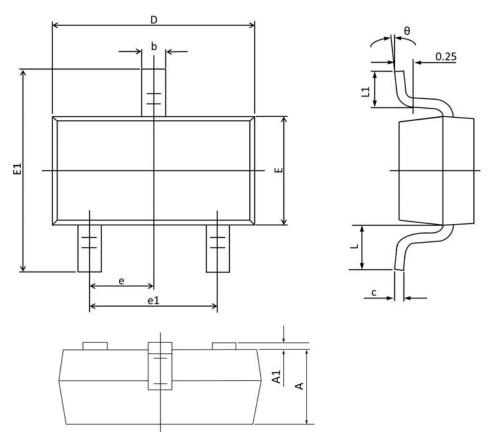


Fig.8 Gate Charge Waveform



SOT-23 PACKAGE INFORMATION



Cross b ol	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A1	0.000	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.090	0.110	0.003	0.004
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E 1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037	TYP.
e1	1.800	2.000	0.071	0.079
L	0.550	0 REF. 0.022 REF.		REF.
L1	0.300	0.500	0.012	0.020
θ	1°	7°	1°	7°



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