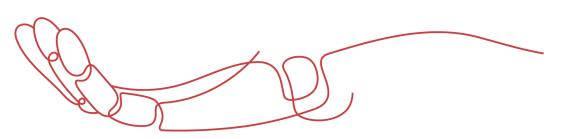




# **PRODUCT DATA SHEET**



To learn more about JGSEMI, please visit our website at







Datasheet

ources Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



# **General Description**

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-30V	15.5m $Ω$	-10A

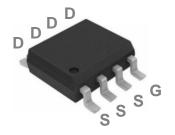
#### **Features**

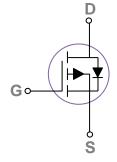
- -30V,-10A, RDS(ON) =15.5m $\Omega$ @VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

# **Applications**

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

# **SOP8 Pin Configuration**





## Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-30	V
V <sub>G</sub> s	Gate-Source Voltage	±20	V
I_	Drain Current – Continuous (T <sub>A</sub> =25°C)	-10	А
lD	Drain Current – Continuous (T <sub>A</sub> =70°C)	-8	А
DM	Drain Current – Pulsed <sup>1</sup>	-40	А
D-	Power Dissipation (T <sub>A</sub> =25°C)	2.5	W
$P_{D}$	Power Dissipation – Derate above 25°C	0.02	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		50	°C/W



# **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	SS Drain-Source Breakdown Voltage V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA		-30			٧
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =-1mA		-0.03		V/°C
IDSS	Drain-Source Leakage Current	V <sub>DS</sub> =-30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	J=25°C		-1	uA
	Diam-Source Leakage Current	V <sub>DS</sub> =-24V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			-10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm20V$ , $V_{DS}=0V$			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> Static Drain-Source On-Resistance	Static Drain-Source On-Pesistance	V <sub>GS</sub> =-10V , I <sub>D</sub> =-8A		12.4	15.5	mΩ
	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-6A		19.2	25	mΩ	
V <sub>GS(th)</sub>	Gate Threshold Voltage	)/ )/ I 050::A	-1.0	<b>-</b> 1.6	<b>-</b> 2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=-250uA$		4		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =-10V , I <sub>D</sub> =-8A		10.5		S

# **Dynamic and switching Characteristics**

Qg	Total Gate Charge <sup>2,3</sup>			14.6	21	
$Q_gs$	Gate-Source Charge <sup>2,3</sup>	V <sub>DS</sub> =-15V , V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-8A		4.1	6	nC
$Q_{gd}$	Gate-Drain Charge <sup>2,3</sup>			6.3	9	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2,3</sup>			9	17	
Tr	Rise Time <sup>2,3</sup>	$V_{DD}$ =-15V , $V_{GS}$ =-10V , $R_G$ =6 $\Omega$		21.8	41	ns
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>	I <sub>D</sub> =-1A		59.8	114	115
Tf	Fall Time <sup>2,3</sup>			14.4	27	
C <sub>iss</sub>	Input Capacitance			1730	2510	
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , F=1MHz		180	260	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			125	180	

## **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	mbol Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V . Force Current			-10	Α
I <sub>SM</sub>	Pulsed Source Current	VG=VD=UV, Force Current			-40	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25°C			-1	V

### Note:

- 1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed, pulse width  $\leq 300$ us, duty cycle  $\leq 2\%$ .
- 3. Essentially independent of operating temperature.

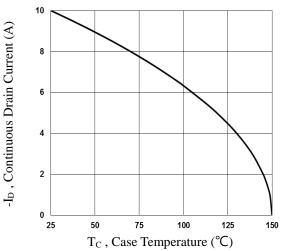


Fig.1 Continuous Drain Current vs. Tc

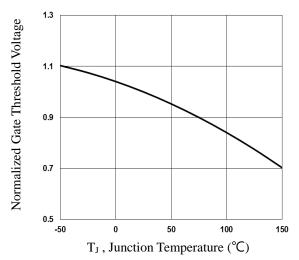


Fig.3 Normalized V<sub>th</sub> vs. T<sub>J</sub>

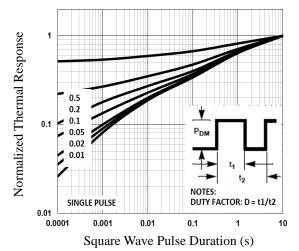


Fig.5 Normalized Transient Impedance

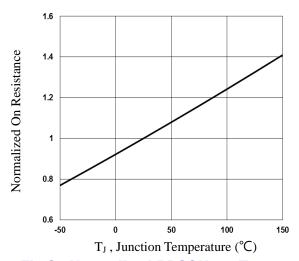


Fig.2 Normalized RDSON vs. T<sub>J</sub>

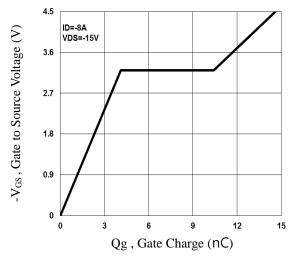


Fig.4 Gate Charge Waveform

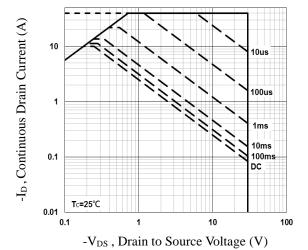
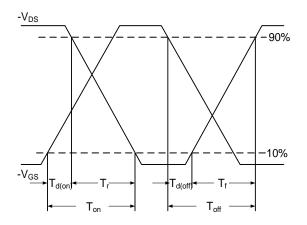


Fig.6 Maximum Safe Operation Area





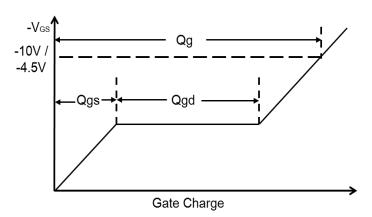
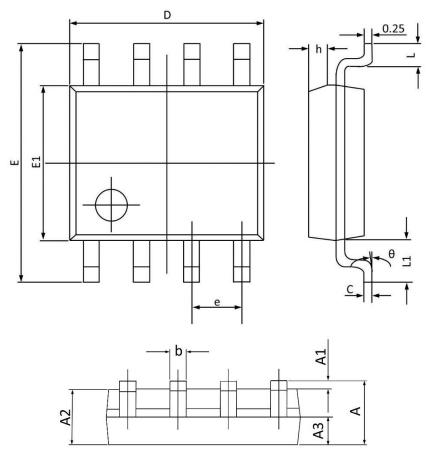


Fig.8 Gate Charge Waveform



# **SOP8 PACKAGE INFORMATION**



Symbol	Dimensions	In Millimeters	Dimension	ns In Inches
Symbol	Min	Max	Min	Max
Α	1.350	1.800	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.650	0.049	0.065
А3	0.500	0.700	0.020	0.028
b	0.300	0.510	0.012	0.020
С	0.150	0.260	0.006	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
е	1.270(BSC)		0.050	(BSC)
h	0.250	0.500	0.010	0.020
L	0.400	1.000	0.016	0.039
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°



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