

Power Splitter/Combiner

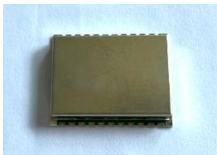
Features

- wide frequency band, 700-3000 MHz
- good coplanarity
- low unbalance
- shielded case
- aqueous washable

Applications

- cellular
- GPS
- PCS
- L Band

JY-SEPS-3-33+



3 Way-0° 50Ω 700 to 3000 MHz

Electrical Specifications at 25°C

Parameter	Frequency(MHz)	Min.	Typ.	Max.	Units
Frequency Range	-	700	-	3000	MHz
Insertion Loss (above theoretical 4.8 dB)	700-800	-	0.5	1.0	dB
	800-2200	-	0.9	1.5	
	2200-2700	-	1.1	1.6	
	2700-3000	-	1.2	1.7	
Isolation	700-800	14	17	-	dB
	800-2200	16	20	-	
	2200-2700	14	18	-	
	2700-3000	14	18	-	
Phase Unbalance	700-800	-	1.0	3.0	Degree
	800-2200	-	2.0	5.0	
	2200-2700	-	2.5	7.0	
	2700-3000	-	4.0	9.0	
Amplitude Unbalance	700-800	-	0.2	0.5	dB
	800-2200	-	0.2	0.5	
	2200-2700	-	0.1	0.5	
	2700-3000	-	0.3	0.8	
VSWR (Port S)	700-800	-	1.5	1.8	:1
	800-2200	-	1.2	1.8	
	2200-2700	-	1.6	2.0	
	2700-3000	-	1.5	2.0	
VSWR (Port 1-3)	700-800	-	1.3	1.5	:1
	800-2200	-	1.3	1.5	
	2200-2700	-	1.4	1.65	
	2700-3000	-	1.4	1.65	

Maximum Ratings

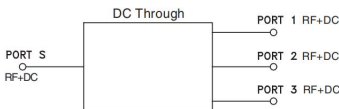
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C
Power Input (as a splitter)	4W max.
Internal Dissipation	0.5W max.
DC Current	2.25 A(750mA for each port)

Permanent damage may occur if any of these limits are exceeded.

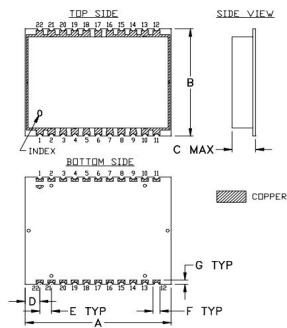
Pid Connections

SUM PORT	17
PORT 1	4
PORT 2	6
PORT 3	8
GROUND	all others

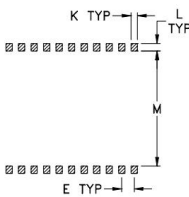
Electrical Schematic



Outline Drawing



PCB Land Pattern



Suggested Layout, Tolerance to be within ±0.5

Outline Dimensions: Unit (mm)

A	31.75	H	-
B	25.40	J	-
C	5.08	K	1.27
D	3.18	L	1.52
E	2.54	M	23.37
F	1.52	wt	4.4
G	1.02		

Suggested PCB Layout

