



Description

This range of devices is designed to protect xDSL line-driver interfaces from overvoltages up to rated limits. Overvoltages are normally caused by a.c. power-system or lightning-flash disturbances which are induced or conducted onto the telephone line. These symmetrical protectors are two-terminal thyristor-crowbar devices. They can be used to protect between conductors, or a pair of devices can be deployed to protect from line to ground. When placed between the xDSL line driver IC and the transformer, this protector will clamp and switch into a low-impedance state, safely diverting the energy transferred by the xDSL coupling transformer. The low capacitance design makes this device suitablefor designs from ADSL all the way up to 30 MHz VDSL2.

Telecom ports need protection against Common Mode (Longitudinal) and Differential (Metallic) surges, to comply with international standards such as ITU-T K.20, K.21 or K.45, Telcordia GR-1089-CORE and YD/T. Common Mode surges are resisted by the galvanic isolation of the coupling transformer which is commonly rated to 2 kV or greater. Differential surges can be transmitted by the transformer, and can stress the Line Driver Interface IC. As the xDSL interface circuit is designed to operate from 3 kHz to to30 MHz, nearby high frequency events such as cable flashover or primary protection activation can generate damaging conditions for the interface requiring this type of protection.

Main applications

Designed for ADSL, ADSL2, VDSL, VDSL2 protection

Features

- Ion-Implanted Breakdown Region- Precise and Stable Voltage Wide
- Low Voltage Overshoot Under Surge
- Low Off-State Capacitance
- Rated for International Surge Wave Shapes

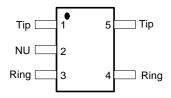
Wave Shape	Standard	IPPSM A
8/20	IEC 61000-4-5	30
10/1000	GR-1089-CORE	18

Production diagram

Bidirectional Thyristor Voltage Protector



SOT23-5 Package (Top View)



Device Symbol



Absolute Maximum Ratings, T_A= 25 °C (Unless Otherwise Noted)

Parameter		Symbol	Value	Unit
	P008A		±8	
Denetitive needs off state valtage	P012A	V_{DRM}	±12	V
Repetitive peak off-state voltage	P016A	- Bittii	±16	
	P024A		±24	
Non-repetitive peak impulse current (see Notes 1, 2, 3)				
	8/20 μs	I _{PPSM}	±30	Α
	10/100 μs		±18	
Junction temperature		TJ	-40-150	°C
Storage temperature range		T_{stg}	-65-150	°C

NOTES:

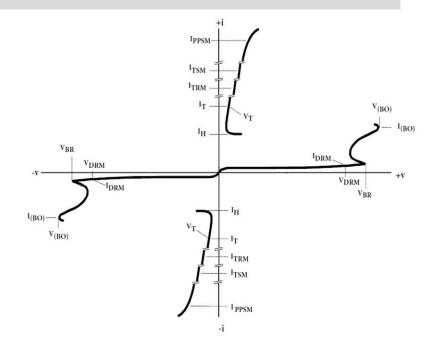
- 1. Initially the device must be in thermal equilibrium with $T_J = 25$ °C.
- The surge may be repeated after the device returns to its initial conditions.
- Rated currents only apply if pins 1 & 5 (Tip) are connected together and pins 3 & 4 (Ring) are connected together.





Parameter Measurement Information

Parameter	Symbol	
Holding current	I _H	
Non-repetitive peak impulse current	IPPSM	
Leakage current	I _{RM}	
Non repetitive peak on-state current	I _{TSM}	
Breakover current	I_{BO}	
Breakover voltage	V_{BO}	
Stand-off voltage	V_{RM}	
Breakdown voltage	V_{BR}	
Line-ground off-state capacitance	C ₀	



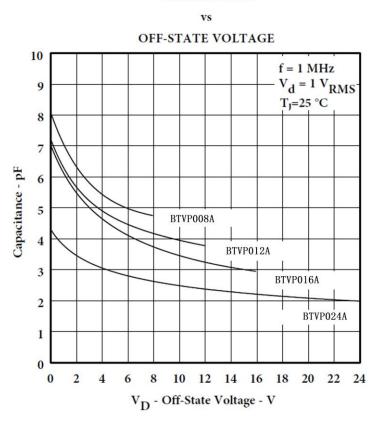
Electrical Characteristics, T_A= 25 °C (Unless Otherwise Noted)

Symbol		Test Conditions		Min.	Тур.	Max.	Unit
I _{DRM}	Repetitive peak off-state current	$V_{D} = V_{DRM}$				±1	μΑ
			P008A			±15	
l .,	5	$dv/dt = \pm 250 V/ms$, RSOURCE= 300Ω	P012A			±20	
V_{BO}	Breakover voltage	N300NCL- 30012	P016A			±25	V
			P024A			±35	
			P008A		±30		
١.		I_T =+5A ,di/dt = ±30mA/ms	P012A		±10		
I _H	I _H Holding current		P016A		±30		mA
		P024A		±30			
		f = 1 MHz,	P008A		6.5		
	O# state samesitanes	V_{d} = 1 V rms,	P012A		6		
Co	Off-state capacitance	$V_{D}= 1 \text{ V to } V_{DRM}$	P016A		5.5		pF
		vo— i v to v DKM	P024A		3.5		

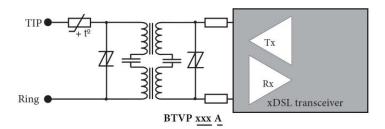


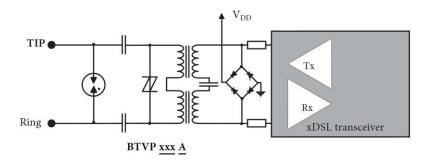
Typical Characteristics

CAPACITANCE



VDSL Application Examples



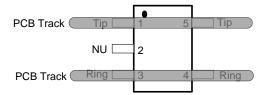


Bidirectional Thyristor Voltage Protector





Recommended PCB Layout



How To Order

Device	Package	Carrier	Marking Code	Standard Quantity	
BTVP <u>xxx</u> <u>A</u>	SOT23-5	Tape	P <u>xxx A</u>	3000pcs	