

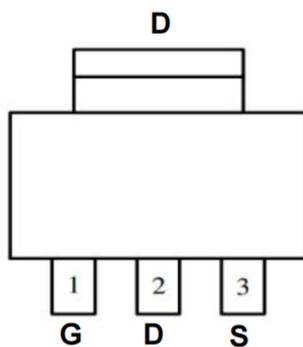
## Product Summary

- $V_{DS}$  250 V
- $I_{DS}$  0.5A
- $R_{DS(ON)}$  (at  $V_{GS}=10$ )  $\leq 2.6\Omega$  (Typ)
- Low Gate Charge Minimize Switching Loss

## Application

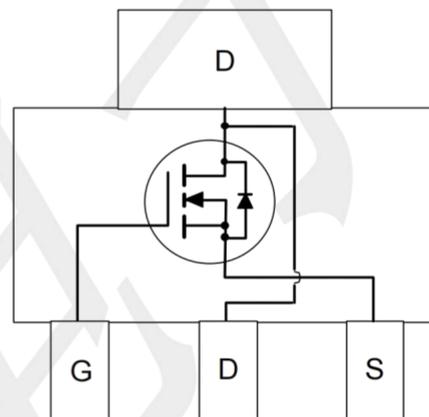
- Adaptor
- Charger
- Power management
- SMPS Standby Power

## Package and Pin Configuration



**SOT-223**

## Circuit diagram



## Absolute Maximum Ratings

( $T_A=25^\circ\text{C}$  unless otherwise noted)

PARAMETER	SYMBOL	Value	UNIT
Drain-Source Voltage	$V_{DS}$	250	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	0.5	A
Continuous Drain Current ( $T_C=70^\circ\text{C}$ )	$I_D$	0.4	A
Pulsed Drain Current (Note 1)	$I_{DM}$	1.5	A
Maximum Power Dissipation @ $T_A=25^\circ\text{C}$	$P_D$	2	W
Operating Junction Temperature Range	$T_J$	+150	$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-55 to +150	$^\circ\text{C}$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	63	$^\circ\text{C/W}$

Note : When mounted on 1" square PCB (FR4 material).

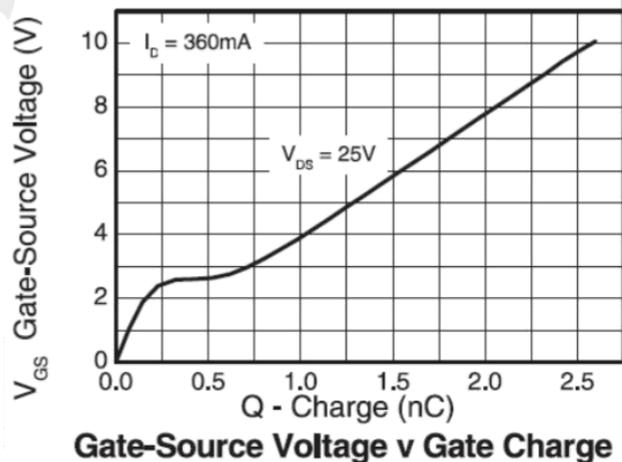
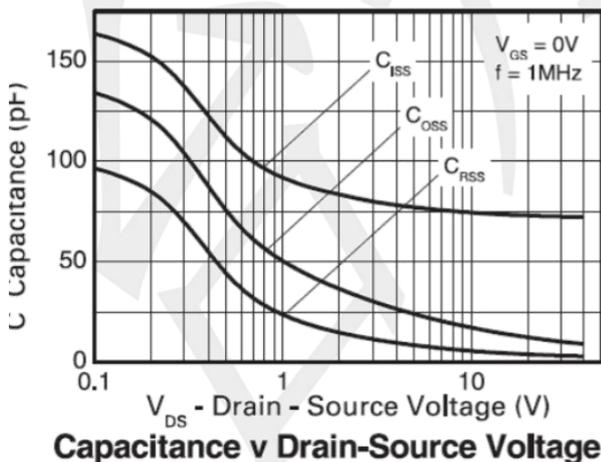
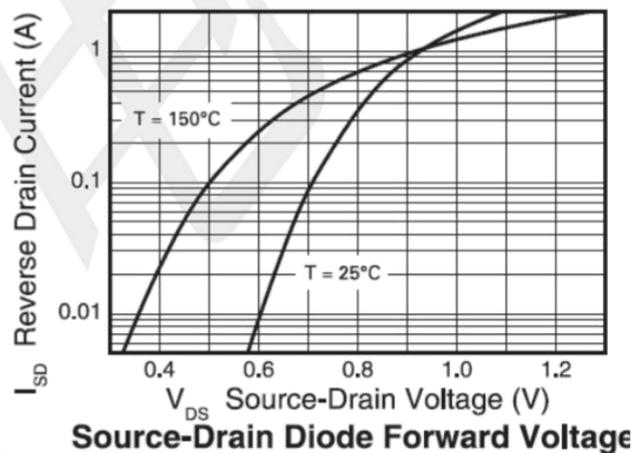
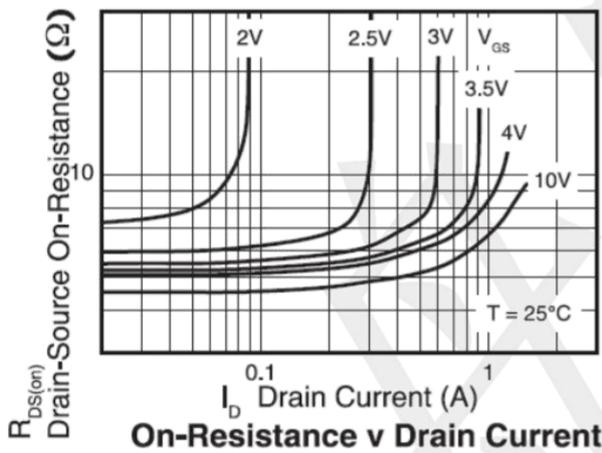
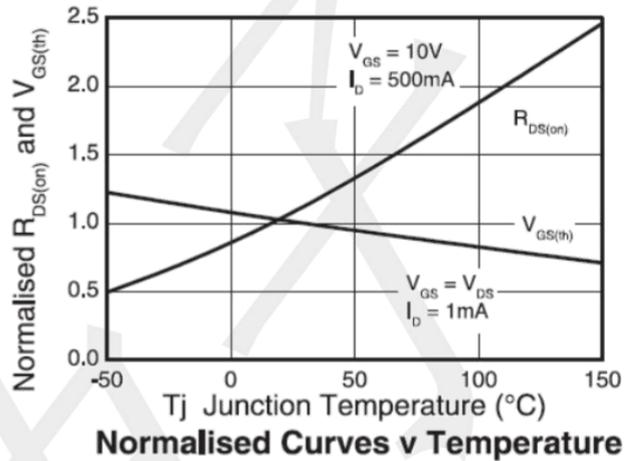
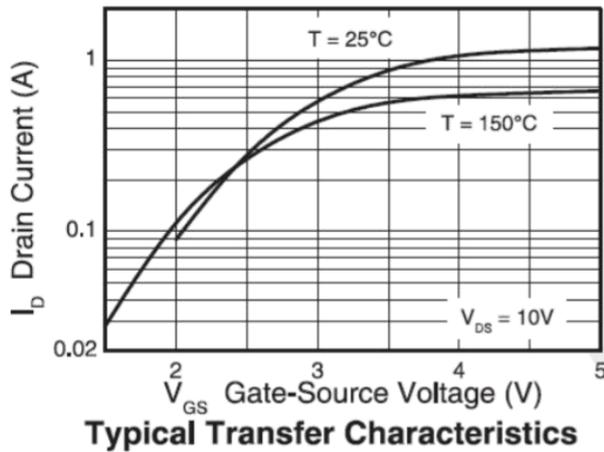
### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	BV <sub>DSS</sub>	240	250	--	V
Gate-Source Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	V <sub>GS(th)</sub>	1.0	1.3	3.0	V
Gate-Source Leakage	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	I <sub>GSS</sub>	--	--	±100	nA
Zero Gate Voltage Drain Current	V <sub>DS</sub> = 200V, V <sub>GS</sub> =0V	I <sub>DSS</sub>	--	--	0.6	μA
Drain-Source On-State Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 0.5A	R <sub>DS(on)</sub>	--	2.6	3.5	Ω
	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 0.5A		--	3.0	4.5	
Forward Trans conductance	V <sub>DS</sub> =10V, I <sub>D</sub> =0.5A	g <sub>FS</sub>	--	0.5	--	S
<b>Dynamic</b> (Note 2)						
Total Gate Charge (Note 3)	V <sub>DS</sub> = 25V, I <sub>D</sub> = 0.36A, V <sub>GS</sub> = 10V	Q <sub>g</sub>	--	2.6	--	nC
Gate-Source Charge (Note 3)		Q <sub>gs</sub>	--	0.2	--	
Gate-Drain Charge (Note 3)		Q <sub>gd</sub>	--	0.5	--	
Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, F = 1.0MHz	C <sub>iss</sub>	--	72	--	pF
Output Capacitance		C <sub>oss</sub>	--	11	--	
Reverse Transfer Capacitance		C <sub>rss</sub>	--	3.6	--	
<b>Switching</b>						
Turn-On Delay Time (Note 3)	V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.36A, V <sub>GS</sub> = 10V, R <sub>G</sub> = 25Ω	t <sub>d(on)</sub>	--	1.26	--	nS
Rise Time (Note 3)		t <sub>r</sub>	--	1.71	--	
Turn-Off Delay Time (Note 3)		t <sub>d(off)</sub>	--	11.4	--	
Fall Time (Note 3)		t <sub>f</sub>	--	3.52	--	
<b>Source-Drain Diode Ratings and Characteristics</b> (Note 2)						
Forward Voltage	V <sub>GS</sub> = 0V, I <sub>S</sub> = 0.5A	V <sub>SD</sub>	--	0.8	1.2	V
Continuous Source Current	Integral reverse diode in the MOSFET	I <sub>S</sub>	--	--	0.5	A
Pulsed Current (Note 1)		I <sub>SM</sub>	--	--	1.5	A

Notes:

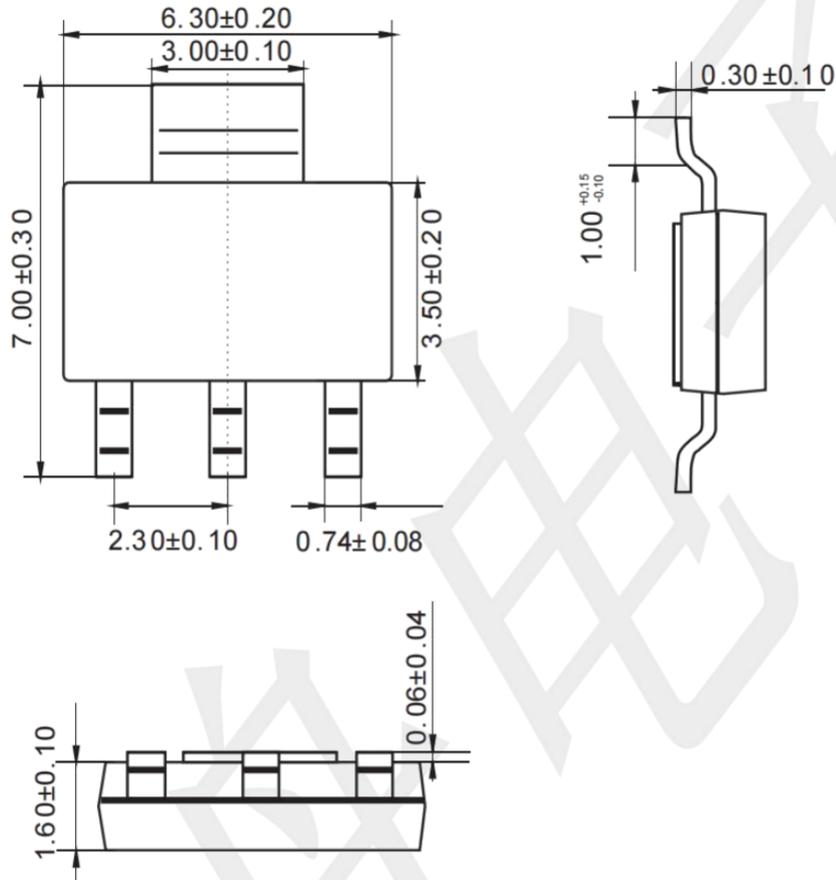
1. Pulse test; pulse width ≤ 300 μS, duty cycle ≤ 1%.
2. Guaranteed by design, not subject to production testing.
3. Independent of operating temperature

**TYPICAL CHARACTERISTICS** ( $T_A=25^\circ\text{C}$  unless otherwise noted)



**Package Outline Dimensions (unit: mm)**

SOT-223



**Mounting Pad Layout (unit: mm)**

