



PRODUCT DATA SHEET



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Datasheet Resources

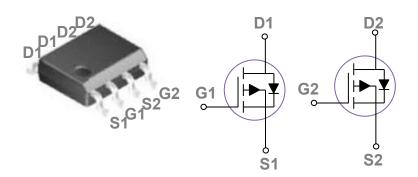
Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to Fairchild_questions@jgsemi.com.



General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOP8 Pin Configu	ration
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BVDSS	RDSON	ID
-30V	15m Ω	-8.5A

Features

- -30V, -8.5A, $RDS(ON) = 23m\Omega@VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-30	V
V _{GS}	Gate-Source Voltage (base on I _{GSS1} condition)	±20	V
	Drain Current – Continuous (T _C =25°C)	-8.5	А
lD	Drain Current – Continuous (T _C =100°C)	-4.43	А
I _{DM}	Drain Current – Pulsed1	-26	А
D	Power Dissipation (T _C =25°C)	2.1	W
P_D	Power Dissipation – Derate above 25°C	0.017	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
Тл	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		60	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions		Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-30			٧
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.03		V/°C
l	Drain Source Leekage Current	V _{DS} =-30V , V _{GS} =0V , T _J =25°C			-1	uA
IDSS	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =125°C			-10	uA
I _{GSS1}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$			±100	nA
I _{GSS2}	Gate-Source Leakage Current	V _{GS} =±25V , V _{DS} =0V			±1	mA

On Characteristics

D	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-5A		15	22	mΩ
R _{DS(ON)}	Static Dialii-Source On-Resistance	V _{GS} =-4.5V , I _D =-3A		21	33	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250uA		-1.5	-2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient			4		mV/°C
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-3A		6.8		S

Dynamic and switching Characteristics

Qg	Total Gate Charge ^{3,4}		 11	
Qgs	Gate-Source Charge ^{3, 4} V _{DS} =-15V , V _{GS} =-4.5V , I _D =-5A		 3.4	 nC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 4.2	
$T_{d(on)}$	Turn-On Delay Time ^{3,4}		 5.8	
Tr	Rise Time ^{3, 4}	V_{DD} =-15 V , V_{GS} =-10 V , R_{G} =6 Ω	 18.8	 no
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}	I _D =-1A	 46.9	 ns
T _f	Fall Time ^{3, 4}		 12.3	
Ciss	Input Capacitance		 1250	
Coss	Coss Output Capacitance V _{DS} =-15V , V _{GS} =0V , F=1MHz		 160	 pF
Crss	Reverse Transfer Capacitance		 90	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V =V =0V Force Current			-8.5	Α
I _{SM}	Pulsed Source Current	V _G =V _D =0V , Force Current			-17	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =35A., R_{G} =25 Ω , Starting T_{J} =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.



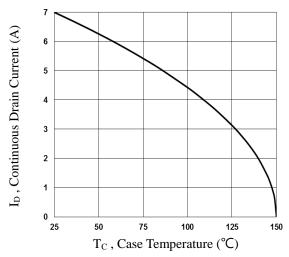


Fig.1 Continuous Drain Current vs. Tc

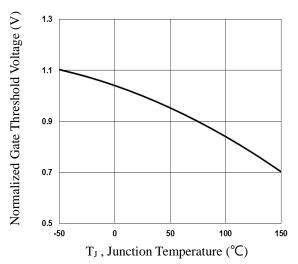


Fig.3 Normalized V_{th} vs. T_J

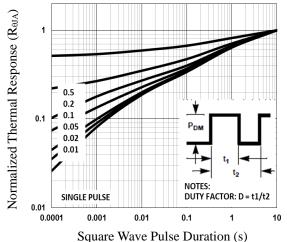


Fig.5 Normalized Transient Impedance

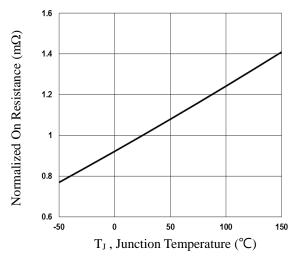


Fig.2 Normalized RDSON vs. T_J

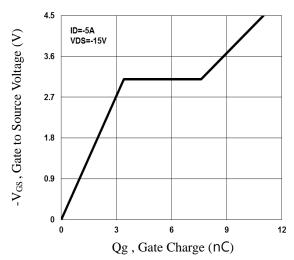


Fig.4 Gate Charge Waveform

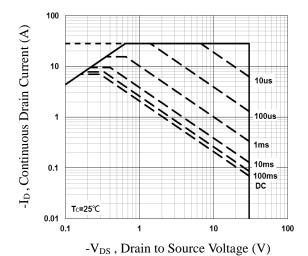
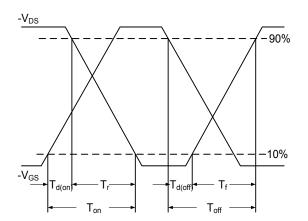


Fig.6 Maximum Safe Operation Area





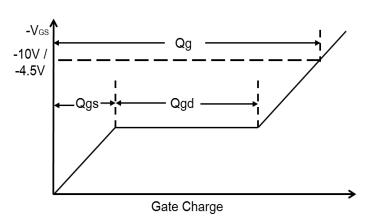
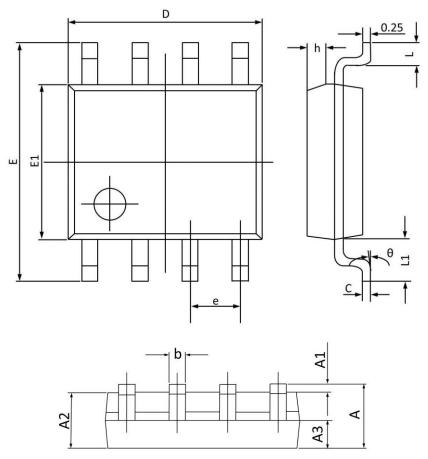


Fig.8 Gate Charge Waveform



SOP8 PACKAGE INFORMATION



Crumbal	Dimensions 1	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E 1	3.700	4.100	0.146	0.161
e	1.270	(BSC)	0.050	(BSC)
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050	(BSC)	0.041	(BSC)
θ	0°	8°	0 °	8°



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