

General Description

The TP2530CADJD16 is a 3A low dropout linear regulator designed for low dropout and high current applications. This device works with dual supplies, a control input for the control circuitry and a power input as low as 1.0V for providing current to output. It features 3A output current and ultra-low-drop output voltage as well as full protection functions. V_{OUT} can be as low as 0.8V.

The other features include soft start, under voltage protection, current limit protection, Power-On-Reset function, and over temperature protection. The is available in DFN3×3-10L package.

Features

- V_{IN} Range 1.0V to 6.0V
- V_{OUT} is Adjustable (0.8V Min)
- Excellent Line Regulation (0.01%/V typ.)
- Excellent Load Regulation (0.1%/A typ.)
- RoHS compliant, Pb-free package
- Dropout Voltage Typically 250 mV at $I_{OUT} = 3A$
- Internal Thermal Overload Protection
- Internal Short-Circuit Current Limit
- V_{OUT} Under Voltage Protection
- Ceramic Capacitor Stable

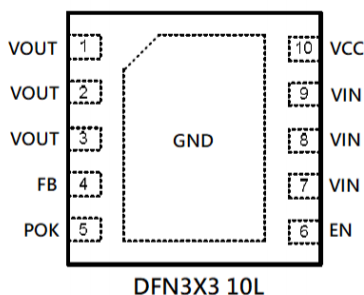
Applications

- Notebook, Netbook, Graphic Cards
- Low Voltage Logic Supplies
- Chipset Supplies
- Server System
- SMPS Post Regulators

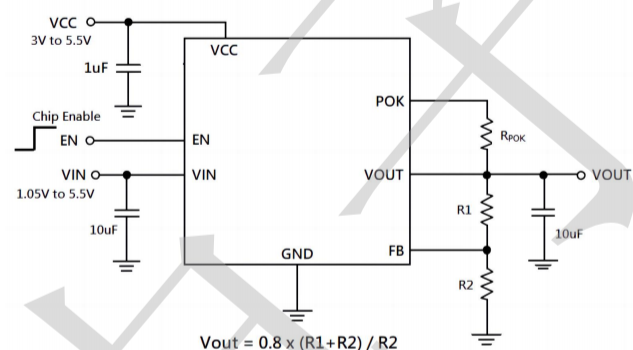
Ordering Information

Part Number	Package	Shipping
TP2530CADJD16	DFN3×3-10L (RoHS compliant and Pb-free package)	5000 pcs / Tape & Reel

Pin Configuration



Typical Application Circuit



Pin Assignment

Pin Name	Pin No.	Pin Function
POK	5	Power OK indication, open drain output.
FB	4	Feedback
VOUT	1,2,3	Output Voltage pin, the Source of power device.
VIN	7,8,9	Input Voltage pin, the Drain of power device.
EN	6	Enable pin. Internal pull high to VCC
VCC	10	Supply input of control circuit.
GND	11(Exposed PAD)	Ground
NC	--	Non connect

Absolute Maximum Ratings

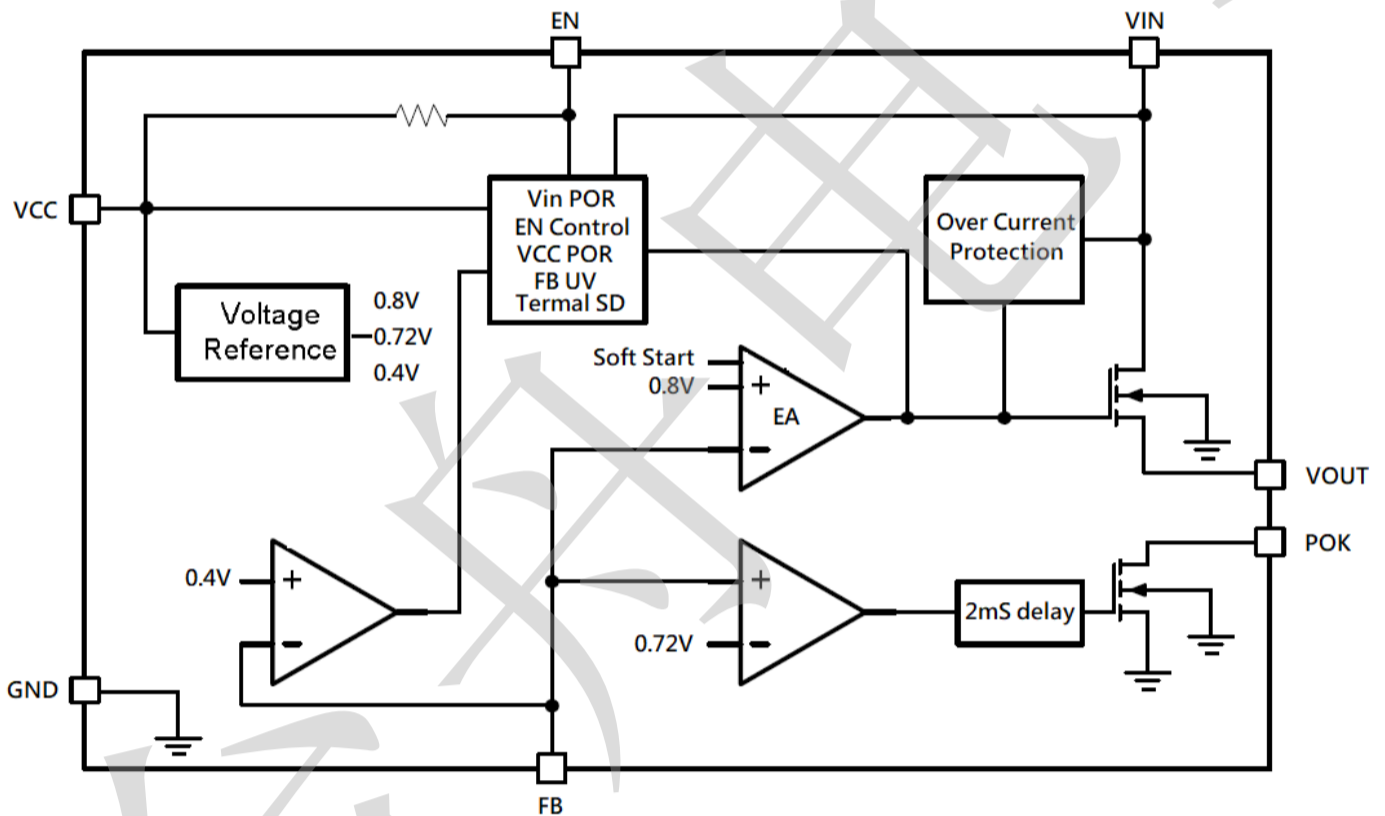
Parameter	Symbol	Ratings	Units
Input Voltage	VIN	-0.3~+6.0	V
Supply Voltage	VCC	-0.3~+6.0	V
Other pins	-	-0.3~VCC+0.3	V
Thermal Resistance, Junction to Ambient	RθJA	60 (Note)	°C/W
Continuous Total Power Dissipation @ TA=25°C	PD	1.67	W
Lead Temperature(Soldering, 10 sec)	TS	300	°C
Storage Temperature Range	Tstg	-65~+150	°C

Note : RθJA is measured in the natural convection at TA=25°C on a 4-layers high effective thermal conductivity test board with minimum copper area of JEDEC 51-7 thermal measurement standard.

Recommended Operating Condition

Parameter	Symbol	Ratings	Units
Input Voltage	V_{IN}	+1.05~ V_{CC}	V
Supply Voltage	V_{CC}	+3.0~+5.5	V
Junction Temperature Range	T_j	-40~+125	°C

Function Block Diagram



Electrical Characteristics

@ $T_J=25^{\circ}\text{C}$, $V_{CC}=5\text{V}$, unless otherwise specified

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Control Input Voltage VCC	VCC	$V_{OUT}=V_{REF}$	3.0	--	6.0	V
VCC POR Threshold	VCCPOR		2.2	--	2.9	V
VCC POR Hysteresis	VCCHY		--	0.4	--	V
Power Input Voltage VIN	VIN	$V_{OUT}=V_{REF}$	1.05	--	VCC	V
VIN POR Threshold	VINPOR		0.8	--	1.0	V
VIN POR Hysteresis	VINHY		--	0.35	--	V
VIN POR Deglitch Time	TDEG		--	100	--	μs
Control Input Current in Shutdown	IVCCSD	$V_{IN}=V_{CC}=5\text{V}$, $V_{EN}=0\text{V}$	--	10	30	μA
Quiescent Current	IQ	$V_{IN}=V_{CC}=V_{EN}=5\text{V}$, $I_{OUT}=0\text{A}$	--	0.9	1.5	mA
Reference Voltage	VREF	$V_{IN}=V_{CC}=V_{EN}=5\text{V}$, $I_{OUT}=0\text{A}$, $V_{OUT}=V_{REF}$	0.785	0.8	0.815	V
VIN Line Regulation	VREFLINE	$1.05\text{V}<V_{IN}<5\text{V}$, $V_{CC}=V_{EN}=5\text{V}$	--	0.01	0.1	%/V
Load Regulation	VREFLOAD	$0\text{A}<I_{OUT}<3\text{A}$, $V_{CC}=V_{EN}=5\text{V}$	--	0.1	0.5	%/A
Dropout Voltage	VDROP	$I_{OUT}=3\text{A}$, $V_{CC}=5\text{V}$, $V_{OUT}=1.2\text{V}$	--	250	360	mV
VOUT Pull Low Resistance	RPULL	$V_{CC}=5\text{V}$, $V_{EN}=0\text{V}$, Sink $=5\text{mA}$	--	--	150	ohm
Enable High Level	VEN		1.1	--	--	V
Disable Low Level	VSD		--	--	0.3	V
Enable Source Current	IEN	$V_{CC}=5\text{V}$, $V_{EN}=0\text{V}$	--	5	10	μA
Enable pull high resistor	REN		500K	--	--	ohm
Output Voltage Ramp Up Time	TSS		0.6	1	2	mS
POK Threshold	VPOKH	VFB Rising		92		%
	VPOKL	VFB falling		82		%
POK Sink Voltage	VPOK	Sinking Current = 5mA	--	--	0.4	V
POK Delay Time	TPOKDE	From $V_{OUT}>92\%$ to POK rising	1	2	4	mS
OCP Threshold Level	IOCP		3.2	4.5	--	A
Under Voltage Threshold	VUVP	VFB Falling	--	0.15	--	V
Thermal Shutdown Temperature	TSD		--	165	--	$^{\circ}\text{C}$
Thermal Shutdown Hysteresis	TSDHY		--	30	--	$^{\circ}\text{C}$

Typical Characteristics

Fig 1. Start Up from EN

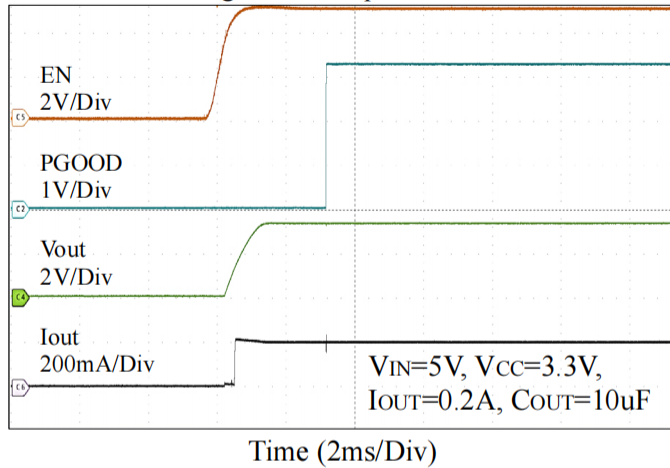


Fig 2. Start Up from VIN

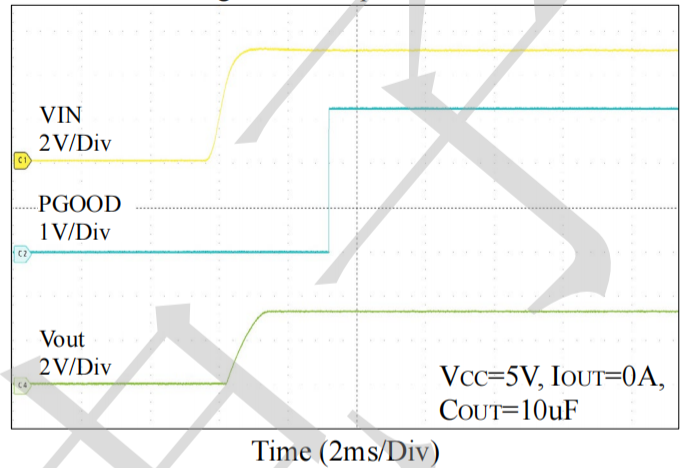


Fig 3. Start Up from VCC

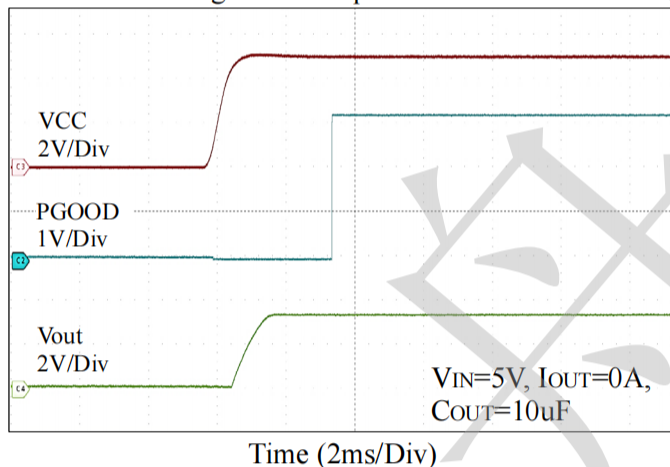


Fig 4. Load Transient

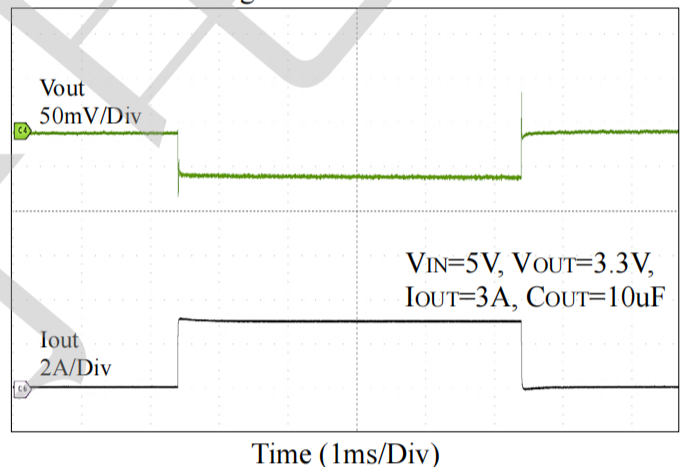


Fig 5. Vdrop vs Load Current

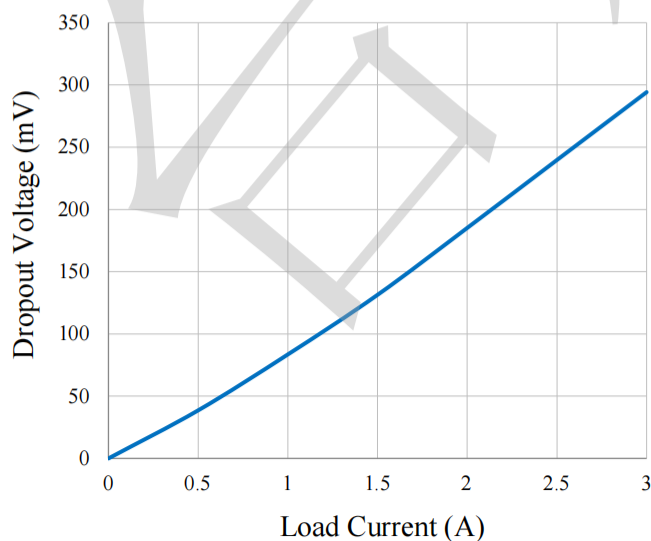
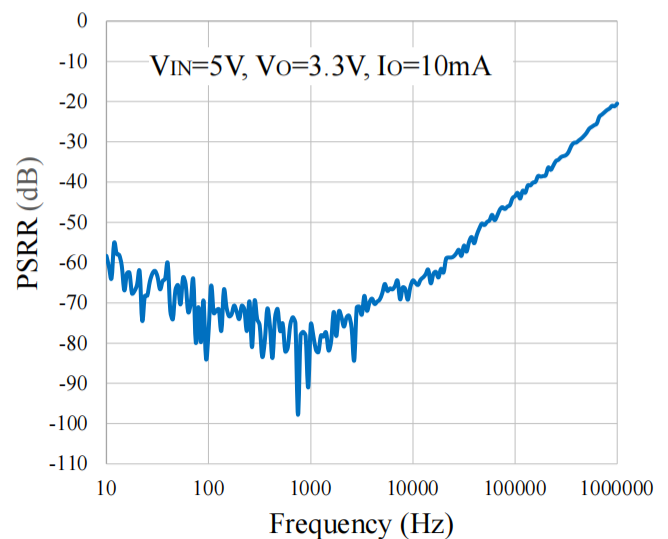


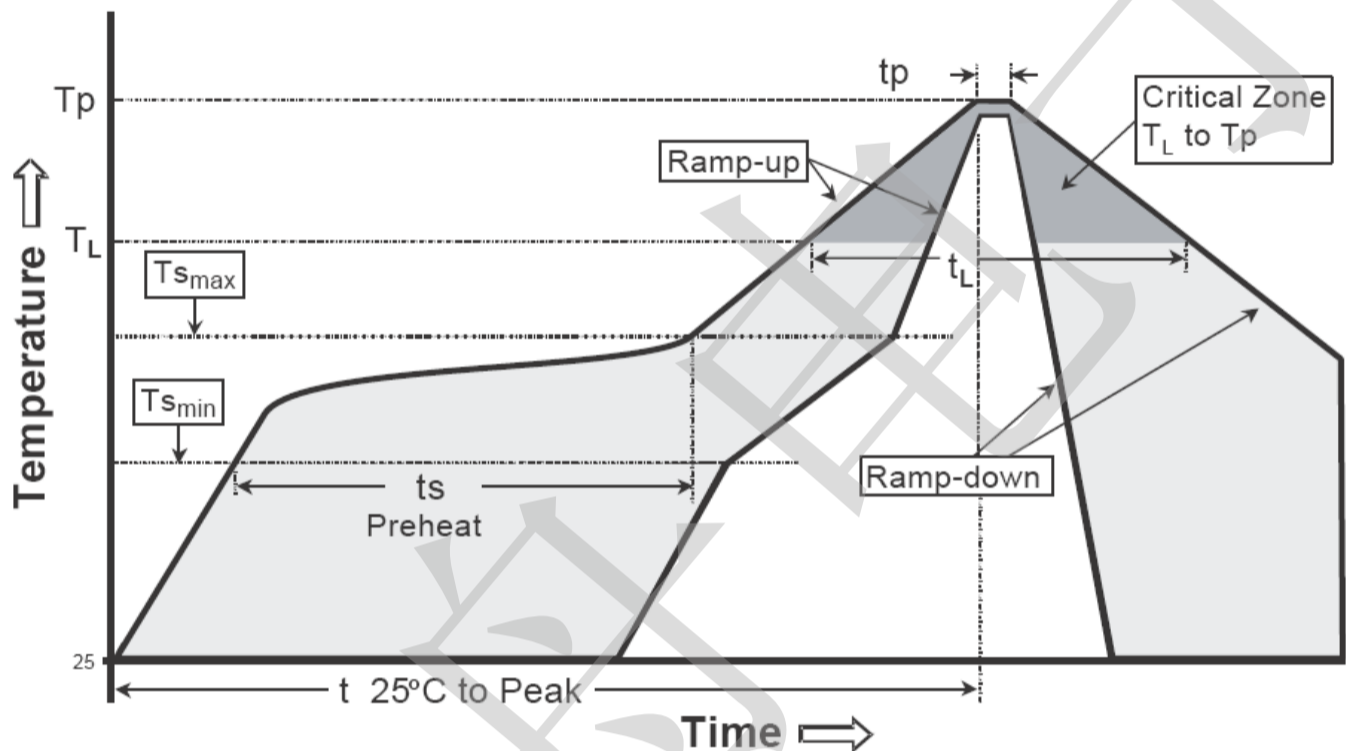
Fig 6. PSRR



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

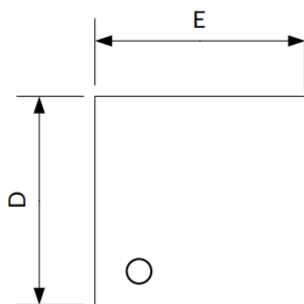
Recommended temperature profile for IR reflow



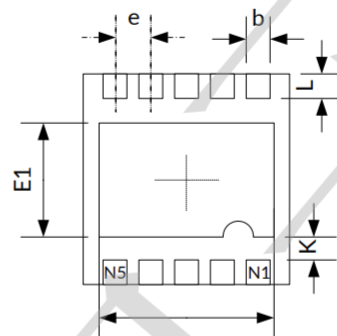
Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _S min)	100°C	150°C
-Temperature Max(T _S max)	150°C	200°C
-Time(t _s min to t _s max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

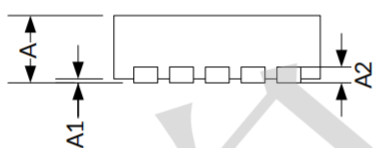
DFN3X3-10



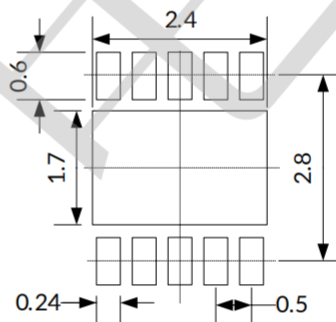
TOP VIEW



BOTTOM VIEW



SIDE VIEW



RECOMMENDED LAND PATTERN (Unit: mm)

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.203		0.008	
b	0.180	0.300	0.007	0.012
D	2.900	3.100	0.114	0.122
D1	2.300	2.600	0.091	0.103
E	2.900	3.100	0.114	0.122
E1	1.500	1.800	0.059	0.071
e	0.500 TYP		0.020 TYP	
k	0.200 MIN		0.008 MIN	
L	0.300	0.500	0.012	0.020