

Features

- 1.5 A maximum peak output current
- 0.8 A minimum peak output current
- Rail-to-rail output voltage
- 110 ns maximum propagation delay
- Under Voltage Lock-Out protection (UVLO) with hysteresis
- Wide operating range: 10 to 30 Volts (V_{CC})
- Guaranteed performance over temperature -40°C ~ +110°C.
- Regulatory Approvals
 - UL - UL1577
 - VDE - EN60747-5-5(VDE0884-5)
 - CQC - GB4943.1

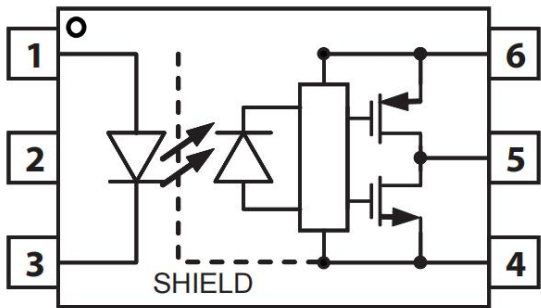
Applications

- Isolated IGBT/Power MOSFET gate drive
- Industrial Inverter
- AC brushless and DC motor drives
- Induction Heating

Description

The TLP5701 series Photocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control inverter applications and inverters in power supply system. It contains an AlGaAs LED optically coupled to an integrated circuit with a power output stage.

The Photocoupler operational parameters are guaranteed over the temperature range from -40°C ~ +110°C.



ORDERING INFORMATION

Outline	Part Number	Package	Marking	Packing	Packing Size	Quantity
	TLP5701P-V	LSOP6	5701X /YYWW A	Reel	13 "	1500
	TLP5701W-V	LSOP6		Reel	13 "	1500

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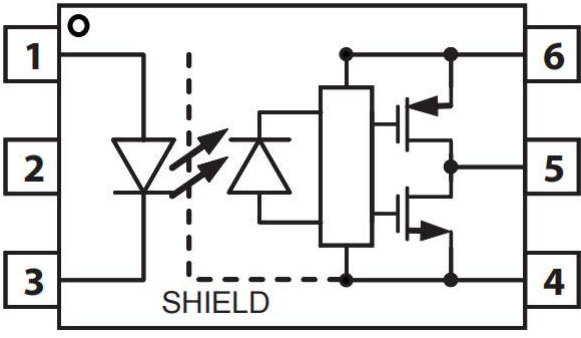
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PIN CONFIGURATION AND FUNCTIONS

	Pin	Name
	1	Anode
	2	NC
	3	Cathode
	4	V _{ss}
	5	V _o
	6	V _{cc}

TRUTH TABLE			
LED	V _{cc} -V _{ss} (Turn-ON, +ve going)	V _{cc} -V _{ss} (Turn-OFF, -ve going)	VO
Off	0V to 30V	0V to 30V	Low
On	0V to 6.9V	0V to 5.9V	Low
On	6.9V to 8.7V	5.9V to 7.5V	Transition
On	8.7V to 30V	7.5V to 30V	High

Note: A 0.1μF bypass capacitor must be connected between Pin 4 and 6.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit	Note
Storage Temperature	T_{stg}	-55	125	°C	-
Operating Temperature	T_{opr}	-40	110	°C	-
Output IC Junction Temperature	T_J	-	125	°C	-
Total Output Supply Voltage	$(V_{CC} - V_{SS})$	0	35	V	-
Average Forward Input Current	I_F	-	20	mA	-
Reverse Input Voltage	V_R	-	5	V	-
“High” Peak Output Current	$I_{OH(PEAK)}$	0.8	1.5	A	1
“Low” Peak Output Current	$I_{OL(PEAK)}$	0.8	1.5	A	1
Output Voltage	$V_{O(PEAK)}$	-0.5	V_{CC}	V	-
Power Dissipation	P_I	-	45	mW	-
Output IC Power Dissipation	P_O	-	250	mW	-
Lead Solder Temperature	T_{sol}	-	260	°C	-

Note: Ambient temperature = 25°C, unless otherwise specified. Stresses exceeding the absolute maximum ratings can cause permanent damage to the device. Exposure to absolute maximum ratings for long periods of time can adversely affect reliability.

Note 1: Exponential waveform. Pulse width $\leq 10 \mu s$, $f \leq 15 \text{ kHz}$

RECOMMENDED OPERATION CONDITIONS

Parameter	Symbol	Min.	Max.	Unit
Operating Temperature	T_a	-40	110	°C
Supply Voltage	V_{CC}	10	30	V
Input Current (ON)	$I_{F(ON)}$	7	16	mA
Input Voltage (OFF)	$V_{F(OFF)}$	-3.0	0.8	V

ELECTRICAL OPTICAL CHARACTERISTICS

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition	Note
INPUT CHARACTERISTICS							
Forward Voltage	V_F	-	1.38	1.8	V	$I_F = 10\text{ mA}$	-
Reverse Current	I_R	-	-	10	μA	$V_R = 5\text{V}$	-
Input Threshold Current (Low to High)	I_{FLH}	-	0.6	2	mA	$V_O > 5\text{V}, I_O = 0\text{A}$	-
Input Threshold Voltage (High to Low)	V_{FHL}	0.8	-	-	V	$V_{CC} = 30\text{ V}, V_O < 5\text{V}$	-
Input Capacitance	C_{IN}	-	60	-	pF	$V_F = 0, f = 1\text{MHz}$	-
OUTPUT CHARACTERISTICS							
High Level Supply Current	I_{CCH}	-	1.50	3	mA	$I_F = 10\text{ mA}, V_{CC} = 30\text{ V},$ $V_O = \text{Open}, R_g = 30\Omega,$ $C_g = 3\text{ nF}$	
Low Level Supply Current	I_{CCL}	-	1.50	3	mA	$I_F = 0\text{ mA}, V_{CC} = 30\text{ V},$ $V_O = \text{Open}, R_g = 30\Omega,$ $C_g = 3\text{ nF}$	
High Level Output Voltage	V_{OH}	29.4	29.69	-	V	$I_F = 10\text{ mA}, I_O = -100\text{ mA}$	2,3
Low Level Output Voltage	V_{OL}	-	0.17	0.34	V	$I_F = 0\text{ mA}, I_O = 100\text{ mA}$	
High Level Output Current	I_{OH}	-0.8	-	-	A	$I_F = 10\text{ mA}, V_{CC} = 30\text{V}$ $V_O = V_{CC} - 4$	1
Low Level Output Current	I_{OL}	0.8	-	-	A	$I_F = 0\text{ mA}, V_{CC} = 30\text{V}$ $V_O = V_{SS} + 4$	1
Under Voltage Lockout Threshold	V_{UVLO+}	6.9	7.8	8.7	V	$V_O > 5\text{V}, I_F = 10\text{ mA}$	
	V_{UVLO-}	5.9	6.9	7.5	V	$V_O < 5\text{V}, I_F = 10\text{ mA}$	

All Typical values at $T_a = 25^\circ\text{C}$ and $V_{CC} - V_{SS} = 30\text{V}$, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Maximum pulse width = 10 μs .

Note 2: In this test V_{OH} is measured with a dc load current. When driving capacitive loads, V_{OH} will approach V_{CC} as I_{OH} approaches zero amps.

Note 3: Maximum pulse width = 1ms.

SWITCHING SPECIFICATION

Parameter	Symbol	Min	Typ	Max	Unit	Test Condition	Note
SWITCHING CHARACTERISTICS							
Propagation Delay Time to Output Low Level	t_{PHL}	-	54	500	ns	$R_g = 30\Omega$, $C_g = 3\text{ nF}$, $f = 10\text{ kHz}$, Duty Cycle = 50% $I_F = 10\text{ mA}$, $V_{CC} = 30\text{ V}$	-
Propagation Delay Time to Output High Level	t_{PLH}	-	69	500	ns		-
Pulse Width Distortion	PWD	-	22	200	ns		-
Propagation Delay Difference Between Any Two Parts	PDD ($t_{PHL} - t_{PLH}$)	-200	-	+200	ns		-
Rise Time	t_r	-	13	-	ns		-
Fall Time	t_f	-	13	-	ns		-
Common Mode Transient Immunity at Logic High	CM_H	-20	-	-	kV/ μs	$I_F = 7\text{ to }16\text{ mA}$ $V_{CC} = 30\text{ V}$, $T_A = 25\text{ }^\circ\text{C}$, $V_{CM} = 1\text{ kV}$	1,2
Common Mode Transient Immunity at Logic Low	CM_L	20	-	-	kV/ μs	$I_F = 0\text{ mA}$ $V_{CC} = 30\text{ V}$, $T_A = 25\text{ }^\circ\text{C}$, $V_{CM} = 1\text{ kV}$	1,3

All Typical values at $T_A = 25^\circ\text{C}$ and $V_{CC} - V_{SS} = 30\text{ V}$, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Pin 2 needs to be connected to LED common.

Note 2: Common mode transient immunity in the high state is the maximum tolerable dV_{CM}/dt of the common mode pulse, V_{CM} , to assure that the output will remain in the high state (meaning $V_O > 10.0\text{ V}$).

Note 3: Common mode transient immunity in a low state is the maximum tolerable dV_{CM}/dt of the common mode pulse, V_{CM} , to assure that the output will remain in a low state (meaning $V_O < 1.0\text{ V}$).

ISOLATION CHARACTERISTIC

Parameter	Symbo	Min.	Typ.	Max.	Unit	Test Condition	Note
Withstand Insulation Test Voltage	V _{ISO}	5000	-	-	V	40% ≤ RH ≤ 60%, t = 1min, T _a = 25 °C	1,2
Input-Output Resistance	R _{I-O}	-	10 ¹²	-	Ω	V _{I-O} = 500V DC	1

All Typical values at T_a = 25°C and V_{CC} – V_{SS} = 30 V, unless otherwise specified; all minimum and maximum specifications are at recommended operating condition.

Note 1: Device is considered a two terminal device: pins 1, 2, 3 are shorted together and pins 4, 5, 6 are shorted together.

Note 2: According to UL1577, each photocoupler is tested by applying an insulation test voltage 6000VRMS for one second. This test is performed before the 100% production test for partial discharge.

TYPICAL PERFORMANCE CURVES

Fig.1 High output rail voltage vs. Temperature

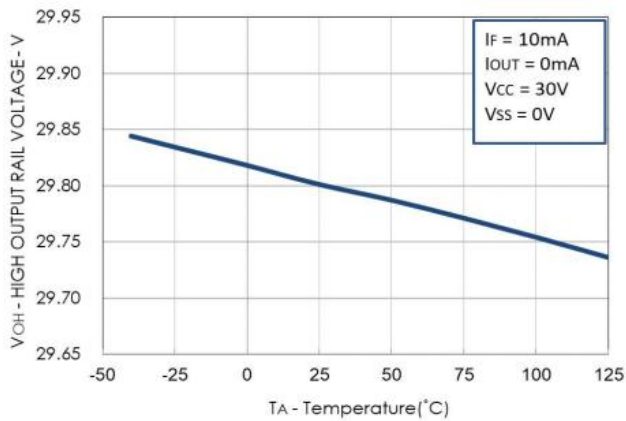


Fig.2 V_{OH} vs. Temperature

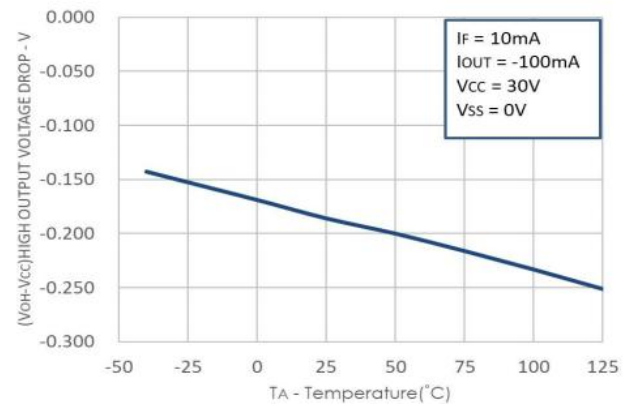


Fig.3 V_{OL} vs. Temperature

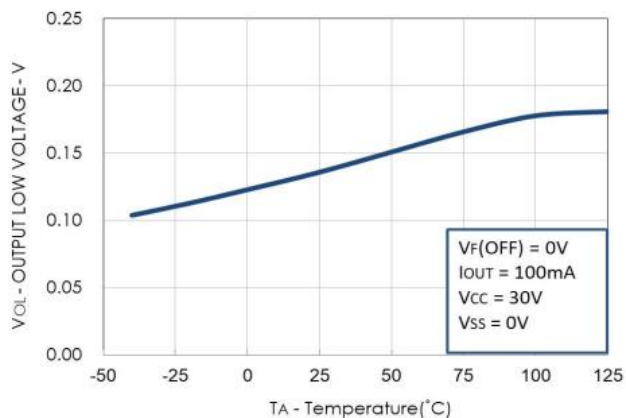


Fig.4 I_{CC} vs. Temperature

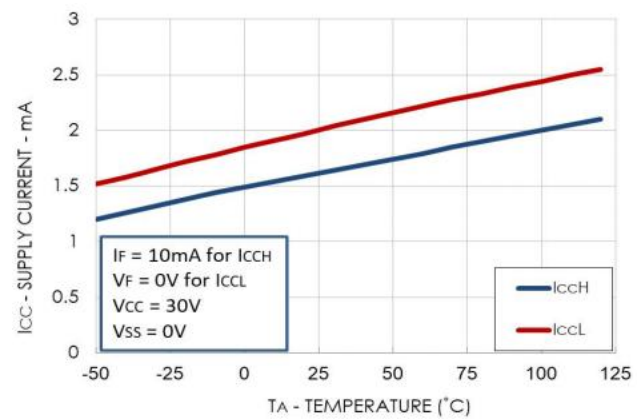


Fig.5 I_{CC} vs. V_{CC}

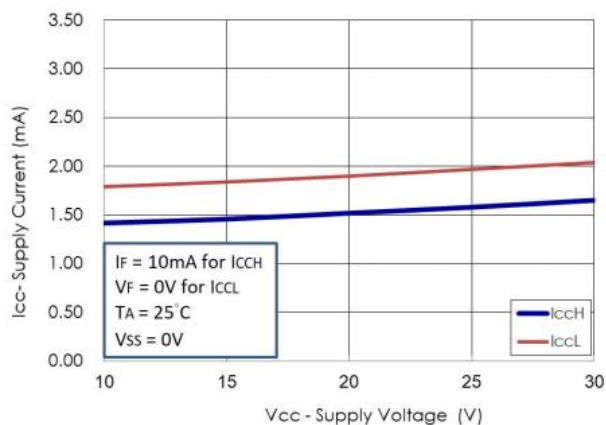


Fig.6 I_{FLH} vs. Hysteresis

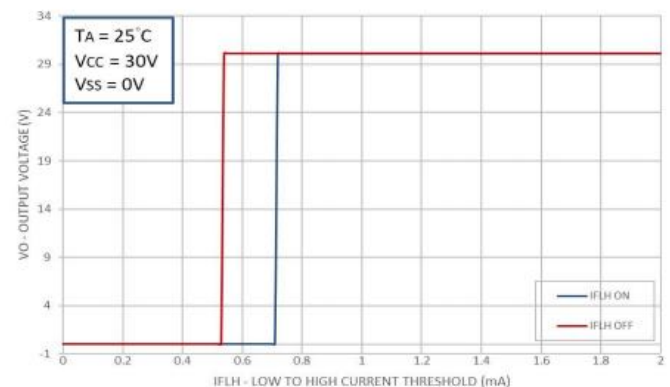


Fig.7 I_{FH} vs. Temperature

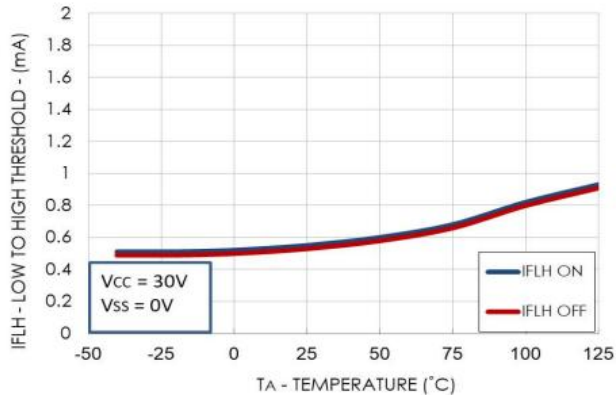


Fig.8 Propagation Delays vs. V_{CC}

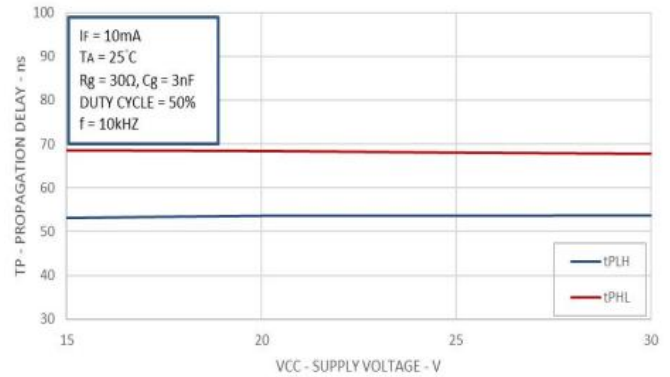


Fig.9 Propagation Delays vs. I_F

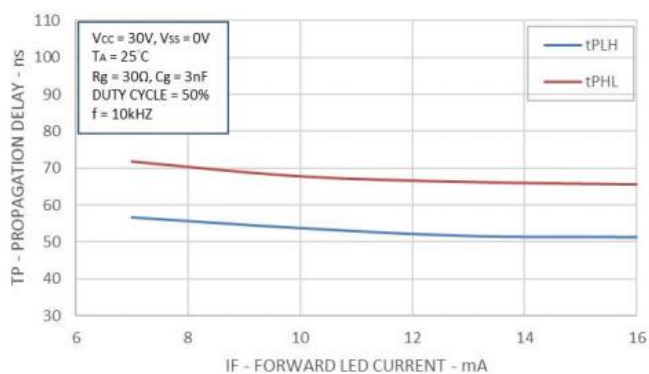


Fig.10 Propagation Delays vs. Temperature

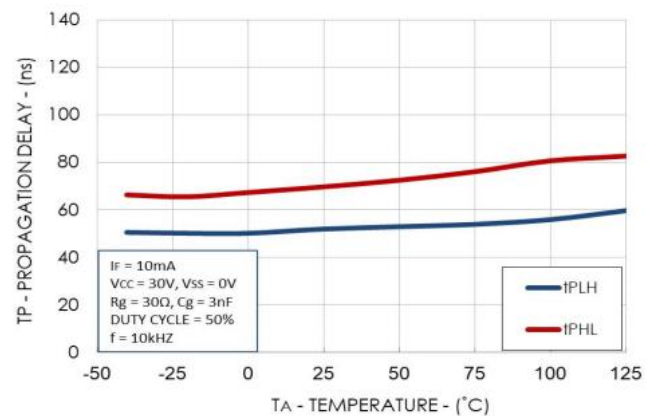


Fig.11 Propagation Delays vs. R_g

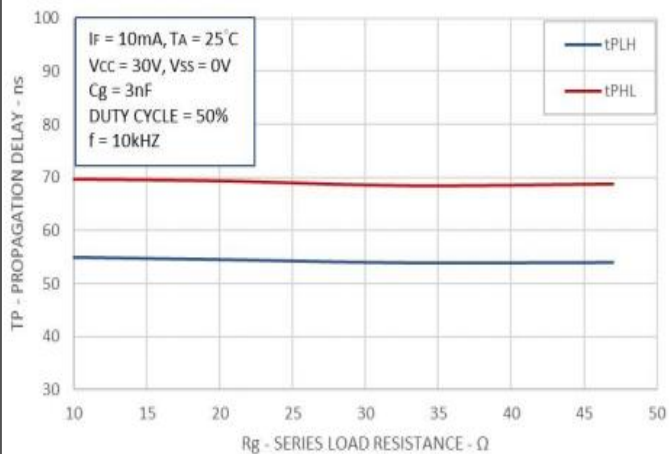


Fig.12 Propagation Delays vs. C_g

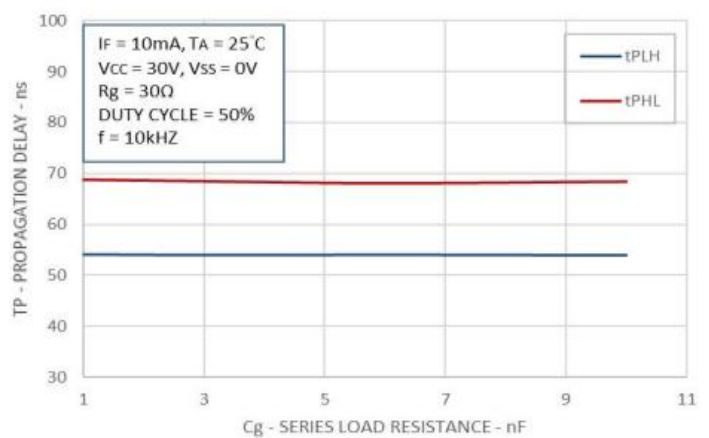
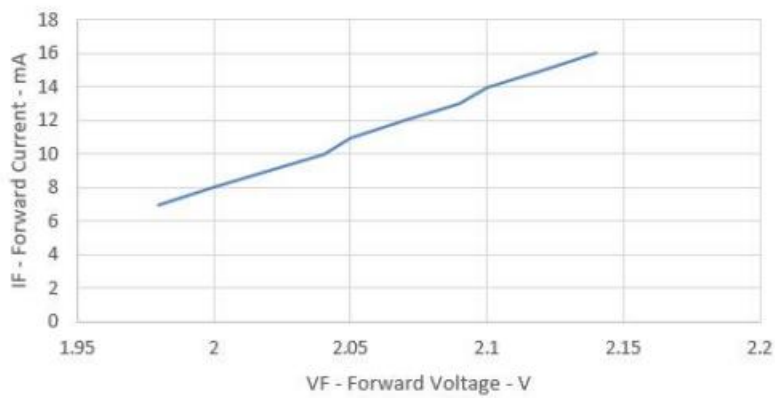


Fig.13 Input Current vs. Forward Voltage



TEST CIRCUITS

Fig.14 I_{OH} Test Circuit

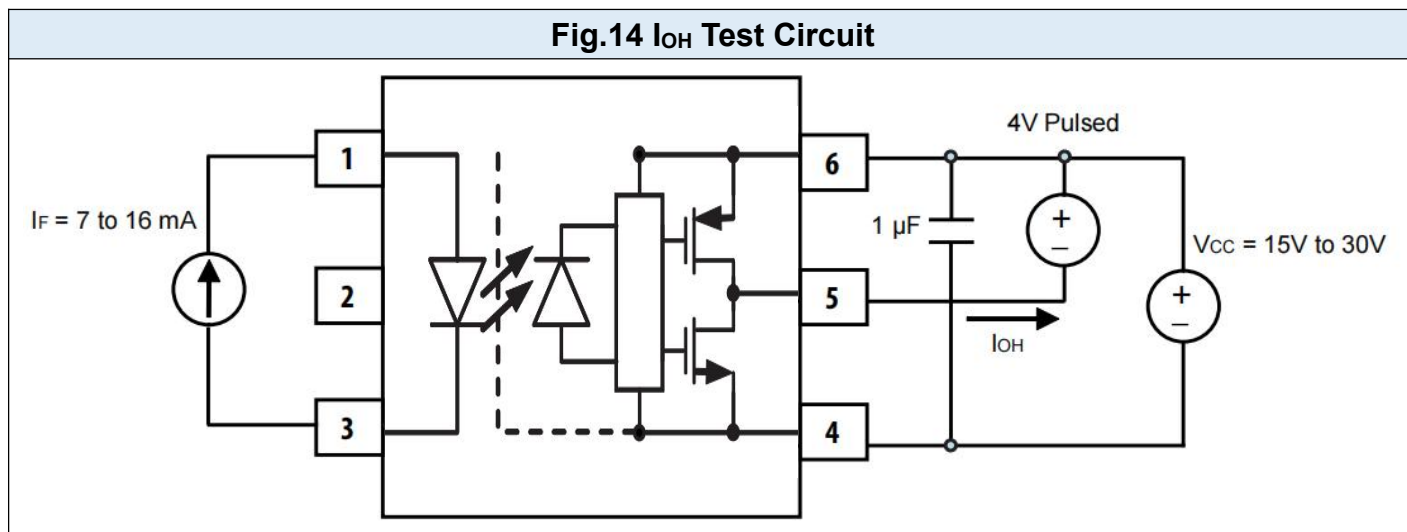


Fig.15 I_{OL} Test Circuit

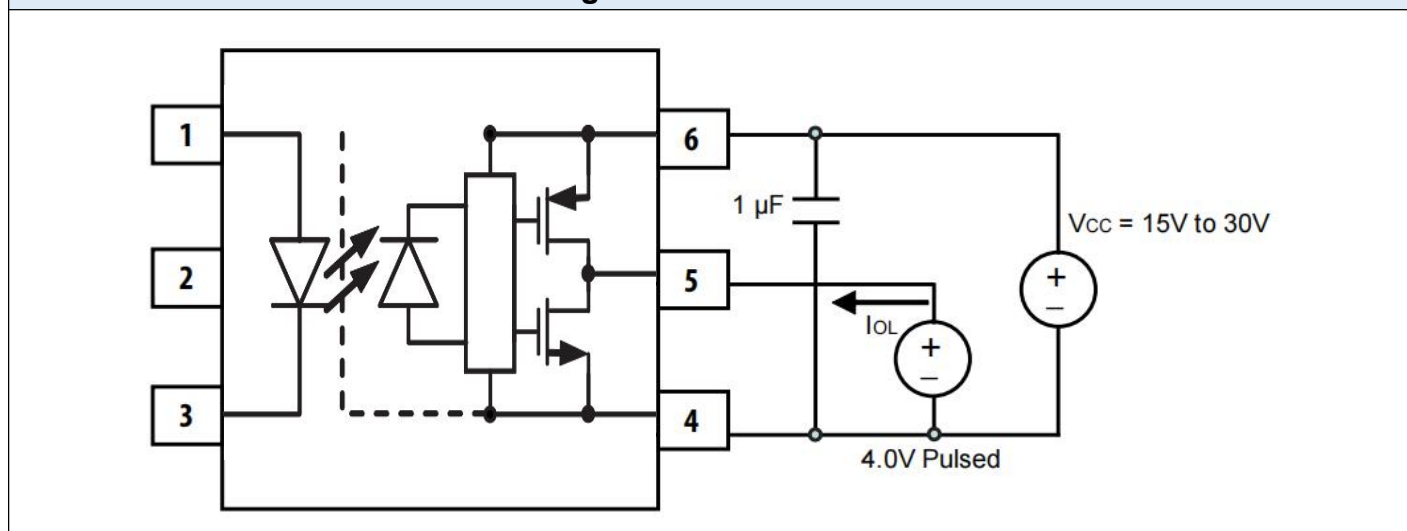


Fig.16 V_{OH} Test Circuit

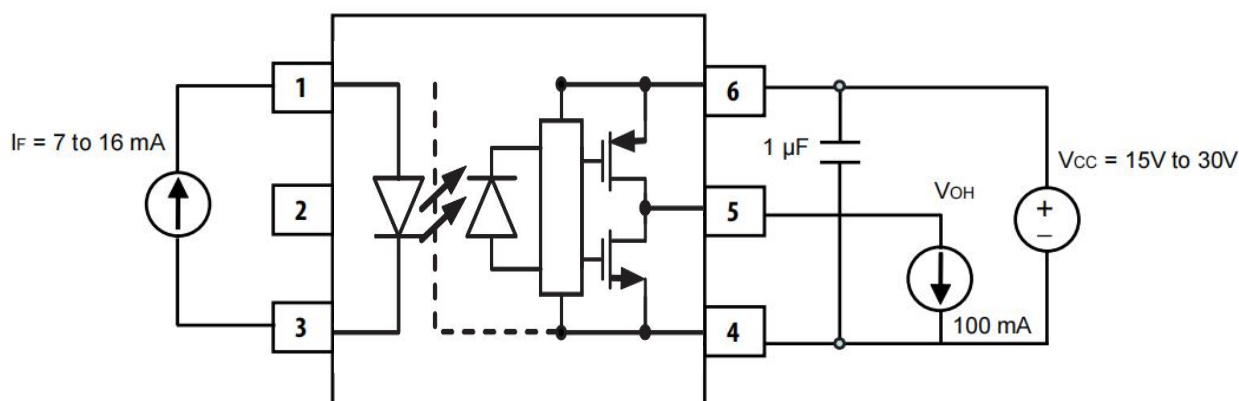


Fig.17 V_{OL} Test Circuit

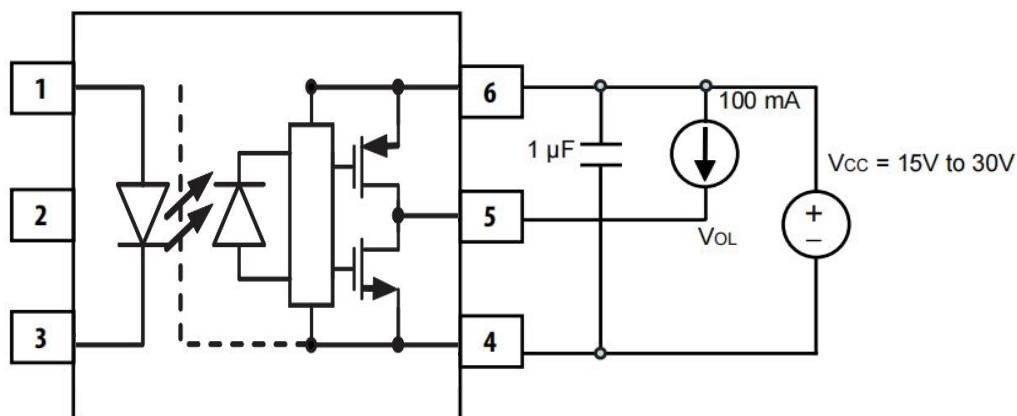


Fig.18 I_{FLH} Test Circuit

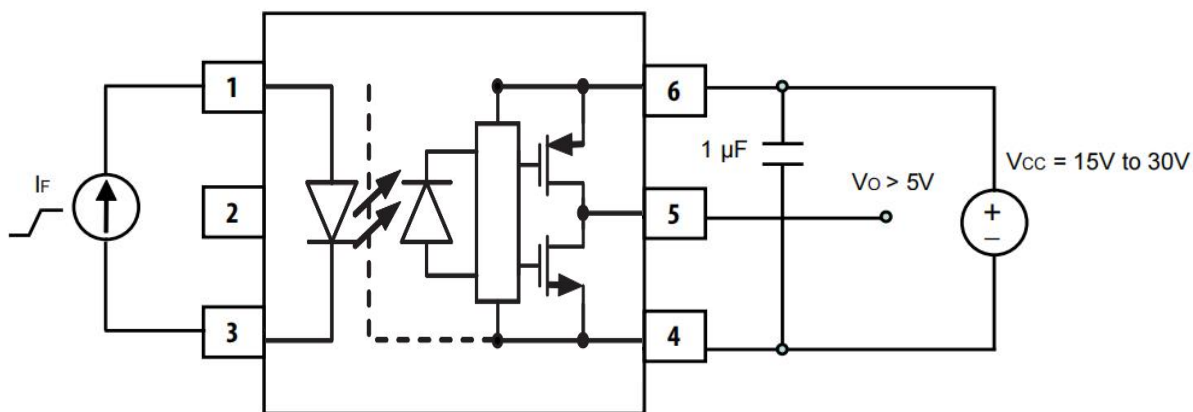


Fig.19 U_{VLO} Test Circuit

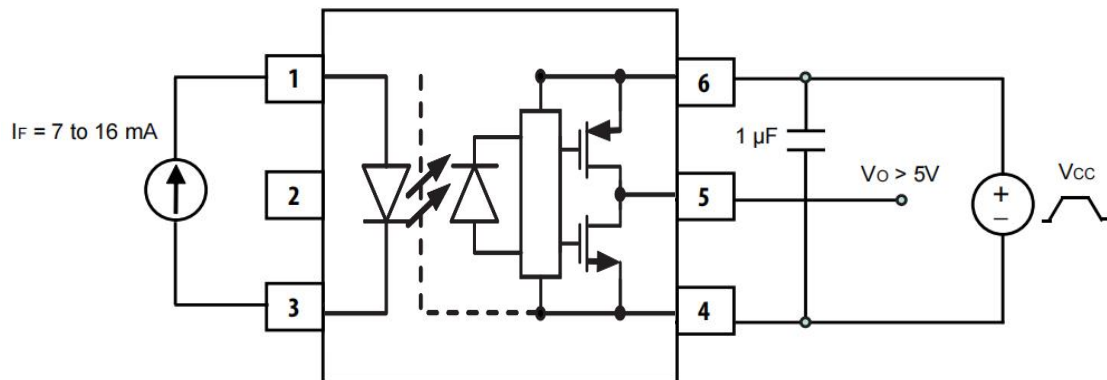


Fig.20 t_{PHL} , t_{PLH} , t_r and t_f Test Circuit and Waveforms

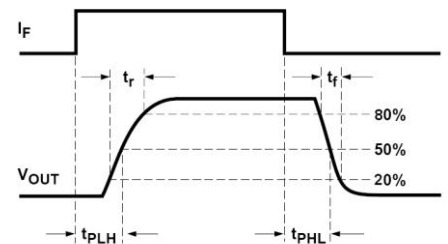
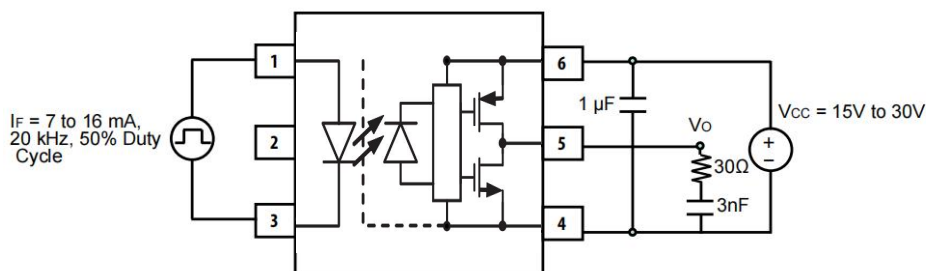
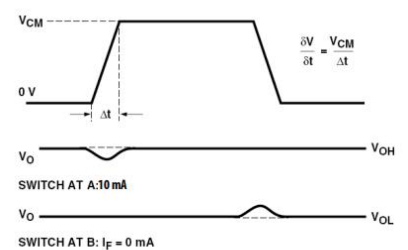
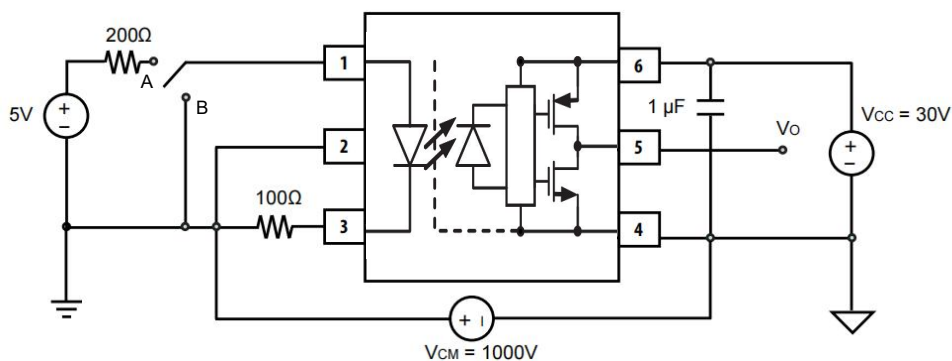
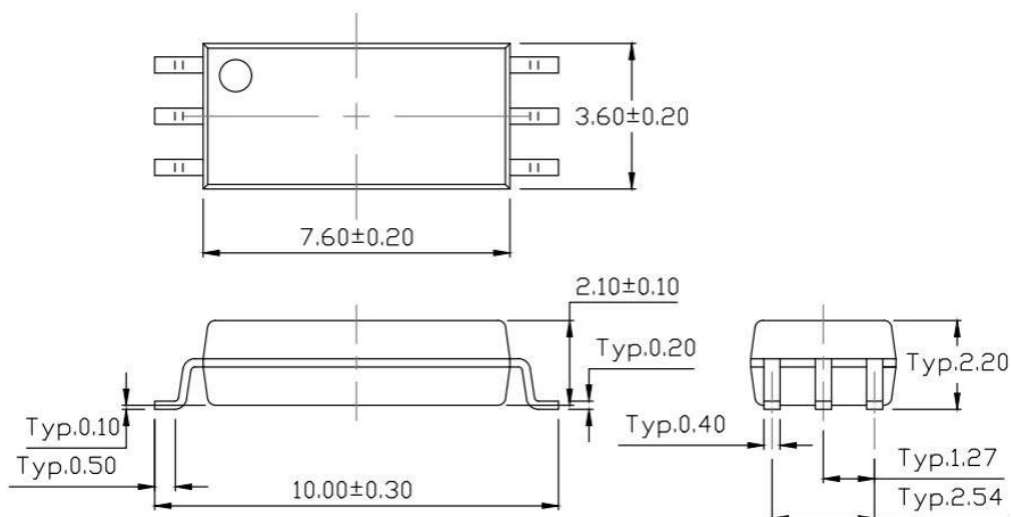


Fig.21 CMR Test Circuit with Split Resistors Network and Waveforms

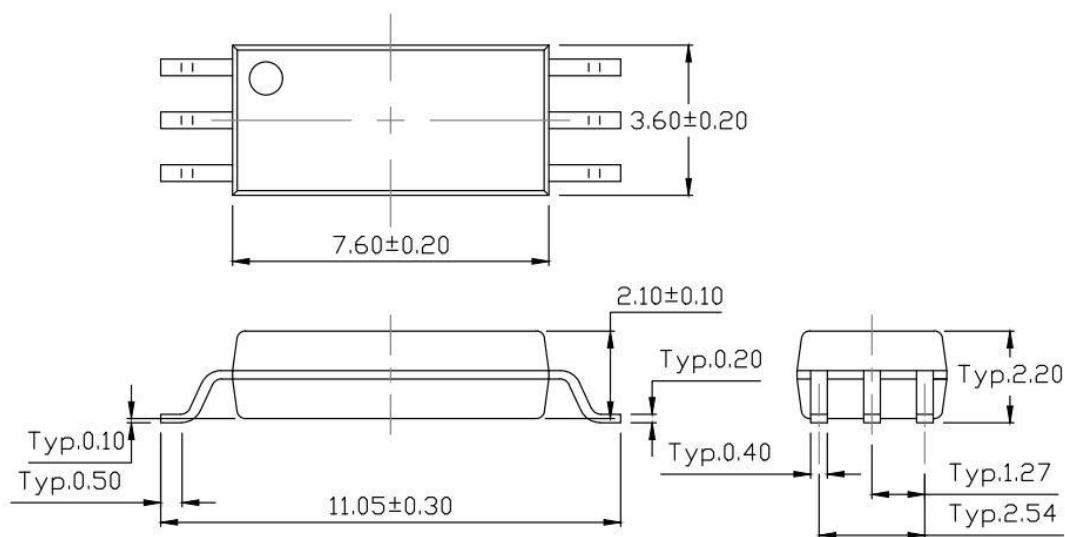


PACKAGE DIMENSIONS

Surface Mount Lead Forming (P type)



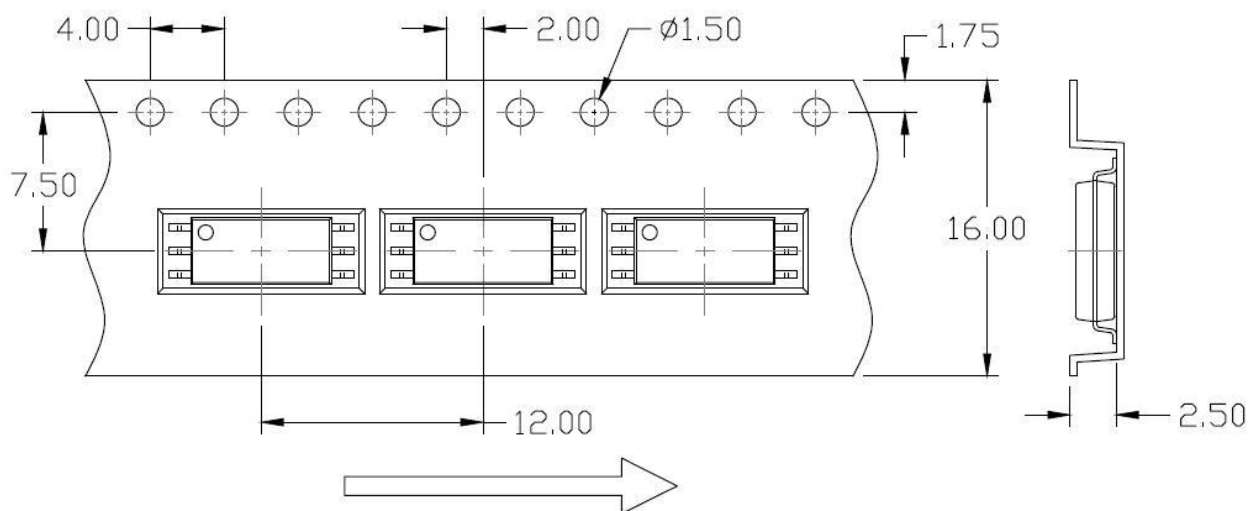
Surface Mount Lead Forming (W type)



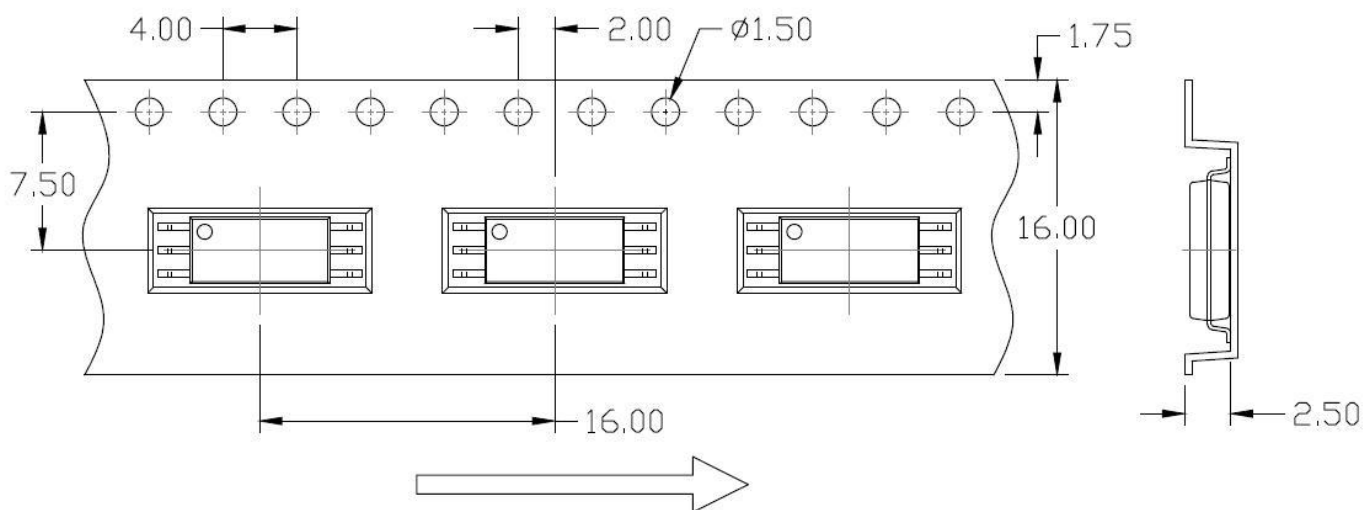
- Dimensions in mm unless otherwise stated

TAPING DIMENSIONS

Option LSOP6-P



Option LSOP6-W

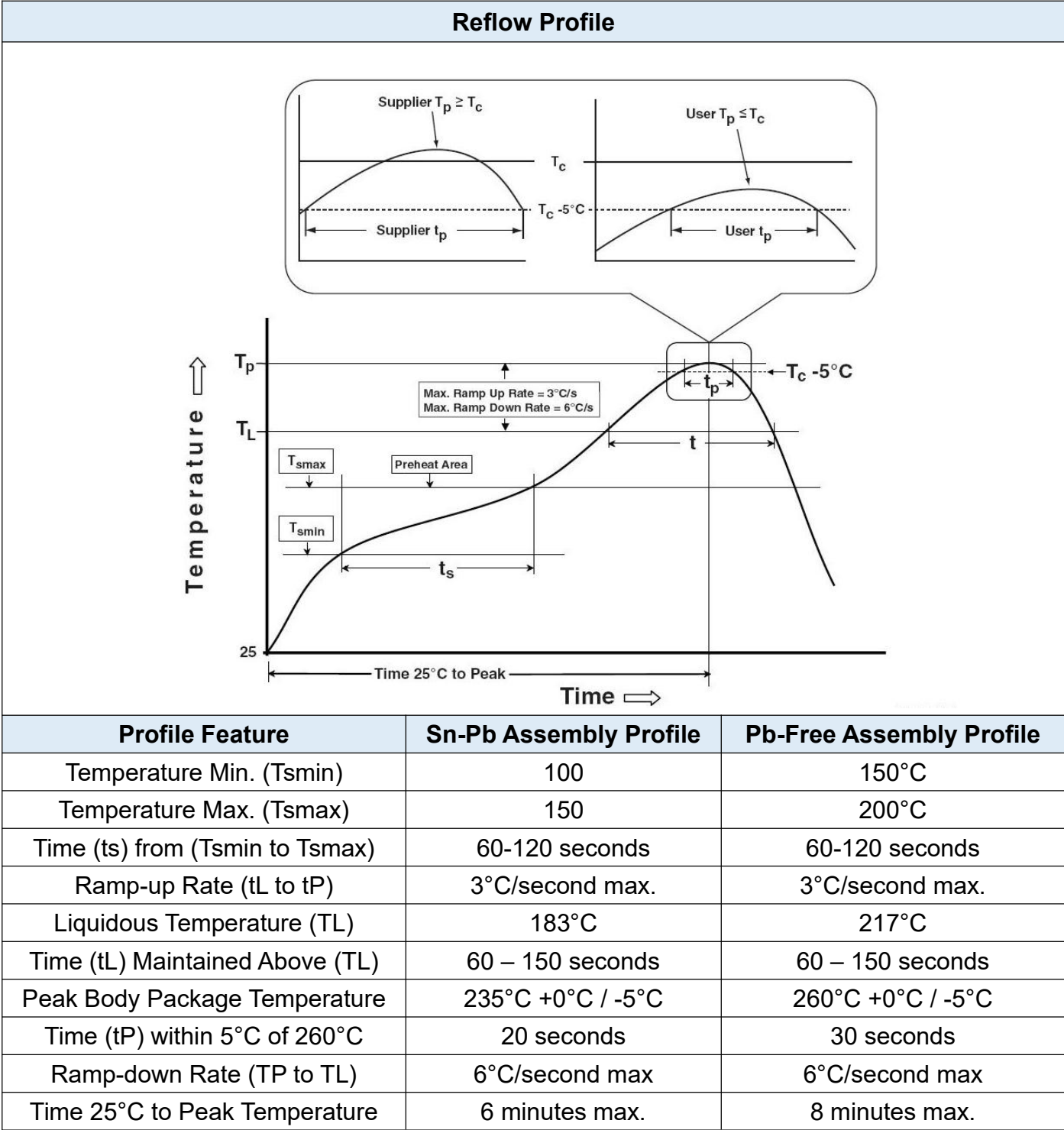


- Dimensions in mm unless otherwise stated

ORDERING AND MARKING INFORMATION

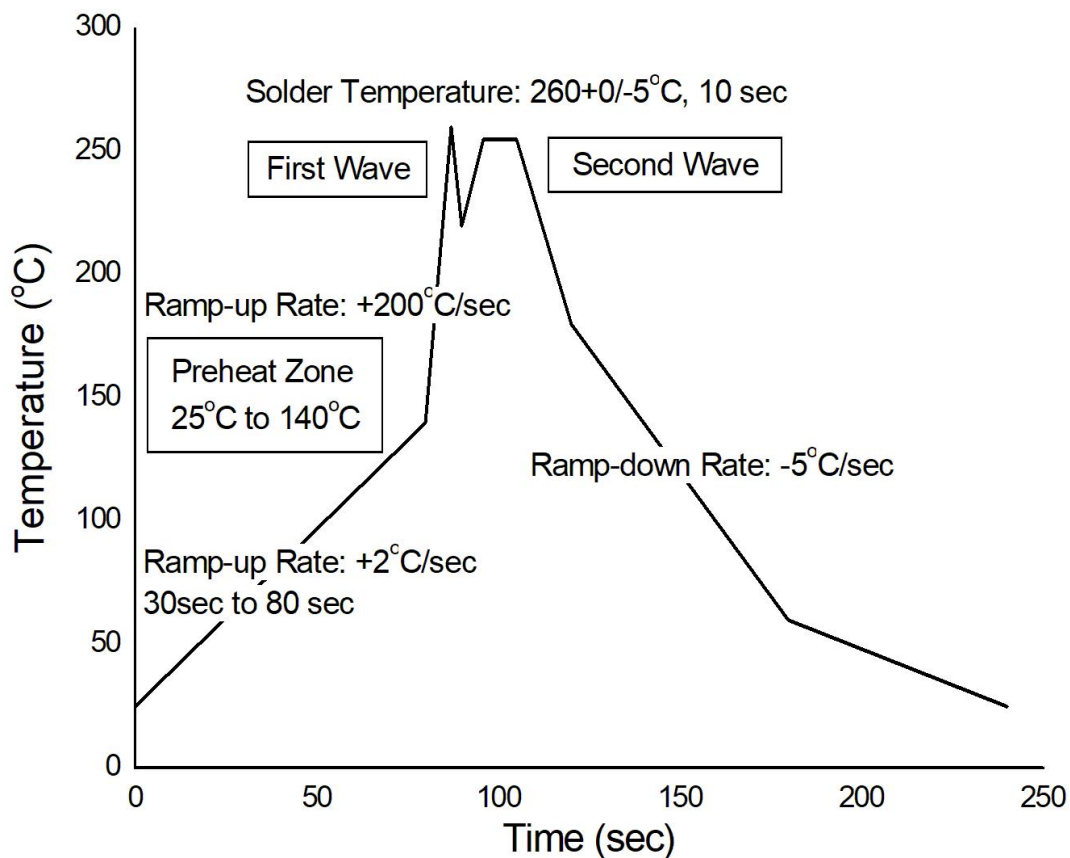
Marking Information			
		5701 : Part Number X : Lead Form Option (P/W) / : ISOMICRON YY : Fiscal Year WW : Work Week A : Manufacturing Code	
Order Code			
Product series Lead Form Option P - 9mm Clearance W- 11mm Clearance		TLP5701 X - X VDE Option: V: VDE pass	
Packing Quantity			
Option	Quantity	Quantity – Inner box	Quantity – Outer box
TLP5701W	1500Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 15k Units
TLP5701P	1500Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 15k Units

REFLOW INFORMATION



TEMPERATURE PROFILE OF SOLDERING

Wave Soldering (JESD22-A111 Compliant)



Hand Soldering By Soldering Iron

Soldering Temperature	380+0/-5°C
Soldering Time	3 sec max.

- One time soldering is recommended for all soldering method.
- Do not solder more than three times for IR reflow soldering.

DISCLAIMER

- ISOMICRON is continually improving the quality, reliability, function and design. ISOMICRON reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
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- This product is not intended to be used for military, aircraft, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Please contact ISOMICRON sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify ISOMICRON's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.