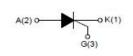






20A SCRs



BT152-600/800 BT152-600R/800R



## **DESCRIPTION:**

Non-Insulated

With high ability to withstand the shock loading of large current, BT152 Series SCRs provide high dv/dt rate with strong resistance to electromagnetic interference. From all three terminals to external heatsink, BT152 series SCRs provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Packages are RoHS compliant (2011/65/EU)

### **FEATURES**

Symbol	Value	Unit		
$V_{DRM}/V_{RRM}$	600/800	V		
I <sub>T(RMS)</sub>	20	Α		
I <sub>GT</sub>	≤15	mA		

## **APPLICATIONS:**

Its application is in solid state relay, motorcycle, power charger, T-tools etc

## ABSOLUTE MAXIMUM RATINGS ( $T_a = 25$ °C)

Parameter	Symbol	Value	Unit
Repetitive peak off-state voltage(Tj=25°C)	$V_{DRM}$	600/800	V
Repetitive peak reverse voltage(Tj=25°C)	$V_{RRM}$	600/800	V
RMS on-state current $ \frac{T_c = 100^{\circ}C}{T_c = 120^{\circ}C} $	I <sub>T(RMS)</sub>	20	А
Non repetitive surge peak on-state current (tp=10ms)	I <sub>TSM</sub>	200	Α
I2t value for fusing (tp=10ms)	l <sup>2</sup> t	200	A2s
Critical rate of rise of on-state current $(I_G=2\times I_{GT})$	dl/dt	50	A/µs
Peak gate current	I <sub>GM</sub>	4	Α
Average gate power dissipation	$P_{G(AV)}$	1	W
Peak gate power	$P_{GM}$	5	W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C

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**ELECTRICAL CHARACTERISTICS** (Ta=25°C Unless otherwise specified)

Parameter		Cumbal	Toot Conditions	Value			Unit
		Symbol	Test Conditions	Min	Тур.	Max.	Unit
Gate Trigger Current		$I_GT$	V -40V/DL-220			25	mΑ
Gate Trigger Voltage		$V_{GT}$	$V_D$ =12V RL=33 $\Omega$			1.3	V
Non-triggering gate volta	age	$V_{\sf GD}$	$V_D=V_{DRM}$ Tj=150°C RL=3.3KΩ	0.2			V
Latching Current		$I_L$	I <sub>G</sub> =1.2I <sub>GT</sub>			70	mA
Holding Current		I <sub>H</sub>	I <sub>T</sub> =500mA			60	mA
Critical Rate of Rise of C	Off-State Vo	dV/dt	V <sub>D</sub> =2/3V <sub>DRM</sub> Gate Open Tj=150°C	200	-		V/µs
STATIC CHARACTERISTICS							
Peak on-state voltage drop	Tj=25°C	$V_{TM}$	I <sub>TM</sub> =40A t <sub>p</sub> =380μs			1.55	V
Threshold voltage	Tj=150°C	$V_{T0}$				0.80	V
Dynamic resistance	Tj=150°C	$R_d$				20	Ω
Maximum forward leakage current	Tj=25°C	I <sub>DRM</sub>				5	μA
Maximum reverse leakage current	Tj=150°C	I <sub>RRM</sub>	$V_D = V_{DRM} V_R = V_{RRM}$			4	mA
THERMAL RESISTANCES							
Parameter		Symbol	Value	Unit			
I							

Parameter	Symbol	Value	Unit
Junction to case(AC)	Rth <sub>(i-c)</sub>	1.05	°C/W







### TYPICAL CHERESTERISTIC CURVES

FIG.1 Maximum power dissipation versus RMS on-state current

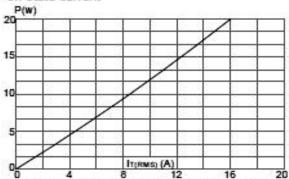


FIG.3: Surge peak on-state current versus number of cycles

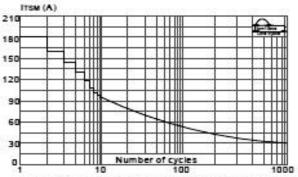


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width tp<10ms, and corresponging value of I't (dl/dt<50A/µs)

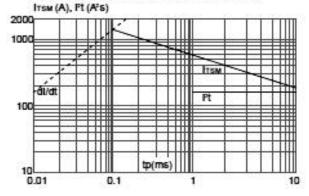


FIG.2: RMS on-state current versus case temperature

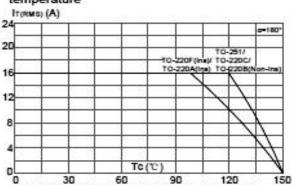


FIG.4: On-state characteristics (maximum values)

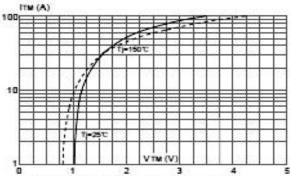
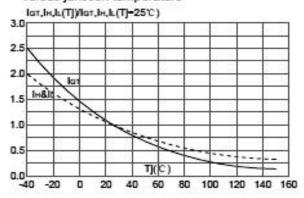


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



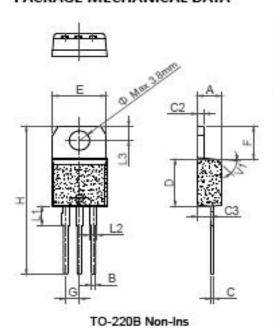






## **Package Details**

## PACKAGE MECHANICAL DATA



	Dimensions						
Ref.	38	Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.	
A	4.40		4.60	0.173	<b>2</b>	0.181	
В	0.61		0.88	0.024		0.035	
С	0.46		0.70	0.018		0.028	
C2	1.21		1.32	0.048		0.052	
C3	2.40		2.72	0.094		0.107	
D	8.60		9.70	0.339		0.382	
E	9.60		10.4	0.378		0.409	
F	6.20		6.60	0.244		0.260	
G	- 8	2.54	8	8	0.1	Š.	
н	28.0		29.8	1.102		1.173	
L1		3.75			0.148		
L2	1.14		1.70	0.045	j)	0.067	
L3	2.65		2.95	0.104	Š.	0.116	
V1		45*			45*		

**Package Details** 

Package	Outline	Tube (Pcs)	Inner box (PCS)	Per Carton
TO-220B	TUBE	50	1000	8000

Note: For AECQ compliant products, please suffix -AH in the part number while ordering







## Recommended Product Storage Environment for Diode and Transistors

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- · Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying.
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

#### Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years, the products shall be subjected FloorLife of CDIL Products and MSL Level

The proc When the products are opened from the original packing, the floor life will start. For this subject to the following JEDEC table may be referred:

	JEDEC MSL Level				
Level	Time	Condition			
1	Unlimited	≤30 °C / 85% RH			
2	1 Year	≤30 °C / 60% RH			
2a	4 Weeks	<30 °C / 60% RH			
3	168 Hours	≤30 °C / 60% RH			
4	72 Hours	≤30 °C / 60% RH			
5	48 Hours	≤30 °C / 60% RH			
5a	24 Hours	≤30 °C / 60% RH			
6	Time on Label(TOL)	<30 °C / 60% RH			

Figure 1 Floor Life according to JEDEC MSL Level







#### **Customer Notes**

### **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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