

1A SCHOTTKY BARRIER RECTIFIERS

SR120 to SR1100



DO-41P

**DO-41P Axial Leaded
Plastic Package
RoHS compliant**

APPLICATION:

For use in Low Voltage, High Frequency Inverters, Free Wheeling and Polarity Protection Applications

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

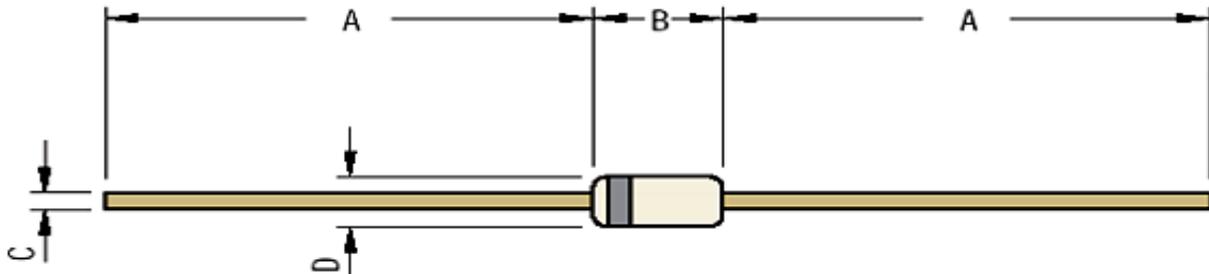
PARAMETER	SYMBOL	SR 120	SR 130	SR 140	SR 150	SR 160	SR 180	SR 1100	UNIT
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	20	30	40	50	60	80	100	V
Maximum RMS Voltage	V_{RMS}	14	21	28	35	42	56	70	V
Maximum DC Blocking Voltage	V_{DC}	20	30	40	50	60	80	100	V
Maximum Average Forward Rectified Current 0.375" (9.5mm) Lead Length at $T_L=100^\circ\text{C}$	$I_{(AV)}$	1.0							A
Peak Forward Surge Current, 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I_{FSM}	40							A
Maximum Forward Voltage at 1.0 A	V_F	0.45	0.55	0.6	0.7		0.85		V
Maximum DC Reverse Current at $T_j=25^\circ\text{C}$ mA	I_R	1.0							mA
at Rated DC Blocking Voltage at $T_j=100^\circ\text{C}$		10							mA
Typical Junction Capacitance	C_J^1	110			80				pF
Typical Thermal Resistance	$R_{th(J-L)}^2$	15							$^\circ\text{C/W}$
Operating Junction Temperature Range	T_J	-55 to +150							$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-55 to +150							$^\circ\text{C}$

Note:

1. Measured at 1MHz and applied reverse voltage of 4.0 V
2. Thermal Resistance Junction to Lead

PACKAGE DETAIL

DO-41P Axial Plastic Package



52mm - DO-41P Package

DIM	MIN	MAX
A	25.4	--
B	4.20	5.20
C	0.70	0.86
D	2.00	2.70

All Dimensions are in mm

26mm - DO-41P Package

DIM	MIN	MAX
A	14.6	--
B	4.10	5.20
C	0.71	0.86
D	2.00	2.70

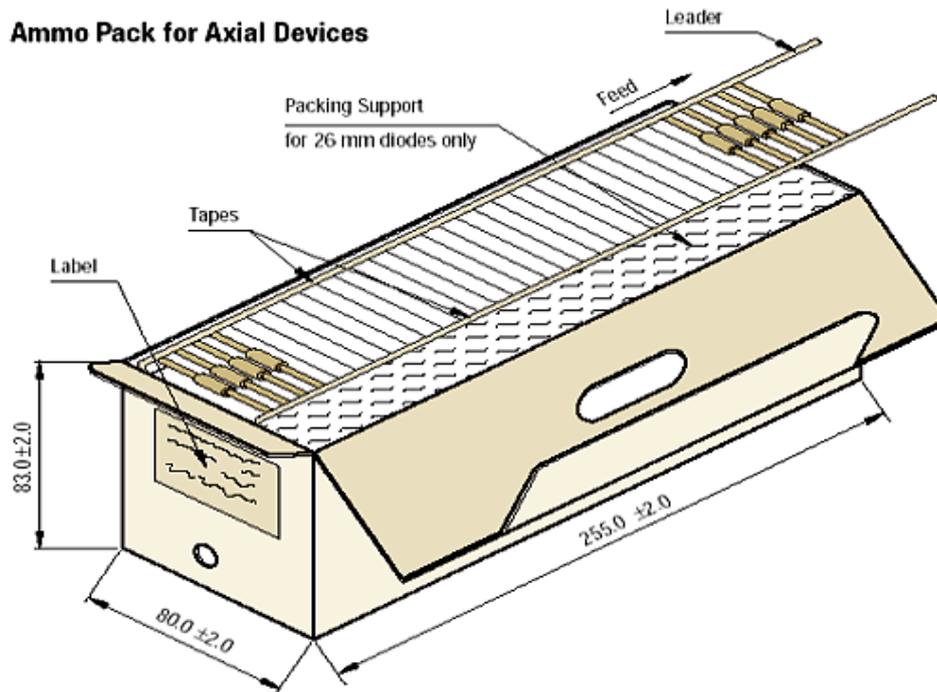
All Dimensions are in mm

Polarity: Colour band denotes cathode end



DO-41P

AMMO PACKING FOR DO-41P



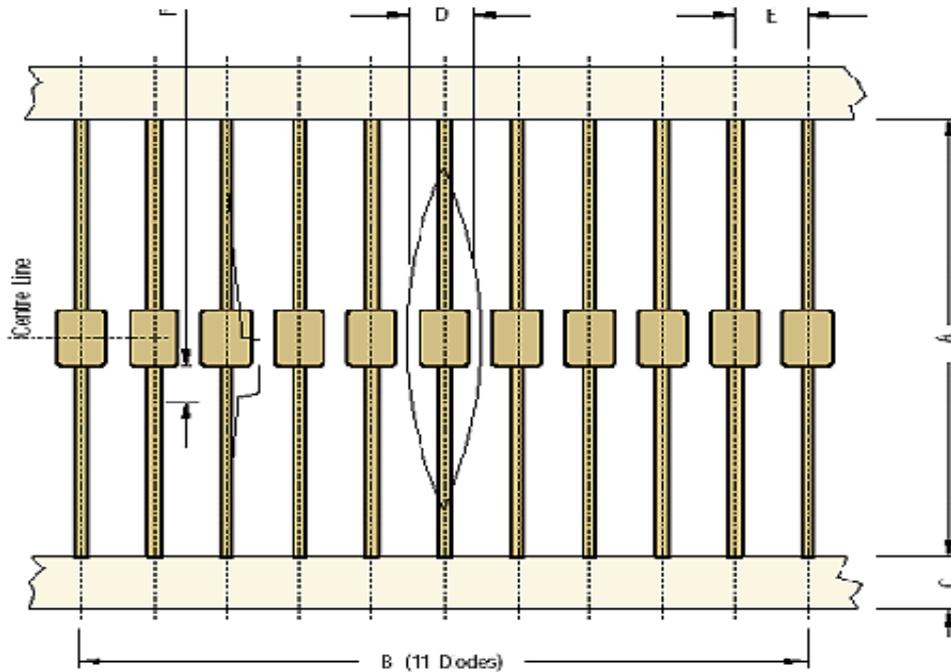
All Dimensions are in mm

Packaging Information

Package/ Case Type	Packaging Type	Std. Packing	Inner Carton			Outer Carton		
		Qty	Qty	Size L x W x H (cm)	Gross Weight (Kg)	Qty	Size L x W x H (cm)	Gross Weig ht (Kg)
DO-41P (52mm)	T&A	5,000	5K	27 x 8 x 14	1.96	45K	46 x 35 x 25	17.5
DO-41P (26mm)	T&A	3,000					50 x 6.5 x 31	

T & A: Tape and Ammo Pack

AXIAL TAPE FOR DO-41P



52 mm Taping Specification

1. 300 mm (min) leader tape on every tape.
2. No. of empty places allowed 0.25% without
3. End of leads shell preferably not protrude beyond the tapes.
4. Components shall be help sufficiently in the tape or tapes so that they can not come free in normal handling.

DO-41P 52 mm Tape		
DIM	Min	Max
A	50.0	54.0
B	95.0	105.0
C	5.60	6.50
D		1.5R
E	9.50	10.50
F		1.25

DO-41P 26 mm Tape		
DIM	Min	Max
A	26.0	26.4
B	50.0	60.0
C	5.00	6.00
D		1.5R
E	4.50	5.50
F		0.8



Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down.
They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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