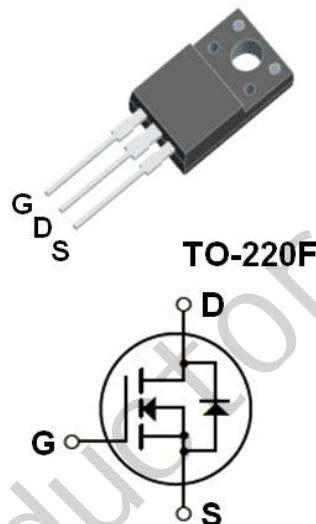


## Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 31\text{nC}$  (Typ.).
- $V_{DSS} = 100\text{V}, I_D = 30\text{A}$
- $R_{DS(on)} : 36\text{m}\Omega$  (Max) @  $V_G = 10\text{V}$
- 100% Avalanche Tested



## Absolute Maximum Ratings\* ( $T_c = 25^\circ\text{C}$ Unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DSS}$	100	V
Gate-Source Voltage	$V_{GSS}$	$\pm 20$	V
Drain Current-Continuous	$I_D$	30	A
Drain Current-Continuous( $T_c = 100^\circ\text{C}$ )	$I_D(100^\circ\text{C})$	12	A
Pulsed Drain Current	$I_{DM}$	60	A
Maximum Power Dissipation	$P_D$	55	W
Single pulse avalanche energy <small>(Note 5)</small>	$E_{AS}$	250	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ\text{C}$

## Thermal Characteristics

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	2.27	$^\circ\text{C}/\text{W}$
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**Electrical Characteristics ( $T_C=25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	100	110	-	V
Zero Gate Voltage Drain Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=100\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-Body Leakage Current	$\text{I}_{\text{GSS}}$	$\text{V}_{\text{GS}}=\pm 20\text{V}, \text{V}_{\text{DS}}=0\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b> <sup>(Note 3)</sup>						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	0.8	1.1	1.5	V
Drain-Source On-State Resistance	$\text{R}_{\text{DS(ON)}}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=5\text{A}$	-	31	35	$\text{m}\Omega$
Forward Transconductance	$\text{g}_{\text{FS}}$	$\text{V}_{\text{DS}}=50\text{V}, \text{I}_D=9\text{A}$	12	-	-	S
<b>Dynamic Characteristics</b> <sup>(Note 4)</sup>						
Input Capacitance	$\text{C}_{\text{iss}}$	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V},$ $F=1.0\text{MHz}$	-	1350	-	PF
Output Capacitance	$\text{C}_{\text{oss}}$		-	240	-	PF
Reverse Transfer Capacitance	$\text{C}_{\text{rss}}$		-	180	-	PF
<b>Switching Characteristics</b> <sup>(Note 4)</sup>						
Turn-on Delay Time	$t_{\text{d(on)}}$	$\text{V}_{\text{DD}}=30\text{V}, \text{I}_D=2\text{A}, \text{R}_L=15\Omega$ $\text{V}_{\text{GS}}=10\text{V}, \text{R}_G=2.5\Omega$	-	13.8	-	nS
Turn-on Rise Time	$t_r$		-	9.3	-	nS
Turn-Off Delay Time	$t_{\text{d(off)}}$		-	43.8	-	nS
Turn-Off Fall Time	$t_f$		-	11.4	-	nS
Total Gate Charge	$\text{Q}_g$	$\text{V}_{\text{DS}}=30\text{V}, \text{I}_D=3\text{A},$ $\text{V}_{\text{GS}}=10\text{V}$	-	31	-	nC
Gate-Source Charge	$\text{Q}_{\text{gs}}$		-	6.4	-	nC
Gate-Drain Charge	$\text{Q}_{\text{gd}}$		-	9.4	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage <sup>(Note 3)</sup>	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=9\text{A}$	-	-	1.2	V
Diode Forward Current <sup>(Note 2)</sup>	$\text{I}_S$		-	-	30	A
Forward Turn-On Time	$t_{\text{on}}$	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production
5. EAS condition :  $\text{T}_j=25^\circ\text{C}, \text{V}_{\text{DD}}=50\text{V}, \text{V}_{\text{G}}=10\text{V}, \text{L}=0.5\text{mH}, \text{R}_G=25\Omega$

## Typical Electrical and Thermal Characteristics (Curves)

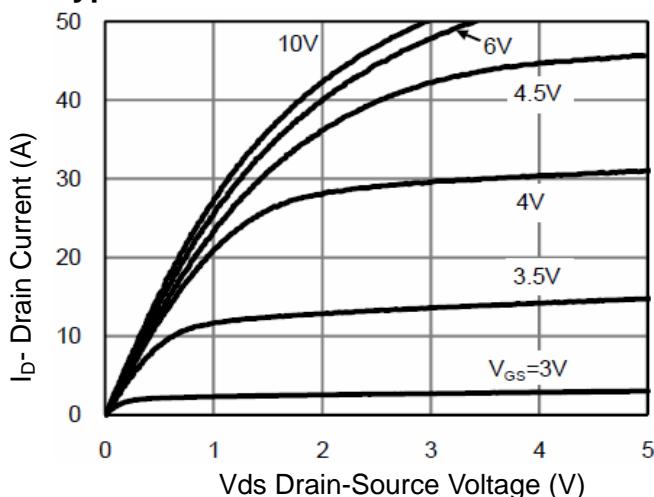


Figure 1 Output Characteristics

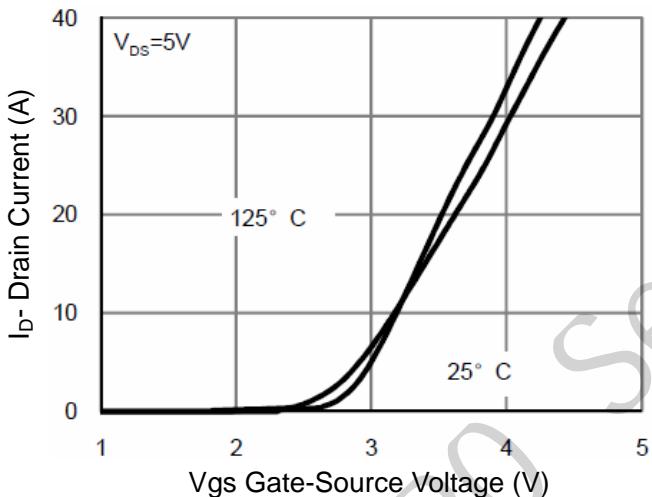


Figure 2 Transfer Characteristics

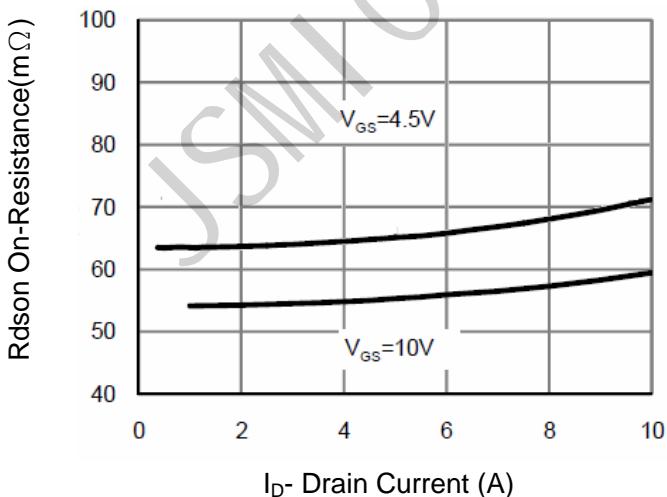


Figure 3 Rdson- Drain Current

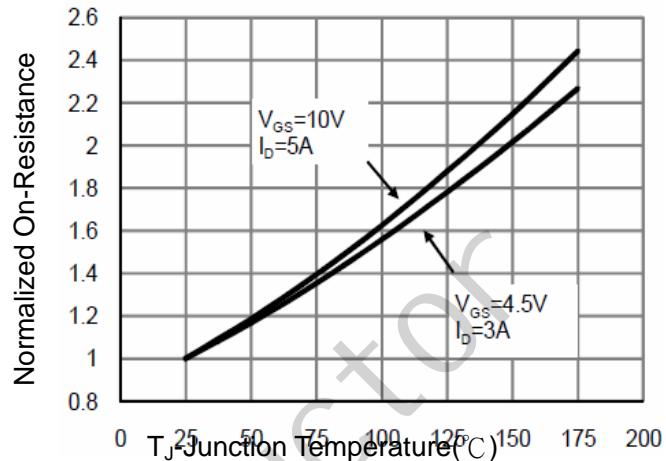


Figure 4 Rdson-JunctionTemperature

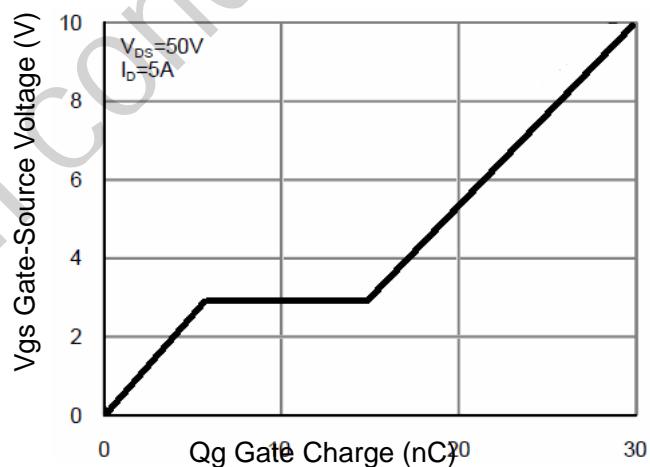


Figure 5 Gate Charge

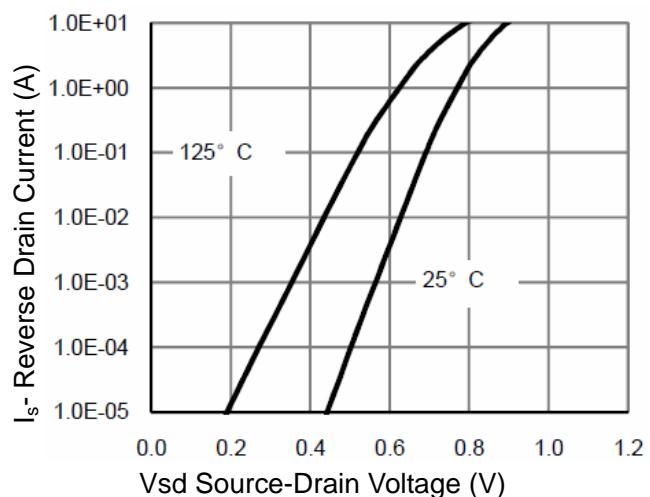
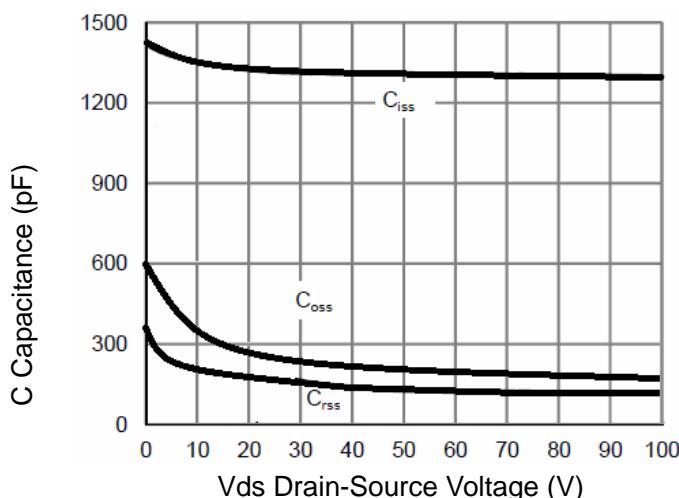
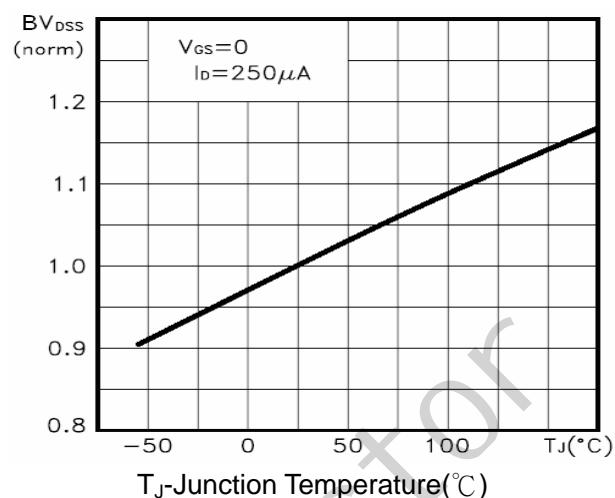
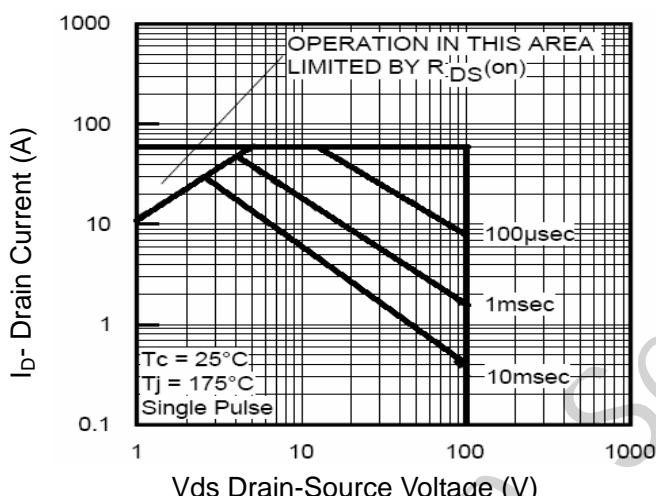
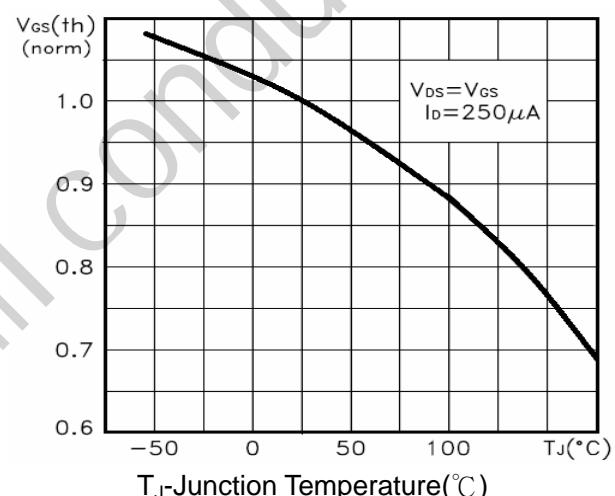
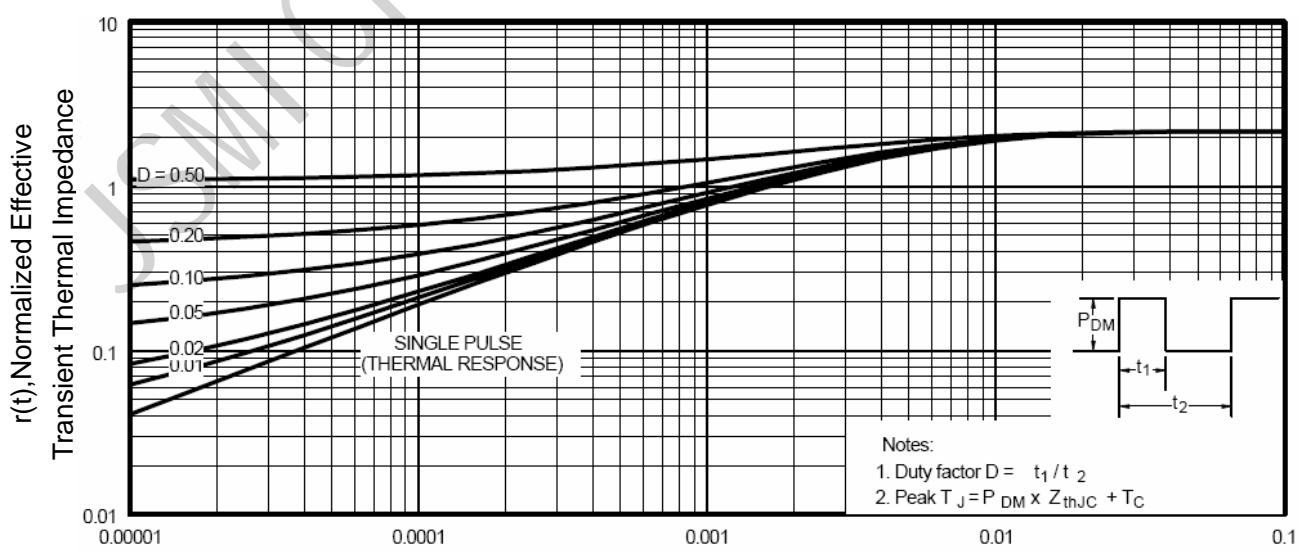
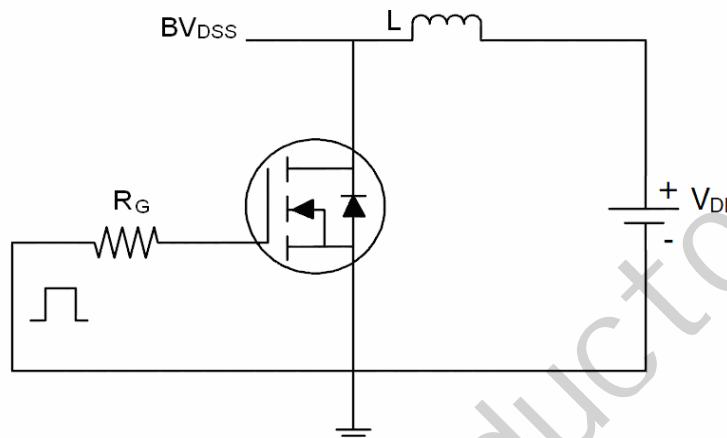


Figure 6 Source- Drain Diode Forward

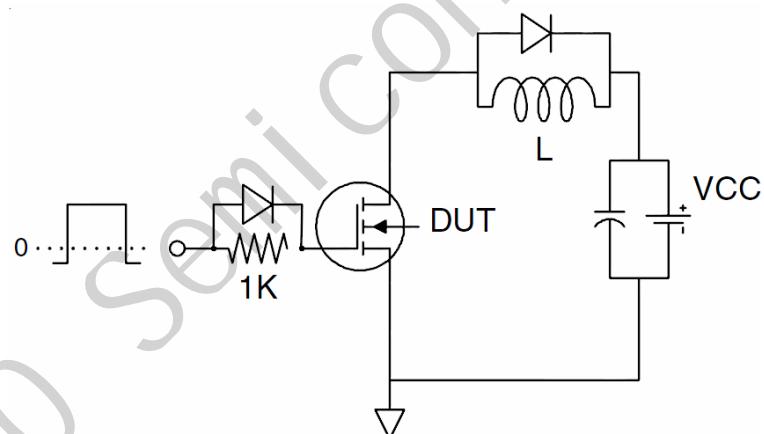

**Figure 7 Capacitance vs Vds**

**Figure 9  $BV_{DSS}$  vs Junction Temperature**

**Figure 8 Safe Operation Area**

**Figure 10  $V_{GS(th)}$  vs Junction Temperature**

**Figure 11 Normalized Maximum Transient Thermal Impedance**

### Test Circuit

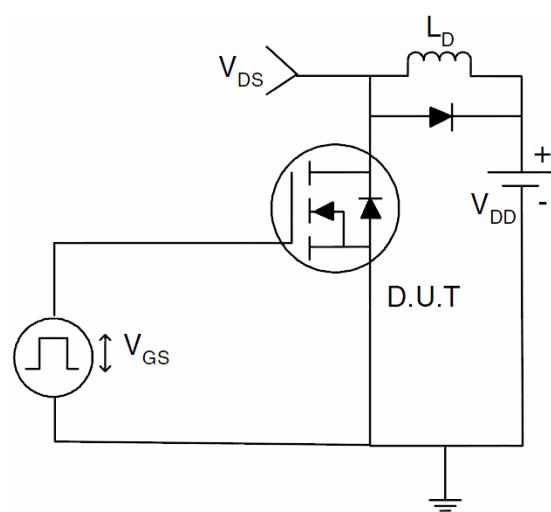
#### 1) E<sub>AS</sub> test Circuit



#### 2) Gate charge test Circuit



#### 3) Switch Time Test Circuit



## TO-220F Package Dimensions

UNIT: mm

SYMBOL	min	nom	max	SYMBOL	min	nom	max
A	9.80		10.60	D		2.54	
A1		7.00		D1	1.15		1.55
A2	2.90		3.40	D2	0.60		1.00
A3	9.10		9.90	D3	0.20		0.50
B1	15.40		16.40	E	2.24		2.84
B2	4.35		4.95	E1		0.70	
B3	6.00		7.40	E2		1.0×45°	
C	3.00		3.70	E3	0.35		0.65
C1	15.00		17.00	E4	2.30		3.30
C2	8.80		10.80	α (度)		30°	

