



PESD5Z3.3

Low capacitance unidirectional ESD protection diodes

2 May 2024

Product data sheet

1. General description

Low capacitance unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 (SC-79) ultra small and flat lead Surface-Mounted Device (SMD) plastic package, designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- ESD protection of one line
- Low diode capacitance
- Max. peak pulse power: $P_{PPM} = 260 \text{ W}$
- Low clamping voltage: $V_{CL} = 8 \text{ V}$
- Low leakage current: $I_{RM} = 8 \text{ nA}$
- ESD protection up to 30 kV (IEC 61000-4-2)
- IEC 61000-4-5 (surge); $I_{PPM} = 20 \text{ A}$

3. Application information

- Computers and peripherals
- Audio and video equipment
- Cellular handsets and accessories
- 10/100/1000 Mbit/s Ethernet
- Communication systems
- Portable electronics
- SIM card protection
- High-speed data lines

4. Quick reference data

Table 1. Quick reference data

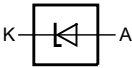
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{RWM}	reverse standoff voltage	$T_{amb} = 25 \text{ }^{\circ}\text{C}$		-	-	3.3	V
I_{PPM}	rated peak pulse current	$t_p = 8/20 \text{ } \mu\text{s}$	[1] [2]	-	-	20	A
V_{CL}	clamping voltage	$I_{PPM} = 20 \text{ A}$; $t_p = 8/20 \text{ } \mu\text{s}$; $T_{amb} = 25 \text{ }^{\circ}\text{C}$	[1] [2]	-	-	18	V

[1] Non-repetitive current pulse 8/20 μs exponentially decaying waveform according to IEC 61000-4-5.

[2] Measured from pin 1 to pin 2.

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	 SC-79 (SOD523)	 006aaa152
2	A	anode		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PESD5Z3.3	SC-79	plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523

7. Marking

Table 4. Marking codes

Type number	Marking code
PESD5Z3.3	N8

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	260	W
I _{PPM}	rated peak pulse current		[1] [2]	-	20	A
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximum ratings						
V _{ESD}	electrostatic discharge voltage	IEC 61000-4-2; contact discharge	[3] [2]	-	30	kV
		MIL-STD-883; human body model (HBM)		-	10	kV
		machine model		-	400	V

- [1] Non-repetitive current pulse 8/20 μs exponentially decaying waveform according to IEC 61000-4-5.
- [2] Measured from pin 1 to pin 2.
- [3] Device stressed with ten non-repetitive ESD pulses.

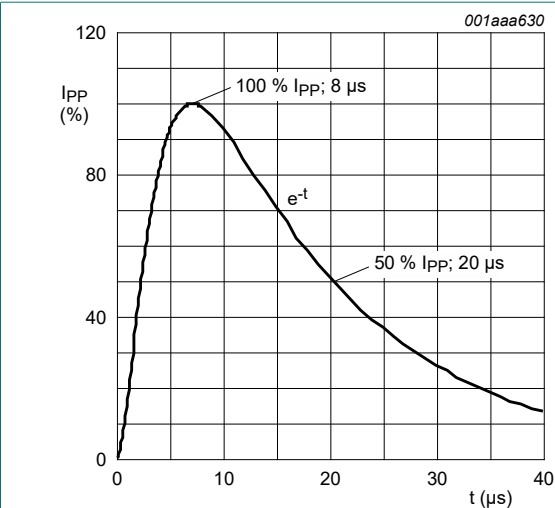


Fig. 1. 8/20 μs pulse waveform according to IEC 61000-4-5

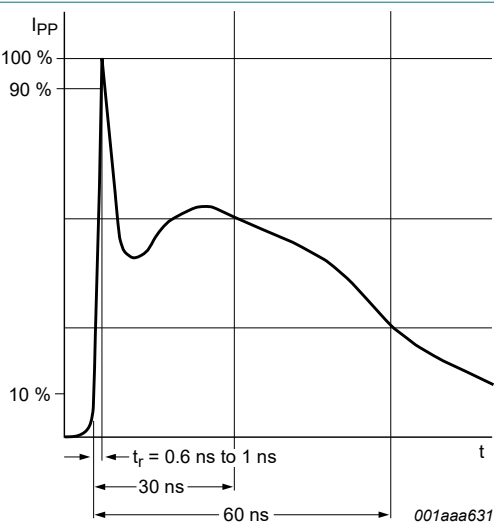


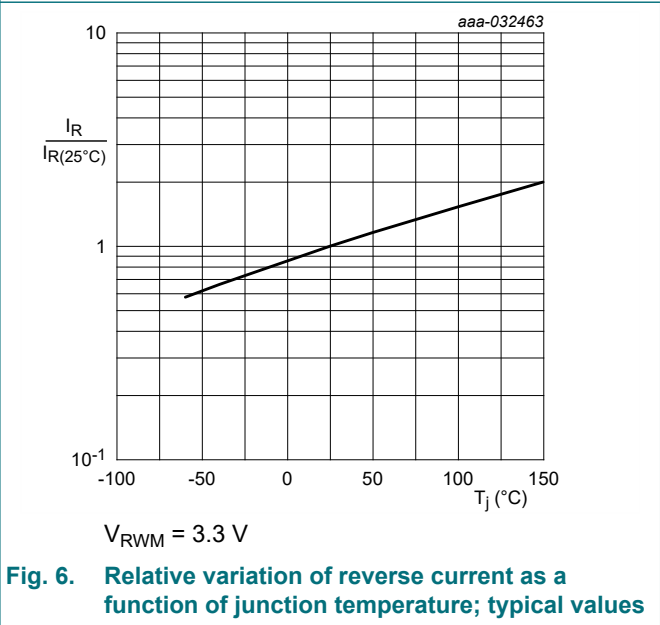
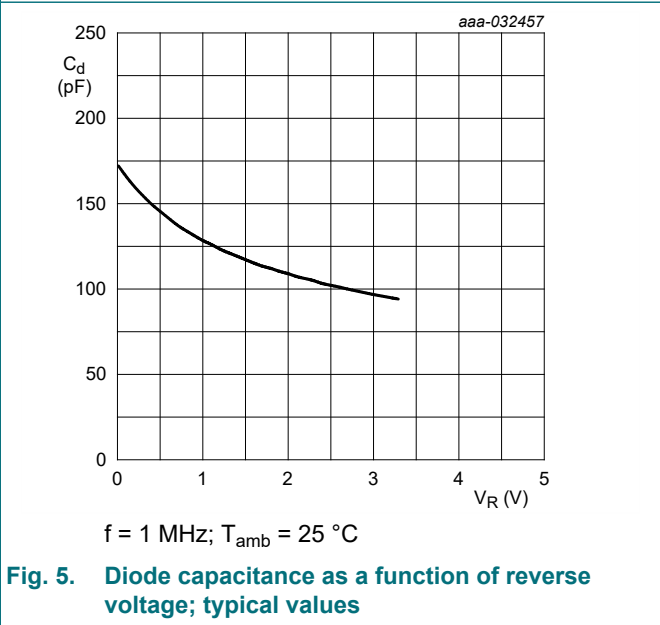
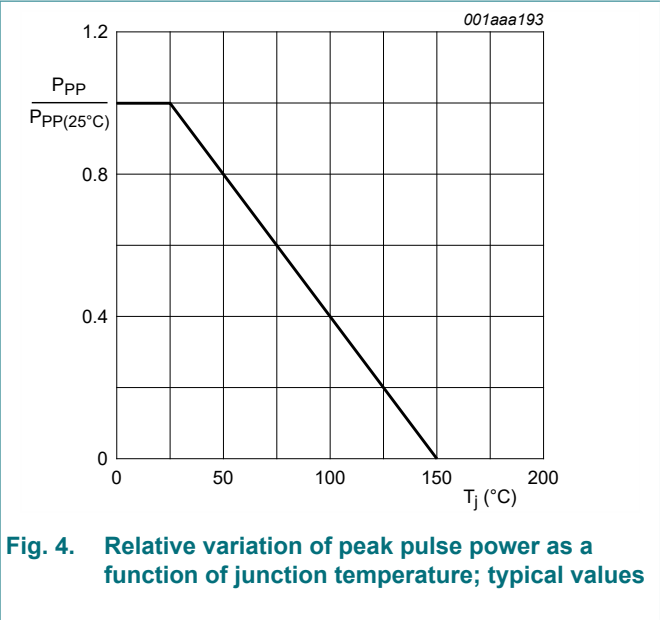
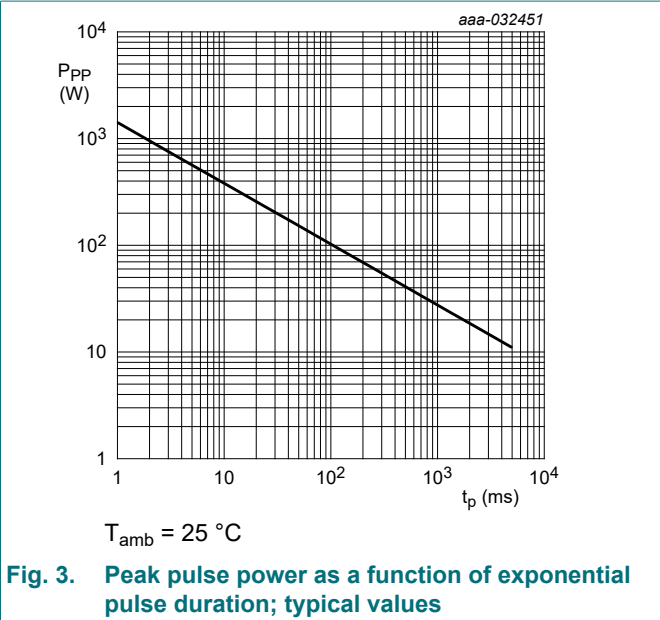
Fig. 2. ESD pulse waveform according to IEC 61000-4-2

9. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	3.3	V
V _{BR}	breakdown voltage	I _R = 1 mA; T _{amb} = 25 °C		5	-	-	V
I _{RM}	reverse leakage current	V _{RWM} = 3.3 V; T _{amb} = 25 °C		-	8	50	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	172	200	pF
V _{CL}	clamping voltage	I _{PP} = 5 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	8	10	V
		I _{PPM} = 20 A; t _p = 8/20 μs; T _{amb} = 25 °C	[1] [2]	-	-	18	V
R _{diff}	differential resistance	I _R = 5 mA; T _{amb} = 25 °C		-	-	10	Ω

- [1] Non-repetitive current pulse 8/20 μs exponentially decaying waveform according to IEC 61000-4-5.
[2] Measured from pin 1 to pin 2.



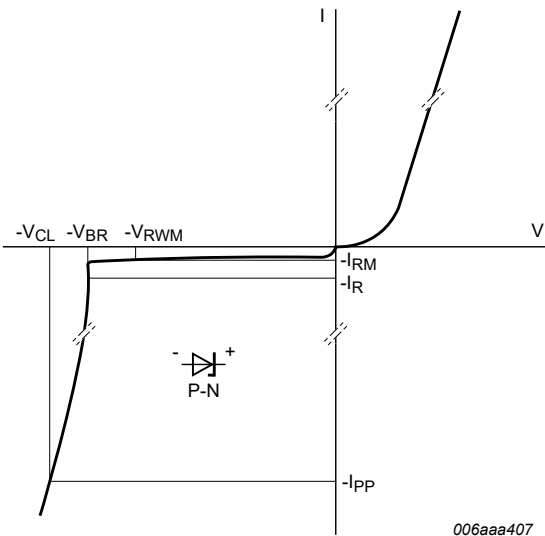


Fig. 7. V-I characteristics for a unidirectional ESD protection diode

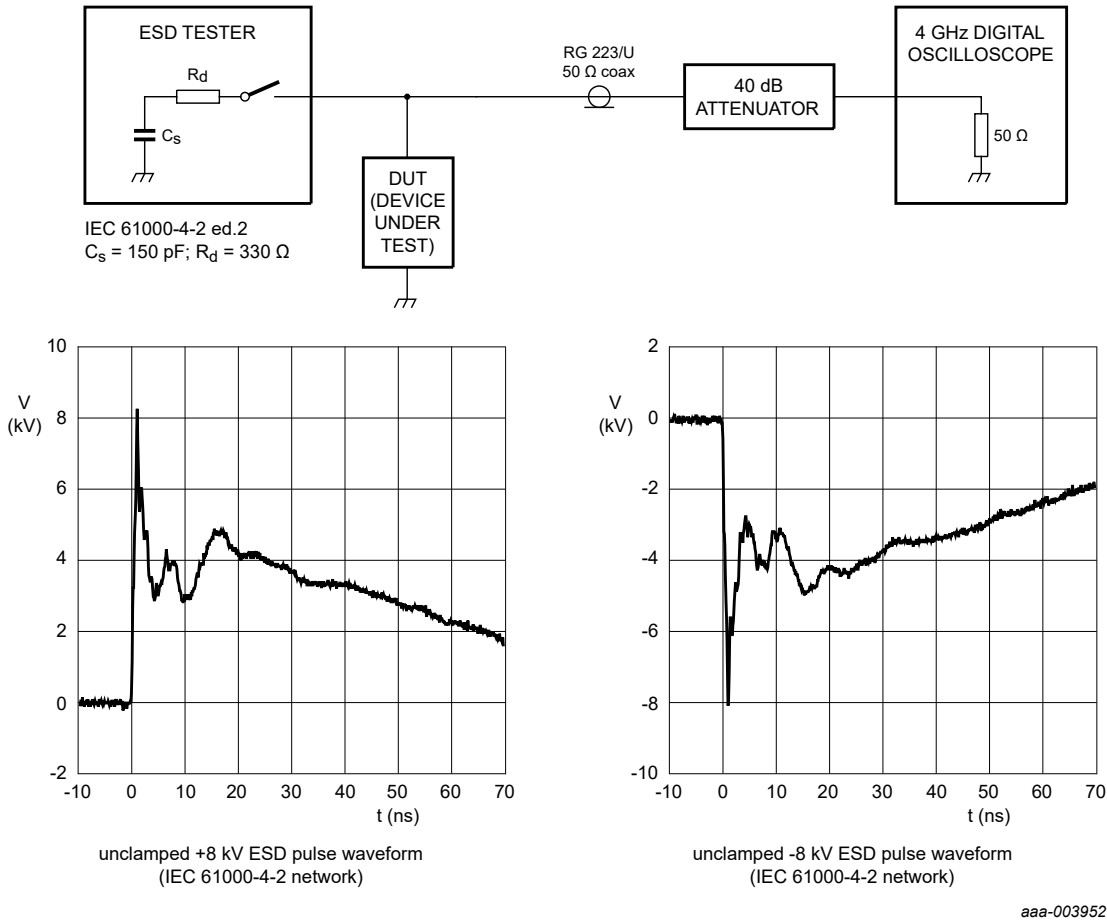


Fig. 8. ESD clamping test setup and waveforms

Low capacitance unidirectional ESD protection diodes

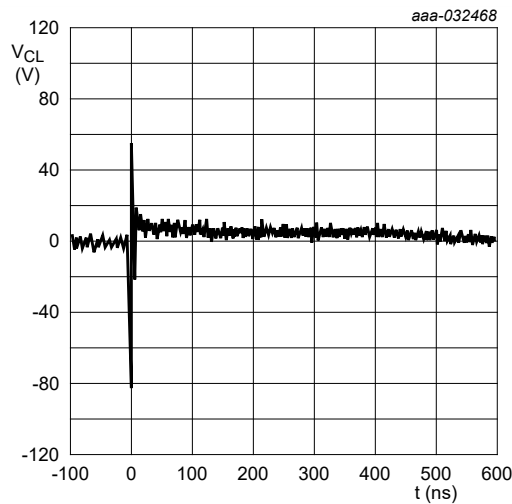


Fig. 9. Clamped +8 kV ESD pulse waveform (IEC 61000-4-2 network)

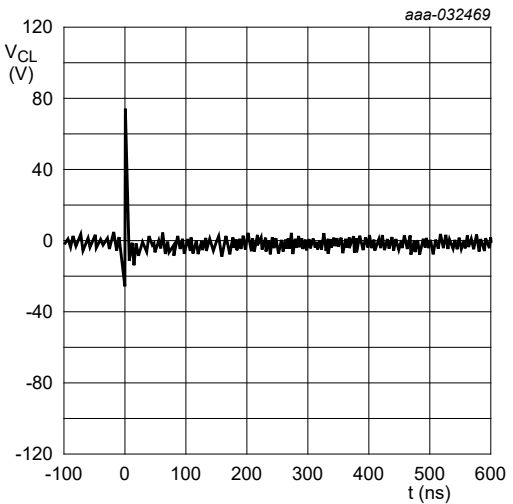


Fig. 10. Clamped -8 kV ESD pulse waveform (IEC 61000-4-2 network)

10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.

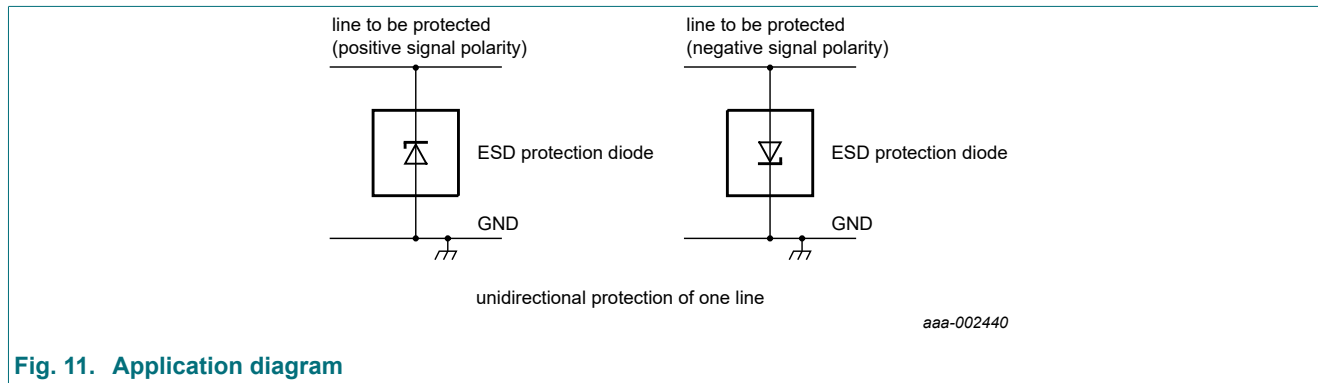


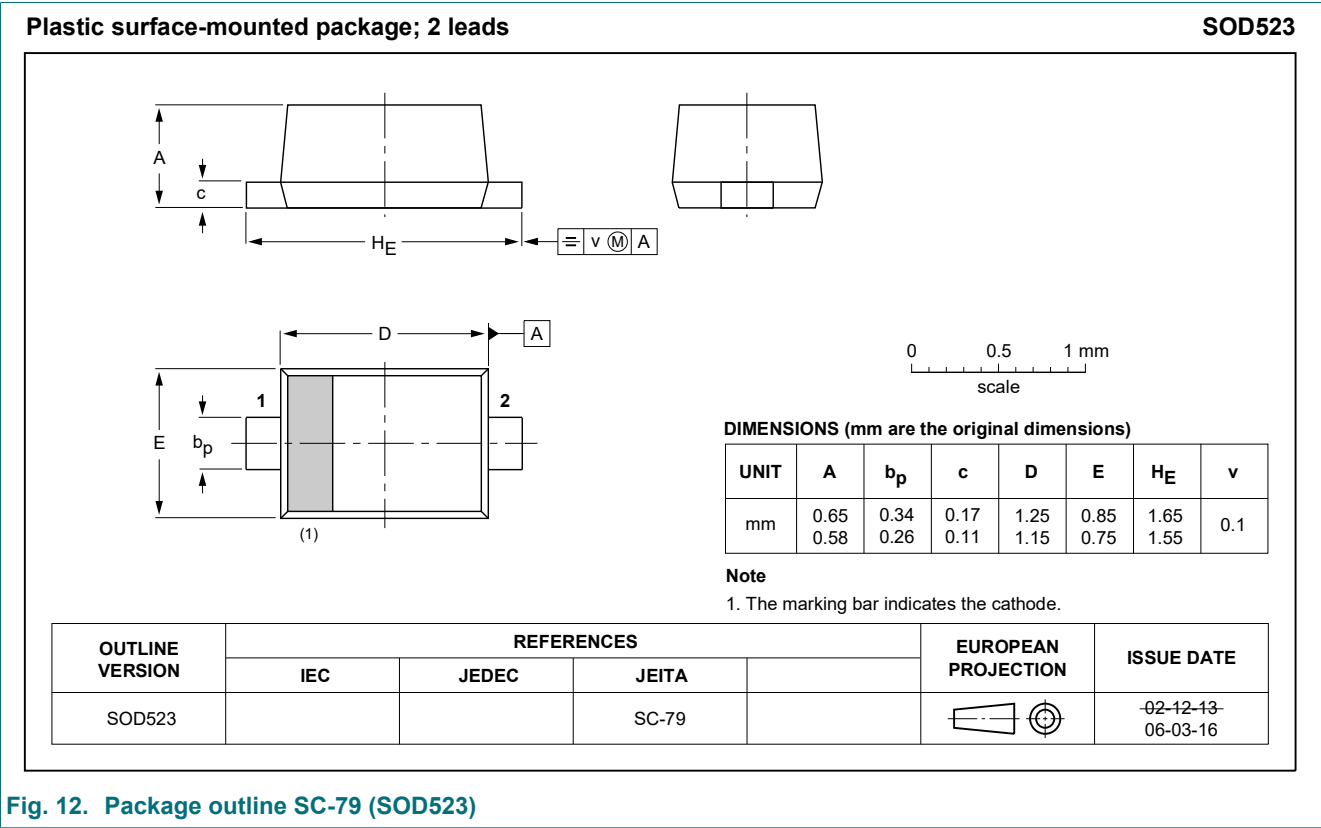
Fig. 11. Application diagram

Circuit board layout and protection device placement

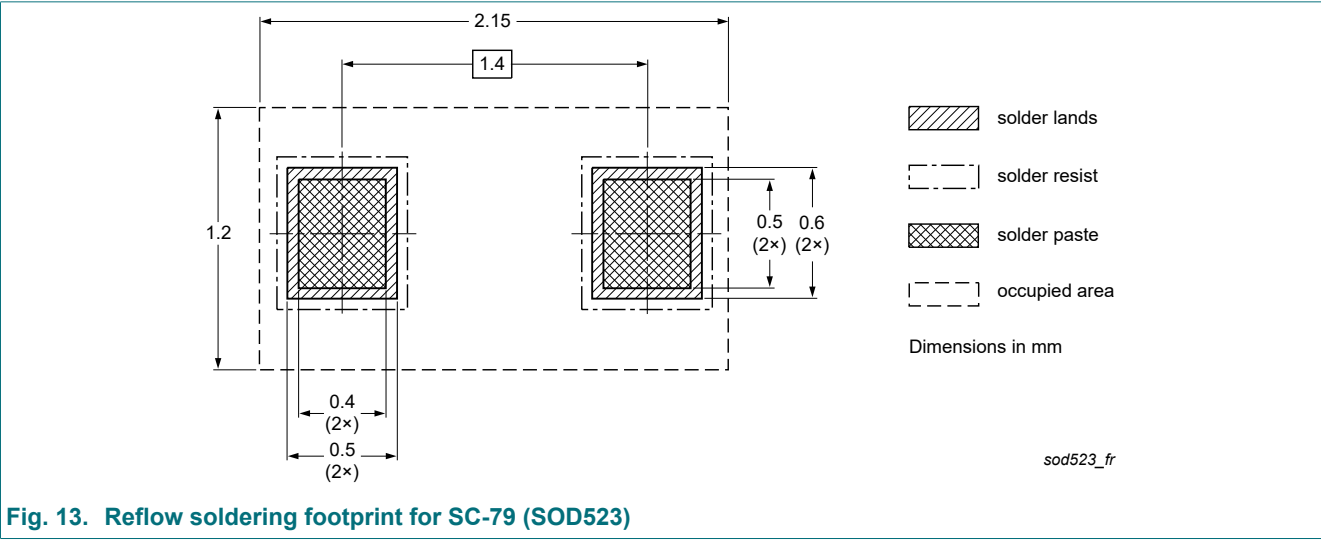
Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

1. Place the device as close to the input terminal or connector as possible.
2. Minimize the path length between the device and the protected line.
3. Keep parallel signal paths to a minimum.
4. Avoid running protected conductors in parallel with unprotected conductors.
5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
6. Minimize the length of the transient return path to ground.
7. Avoid using shared transient return paths to a common ground point.
8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Package outline



12. Soldering



13. Revision history

Table 7. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD5Z3.3 v.4	20240502	Product data sheet	-	PESD5Z3.3 v.3
Modifications:	Product changed to non-automotive qualification. Please refer to nexperia.com for automotive(-Q) product alternative(s).			
PESD5Z3.3 v.3	20210406	Product data sheet	-	PESD5ZX_SER_2
PESD5ZX_SER_2	20080404	Product data sheet	-	PESD5ZX_SER_1
PESD5ZX_SER_1	20070813	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <https://www.nexperia.com>.

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