



TWTLSEMI

TL-2SK3541

SOT723 60V N-Channel Enhancement Mode MOSFET

Description

The 2SK3541 uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

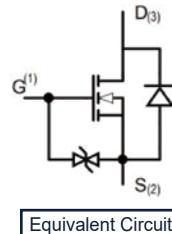
- $V_{DS} = 60V$ $I_D = 115mA$
- $R_{DS(ON)} < 3\Omega$ @ $V_{GS}=10V$



SOT723

Applications

- Load switch
- Power management
- Battery operated systems
- Level Shift Circuits
- DC-DC Converter



Equivalent Circuit

Ordering information

Product ID	Pack	Naming rule	Marking	Qty(PCS)
2SK3541	SOT723		KN	8000

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current ($T_J=150^\circ C$)	115	mA
I_{DM}	Pulsed Drain Current ²	800	mA
P_D	Total Power Dissipation ³	150	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	125	$^\circ C$
$R_{\theta JA}$	Thermal Resistance Junction-ambient 1	833	$^\circ C/W$

Electrical Characteristics ($T_A=25^\circ C$, unless otherwise noted)

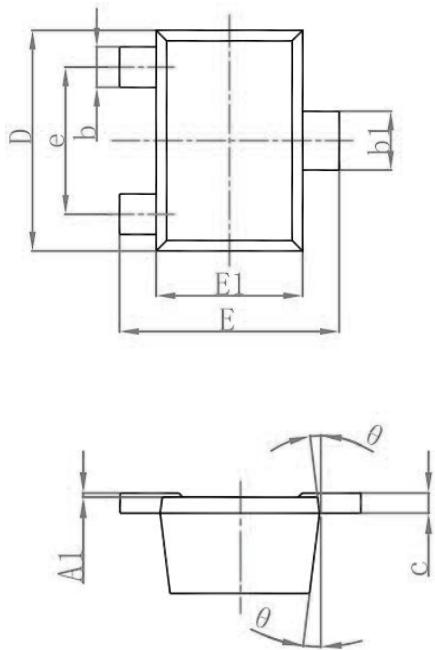
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
On/Off States						
B_{VDSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=0.3A$	---	---	3	Ω
		$V_{GS}=5V, I_D=0.3A$	---	---	3.5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.8	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=48V, V_{GS}=0V$	---	---	1	μA
			---	---		
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 10	nA
Switching Times						
$T_{d(on)}$	Turn-On Delay Time	$V_{GS}=10V, V_{DS}=30V, I_D=200mA, R_G=3.3\Omega$	---	10	---	ns
T_r	Rise Time		---	20	---	
$T_{d(off)}$	Turn-Off Delay Time		---	15	---	
T_f	Fall Time		---	10	---	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, f=1MHz$	---	40	---	pF
C_{oss}	Output Capacitance		---	30	---	
C_{rss}	Reverse Transfer Capacitance		---	10	---	
Drain-Source Diode Characteristics						
I_s	Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	---	---	0.15	A
V_{SD}	Diode Forward Voltage ²	$V_{GS}=0V, I_s=1A, T_j=25^\circ C$	---	---	1.2	V
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	---	---	---	0.8	A

Notes:

1. Surface mounted on FR4 board using the minimum recommended pad size.
2. Pulse Test : Pulse Width=300 μs , Duty Cycle=2%.
3. These parameters have no way to verify.

SOT723 Package Outline Dimensions

SOT723



SYMBOL	MILLIMETERS	
	MIN	MAX
A	0.40	0.50
A1	0.00	0.10
b	0.15	0.25
b1	0.20	0.30
c	0.06	0.16
D	1.10	1.30
e	0.8TYP	
E	1.10	1.30
E1	0.70	0.90
θ	8°	10°