# Transistor, NPN TO-3





## **Description:**

Fast switching speeds and high current capacity ideally suit these parts for use in switching regulators, inverters, wide-band amplifiers and power oscillators in industrial and commercial applications

#### Features:

- High speed  $t_f = 0.5 \mu s$  (Max.)
- High current I<sub>C</sub> (max.) = 30A
- Low saturation  $V_{CE}$  (sat) = 2.5V (max.) at  $I_{C}$  = 20A
- · Pb-free package

# **Maximum Ratings**

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	V <sub>CEO</sub>	90		
Collector-Base Voltage	V <sub>CBO</sub>	V DC		
Collector-Emitter Voltage	V <sub>CEV</sub>	150		
Emitter-Base Voltage	V <sub>EBO</sub>	7 20 30 A DC		
Collector Current-Continuous -Peak (Note 2)	I <sub>C</sub>			
Base Current-Continuous	P <sub>B</sub>	5	5	
Total Power Dissipation at T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	140 0.8	W W/°C	
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>Stg</sub>	-65 to +200	°C	

### **Thermal Characteristics**

Characteristic	Symbol	Max.	Unit
Thermal Resistance Junction to Case	$R_{_{ heta JC}}$	1.25	°C/W

Stresses exceeding maximum ratings may damage the device. Maximum ratings are stress ratings only. Functional operation above the recommended operating conditions is not implied. Extended exposure to stresses above the recommended operating conditions may affect device reliability

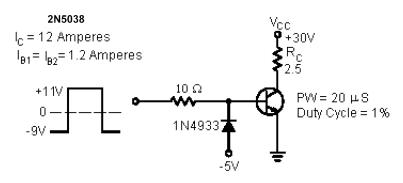
- 1. Indicates JEDEC registered data
- 2. Pulse test: pulse width ≤10ms, duty cycle ≤50%



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# **Switching Time Test Circuit**



### Electrical Characteristics (TC = 25°C unless otherwise noted)

	Characteristic	Symbol	Min.	Max.	Unit	
Off Characteristics		•		•	•	
Collector-Emitter Sustaining Voltage (Note 4) $(I_C = 200 \text{mA DC}, I_B = 0)$		V <sub>CEO (sus)</sub>	90	-	V DC	
Collector Cut off Curren ( $V_{CE}$ = 140V DC, $V_{BE}$ (or ( $V_{CE}$ = 100V DC, $V_{BE}$ (or ( $V_{CE}$ = 100V DC, $V_{BE}$ (or ( $V_{CE}$ = 100V DC)	f <sub>f)</sub> = 1.5V) f <sub>f)</sub> = 1.5V DC, T <sub>C</sub> = 150°C)		mA DC			
		I <sub>EBO</sub>	-	5 50		
On Characteristics (N	ote 4)				'	
DC Current Gain (I <sub>C</sub> = 12A DC, V <sub>CE</sub> = 5V	DC)	h <sub>FE</sub>	20	100	-	
Collector-Emitter Saturation Voltage $(I_C = 20A DC, I_B = 5A DC)$		V <sub>CE (sat)</sub>	-	2.5	V-D0	
Base-Emitter Saturation Voltage (I <sub>C</sub> = 20A DC, I <sub>B</sub> = 5A DC)		V <sub>BE (sat)</sub>	-	3.3	V DC	
Dynamic Characterist	tics					
	Emitter Small-Signal Short-Circuit Forward (I <sub>C</sub> = 2A DC, V <sub>CE</sub> = 10V DC, f = 5MHz)	h <sub>fe</sub>	12	-	-	
Switching Characterist	ics					
Resistive Load						
Rise Time	(V <sub>CC</sub> = 30V DC)	t <sub>r</sub>	-	0.5		
Storage Time	$(I_C = 12A DC, I_{B1} = I_{B2} = 1.2A DC)$	ts	-	1.5	μs	

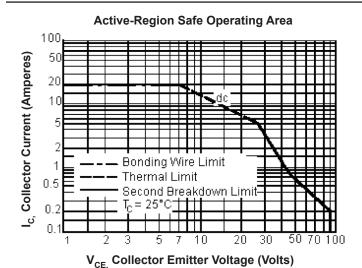
#### Note:

- 3. Indicates JEDEC Registered Data.
- 4. Pulse Test: Pulse Width ≤300µs, Duty Cycle ≤2%

multicomp

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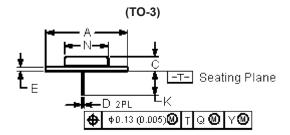


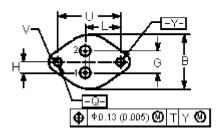


There are two limitation on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $\rm I_C\text{-}V_{CE}$  limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

Second breakdown pulse limits are valid for duty cycles to 10%. At high case temperatures, thermal limitations may reduce the power that can be handled to values less than the limitations imposed by second breakdown

#### **Dimensions**





#### **Dimensions** Min. Max. Α 1.55 (39.37) Reference В 1.05 (26.67) С 0.25 (6.35) 0.335 (8.51) D 0.038 (0.97) 0.043 (1.09) Ε 0.055 (1.4) 0.07 (1.77) G 0.43 (10.92) BSC Н 0.215 (5.46) BSC Κ 0.44 (11.18) 0.48 (12.19) L 0.665 (16.89) BSC Ν 0.83 (21.08) 0.151 (3.84) 0.165 (4.19) 1.187 (30.15) BSC V 0.131 (3.33) 0.188 (4.77)

Dimensions: Inches (Millimetres)

### **Pin Configuration:**

Pin 1. Base

2. Emitter Collector (Case)

#### **Part Number Table**

Description	Part Number	
Transistor, NPN, TO-3	2N5038	

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