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### **Descriptions**

The WAS4735Q-16/TR is a bidirectional low-power dual port, high-speed, USB 2.0 analog switch with integrated protection for USB Type-C<sup>™</sup> systems. The device is configured as a dual 4:1 or 1:4 switch. It is optimized for use with the USB 2.0 DP/DM lines in a USB Type-C<sup>™</sup> system.

The WAS4735Q-16/TR has low bit-to-bit skew and high channel-to-channel noise isolation, and is compatible with various standards, such as high-speed USB 2.0 (480Mbps). Each switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. Its bandwidth is wide enough to pass high-speed USB 2.0 differential signals (480 Mb/s) with good signal integrity.

GPIO controls of SELx are 1.8V logic compatible. The WAS4735Q-16/TR is available in QFN 2 x 2.55 with Pb-free and Halogen-free making it a perfect candidate for mobile and space constrained applications.

#### **Features**

- Low On-resistance, Ron=3Ω when VCC =5V
- 1.8V Logic Compatible Control Pin
- D+/- Overrides VCC to Achieve True Isolation Even When Supply Is Dead
- High Off-Isolation: -100dB @ 100KHz
- Low Channel-to-Channel Crosstalk: -97dB @ 100KHz
- High Bandwidth ( -3dB @800MHz) Suitable for USB2.0 High-Speed Routing
- Low Quiescent Current (<2uA) With Very Wide Supply Range (1.5V ~ 5.5V)</li>

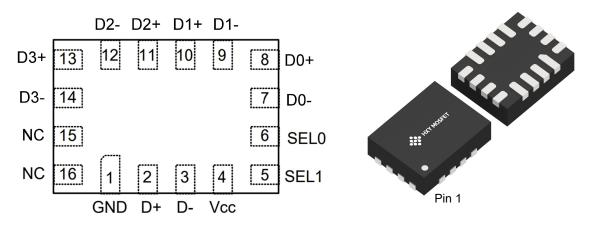
### **Applications**

- Anywhere a USB Type-C<sup>™</sup> or Micro-B Connector is Used
- Mobile Phones, Tablets and Notebooks

#### **Order Information**

Part Number	Paci	kage	Quantity Per Reel
WAS4735Q-16/TR	QFN2X2.55	Tape and Reel	3,000PCS





QFN2x2.55

## **Pin Descriptions**

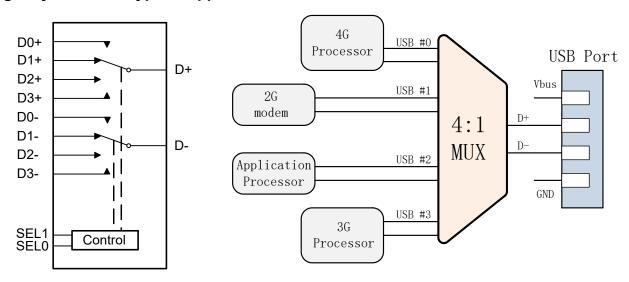
Pin Number	Symbol	Descriptions	
1	GND	Ground	
2	D+	D+ Common Port (HS or FS USB)	
3	D-	D- Common Port (HS or FS USB)	
4	VCC	Power Supply	
5	SEL1	Path Selection Control Input (See Table Below)	
6	SEL0	Path Selection Control Input (See Table Below)	
7	D0-	D- From the 1st Source Path (HS or FS USB)	
8	D0+	D+ From the 1st Source Path (HS or FS USB)	
9	D1-	D- From the 2nd Source Path (HS or FS USB)	
10	D1+	D+ From the 2nd Source Path (HS or FS USB)	
11	D2+	D+ From the 3rd Source Path (HS or FS USB)	
12	D2-	D- From the 3rd Source Path (HS or FS USB)	
13	D3+	D+ From the 4th Source Path (HS or FS USB)	
14	D3-	D- From the 4th Source Path (HS or FS USB)	
15,16	NC	No Connect	

## **Function Descriptions**

SEL1	SEL0	Function	
0	0	D+ = D0+, D- = D0-	
0	1	D+ = D1+, D- = D1-	
1	0	D+ = D2+, D- = D2-	
1	1	D+ = D3+, D- = D3-	



## **Logic Symbol and Typical Applications**



**Logic Symbol** 

**Typical Applications (Mobile Phone Example)** 

## Absolute Maximum Ratings (1)

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	-0.3 ~ 6.5	٧
Control Input Voltage	V <sub>IN</sub>	-0.3 ~ 6.5	V
Continuous Current Through Dx +/- and D+/-		±100	mA
Peak Current Through Dx +/- and D+/- (pulsed at 1ms 50% duty cycle)		±200	mA
Storage Temperature Range	T <sub>STG</sub>	-55 ~ 150	°C
Junction Temperature under Bias	Τ <sub>J</sub>	150	°С
Lead Temperature (Soldering, 10 seconds)	TL	260	°С
Power Dissipation	P <sub>D</sub>	250	mW

## Recommend Operating Ratings (2)

The definition of personing that may be			_
Parameter	Symbol	Value	Unit
Supply Voltage Operating	Vcc	1.5 ~ 5.5	V
Control Input Voltage	V <sub>IN</sub>	-0.3 ~ 5.5	V
Input Signal Voltage	Vsw	-0.3 ~ 5.5	V
Operating Temperature	T <sub>A</sub>	-40 ~ 85	°C
Thermal Resistance	$R_{\theta JA}$	360	°C/W

### Note:

- "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.
- 2. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.



## DC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

Parameter Symbol Conditi		Conditions	Min.	Тур.	Max.	Unit
Input logic high lovel	\/	VCC: 3.3 ~ 5.5V	1.6			V
Input logic high level	$V_{IH}$	VCC: 1.5 ~ 3.3V	1.4			V
Input logic low lovel	M	VCC: 3.3 ~ 5.5V			0.6	V
Input logic low level	$V_{IL}$	VCC: 1.5 ~ 3.3V			0.4	V
Supply quiescent current	Icc	I <sub>COM</sub> =0, V <sub>IN</sub> =0 or V <sub>IN</sub> =VCC			1.0	uA
Increase in L., per input	1	I <sub>COM</sub> =0, VCC=4.5V			1.0	uA
Increase in I <sub>CC</sub> per input	I <sub>CCT</sub>	V <sub>IN</sub> >1.8 or V <sub>IN</sub> <0.5			1.0	uA
Off state leakage from COM <sub>X</sub> to NC <sub>X</sub> (or NO <sub>X</sub> )	I <sub>COMx</sub>	$V_{COM} = 5.5V$ , $V_{NC(or\ NO)} = 0V$			±2.0	uA
	R <sub>ON1</sub>	V <sub>COM</sub> =0 ~ 0.5V, I <sub>COM</sub> =30mA		6.2	7.2	Ω
On-Resistance	R <sub>ON2</sub>	V <sub>COM</sub> =0.5 ~ 2.0V, I <sub>COM</sub> =30mA		7.2	7.9	Ω
On-Resistance	R <sub>ON3</sub>	V <sub>COM</sub> =2.0 ~ 4.0V, I <sub>COM</sub> =30mA		5.2	7.2	Ω
	R <sub>ON4</sub>	V <sub>COM</sub> =4.0 ~ 5.5V, I <sub>COM</sub> =30mA		3.3	3.8	Ω
	$R_{FLAT1}$	V <sub>COM</sub> =0 ~ 0.5V, I <sub>COM</sub> =30mA		1.4		Ω
On-Resistance Flatness	$R_{FLAT2}$	V <sub>COM</sub> =0.5 ~ 2.0V, I <sub>COM</sub> =30mA		1.0		Ω
On-Resistance Flatness	$R_{FLAT3}$	V <sub>COM</sub> =2.0 ~ 4.0V, I <sub>COM</sub> =30mA		3.2		Ω
	$R_{FLAT4}$	V <sub>COM</sub> =4.0 ~ 5.5V, I <sub>COM</sub> =30mA		0.6		Ω
On-Resistance Matching Between Channels	$\DeltaR_ON$	V <sub>COM</sub> =0~5.5V, I <sub>COM</sub> =30mA,		0.2	0.4	Ω

## AC Electronics Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

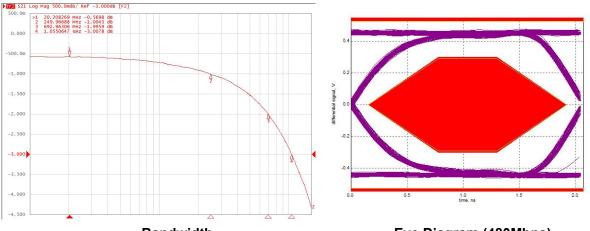
Parameter	Symbol	Ol Conditions		Тур.	Max.	Unit
Turn-On Time	$T_ON$	$V_{COM}$ =1.5V, $C_L$ =35pF, $R_L$ =50 $\Omega$		200		ns
Turn-Off Time	$T_{OFF}$	$V_{COM}$ =1.5V, $C_L$ =35pF, $R_L$ =50 $\Omega$		200		ns
Break-Before-Make time	Тввм	$V_{COM}$ =1.5V, $C_L$ =35pF, $R_L$ =50 $\Omega$		500		ns
-3dB Bandwidth	BW	$R_L=50\Omega$ , $C_L=0pF$		800		MHz
Officelation	OIDD.	F=1KHz, $R_L$ =50 $\Omega$		-81		dB
Off isolation	OIRR	$F=10KHz$ , $R_L=50\Omega$		-80		dB
Croostalle	Vtalk	F=1KHz, $R_L$ =50 $\Omega$		-83		dB
Crosstalk	Xtalk	F=10KHz, $R_L$ =50 $\Omega$		-82		dB
Total Harmonic Distortion	THD	F=20Hz to 20KHz		-80		dB
Total Harmonic Distortion	וחט	$V_{COM}$ =600m $V$ p-p @ $R_L$ =32 $\Omega$ ,		-60		uБ

### Capacitance (Ta=25°C, VCC=3.3V, unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Off capacitance	$C_{OFF}$	F=100KHz		5		pF
On capacitance	C <sub>ON</sub>	F=100KHz		7		pF

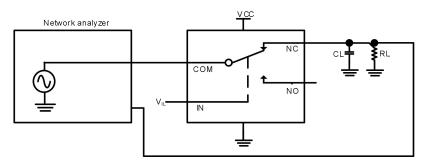


## Typical Characteristics (Ta=25°C, VCC=3.3V, unless otherwise noted)

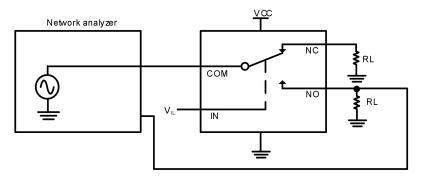


Bandwidth

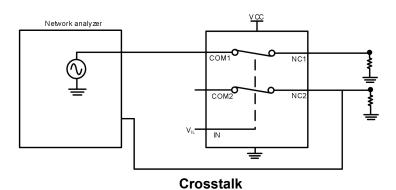
Eye Diagram (480Mbps)



**Bandwidth** 



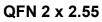
Off isolation

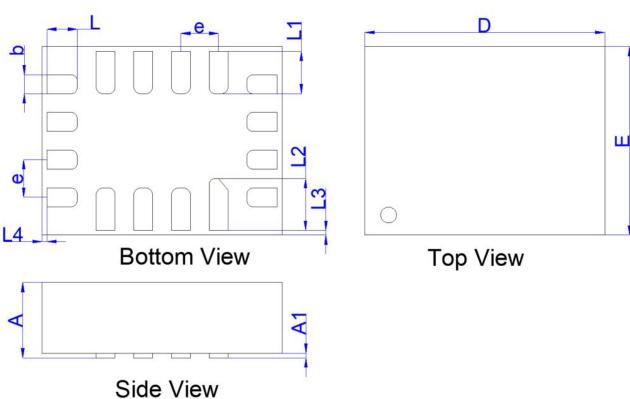


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# Package outline dimensions





Symbol		Dimension in Millime	ters
Symbol	Min.	Тур.	Max.
А	0.700	-	0.800
A1	0.025	-	0.075
D	2.500	2.550	2.600
E	1.950	2.000	2.050
b	0.150	0.200	0.250
L	0.275	0.325	0.375
L1	0.400	0.450	0.500
L2	0.500	0.550	0.600
L3	0.010	0.050	0.090
L4	0.010	0.050	0.090
е		0.400BSC	



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