

Description

The CJX3134K is a dual N-channel enhanced MOS field-effect transistor. Uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. Device is suitable for use in DC-DC conversion, power switch and charging circuit. Standard Product CJX3134K is Pb-free.

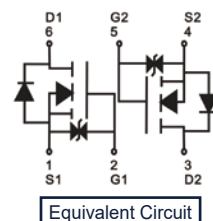
General Features

- Surface Mount Package
- N-Channel Switch with Low $R_{DS(on)}$
- Operated at Low Logic Level Gate Drive
- Equivalent to Two CJ3134K.



Applications

- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable Electronics
- Logic Level Shift



Ordering information

Product ID	Pack	Naming rule	Marking	Qty(PCS)
CJX3134K	SOT-563	CJX3134K 产品名称 product name	34K	3000

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	± 12	V
I_D	Continuous drain current ($t \leq 10s$)	0.75	A
P_D	Power Dissipation(note1)	0.15	W
$R_{\theta JA}$	Thermal Resistance from Junction to Ambient	833	$^{\circ}\text{C}/\text{W}$
T_J	Junction temperature	150	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-55~+150	$^{\circ}\text{C}$

Electrical Characteristics (T_A=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
STATIC PARAMETERS						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250μA	20	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =20V,V _{GS} =0V	---	---	1	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±10V , V _{DS} =0V	---	---	±20	μA
V _{GS(th)}	Gate Threshold Voltage (note 2)	V _{GS} =V _{DS} , I _D =250μA	0.35	0.54	1.1	V
R _{DS(ON)}	Drain-source on-resistance (note 2)	V _{GS} =4.5V, I _D =0.65A	---	270	380	mΩ
		V _{GS} =2.5V,I _D =0.55A	---	320	450	
		V _{GS} =1.8V , I _D =0.45A	---	390	800	
g _{fs}	Forward Transconductance (note 2)	V _{DS} =10V , I _D =0.8A	---	1.6	---	S
V _{SD}	Diode Forward Voltage (note 2)	I _S =0.15A,V _{GS} =0V	---	---	1.2	V
DYNAMIC PARAMETERS (note 3)						
C _{iss}	Input Capacitance	V _{DS} =16V , V _{GS} =0V , f=1MHz	---	79	120	pF
C _{oss}	Output Capacitance		---	13	20	
C _{rss}	Reverse Transfer Capacitance		---	9	15	
SWITCHING PARAMETERS (note 3)						
T _{d(on)}	Turn-On Delay Time	V _{GS} =4.5V,V _{DS} =10V, I _D =0.5A,R _{GEN} =10Ω	---	6.7	---	ns
T _r	Rise Time		---	4.8	---	
T _{d(off)}	Turn-Off Delay Time		---	17.3	---	
T _f	Fall Time		---	7.4	---	
Q _g	Total Gate Charge	V _{DS} =10V,V _{GS} =4.5V,I _D =7A	---	20	---	nC
Q _{gs}	Gate-Source Charge		---	1	---	
Q _{gd}	Gate-Drain Charge		---	4	---	

Notes :

1. Repetitive rating : Pulse width limited by junction temperature.
2. Pulse Test : Pulse width≤300μs, duty cycle≤0.5%.
3. Guaranteed by design, not subject to production testing.



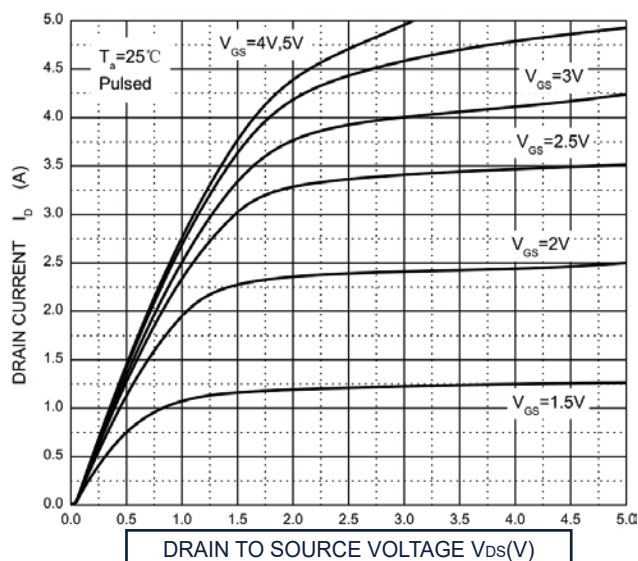
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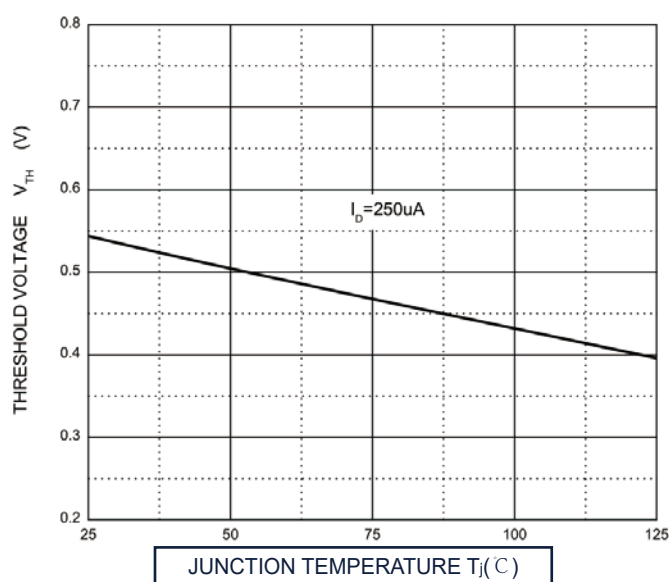
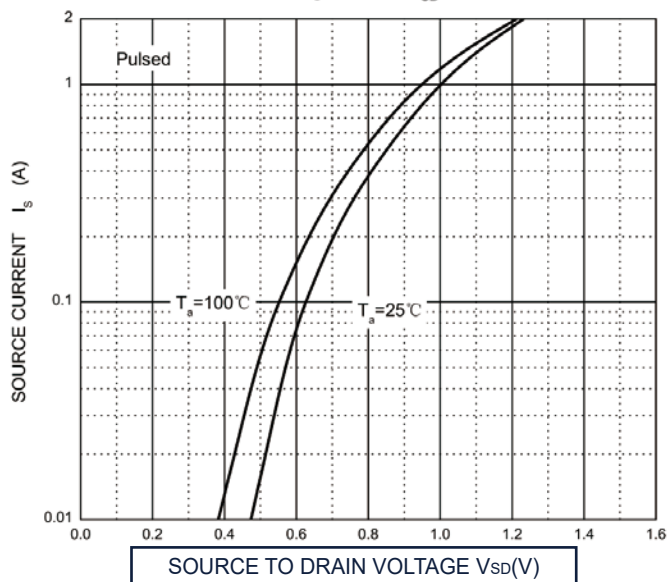
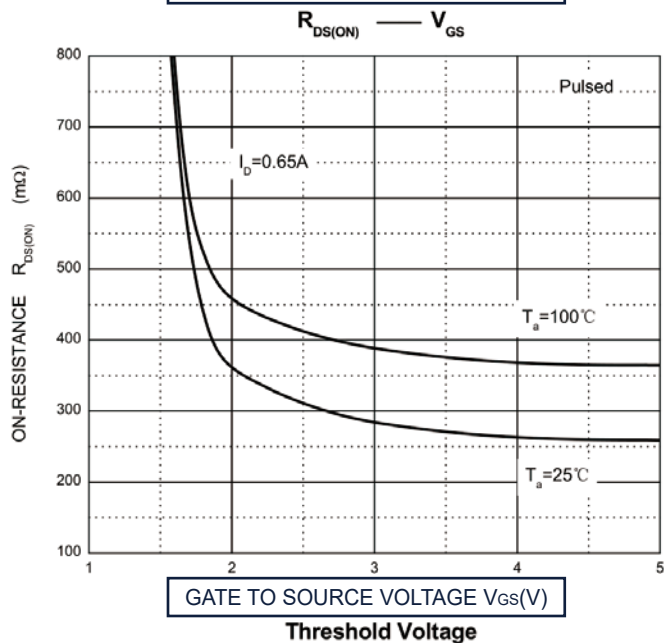
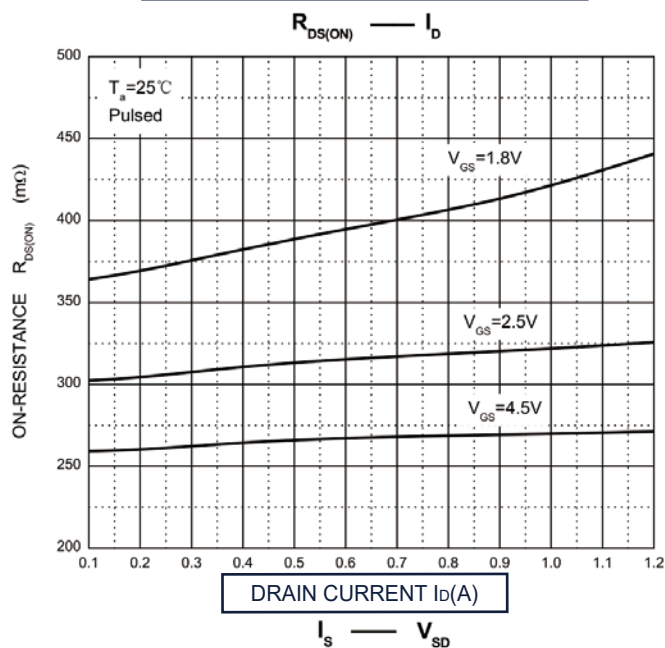
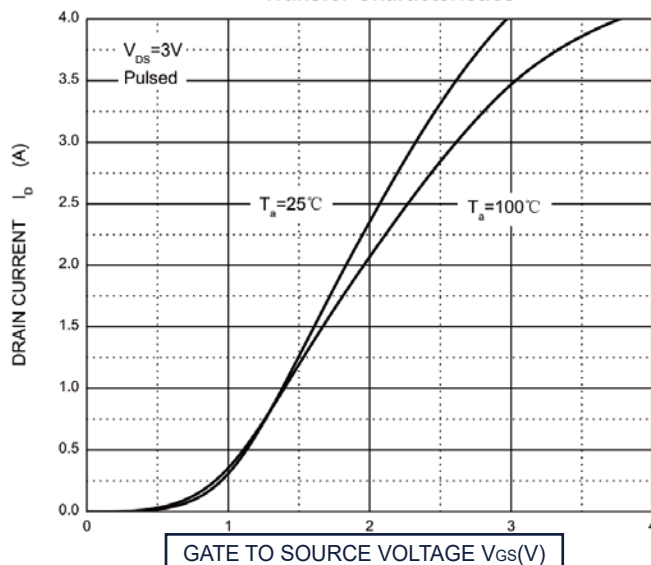
SOT-563 20V Dual N-Channel MOSFET

Typical Characteristics

Output Characteristics

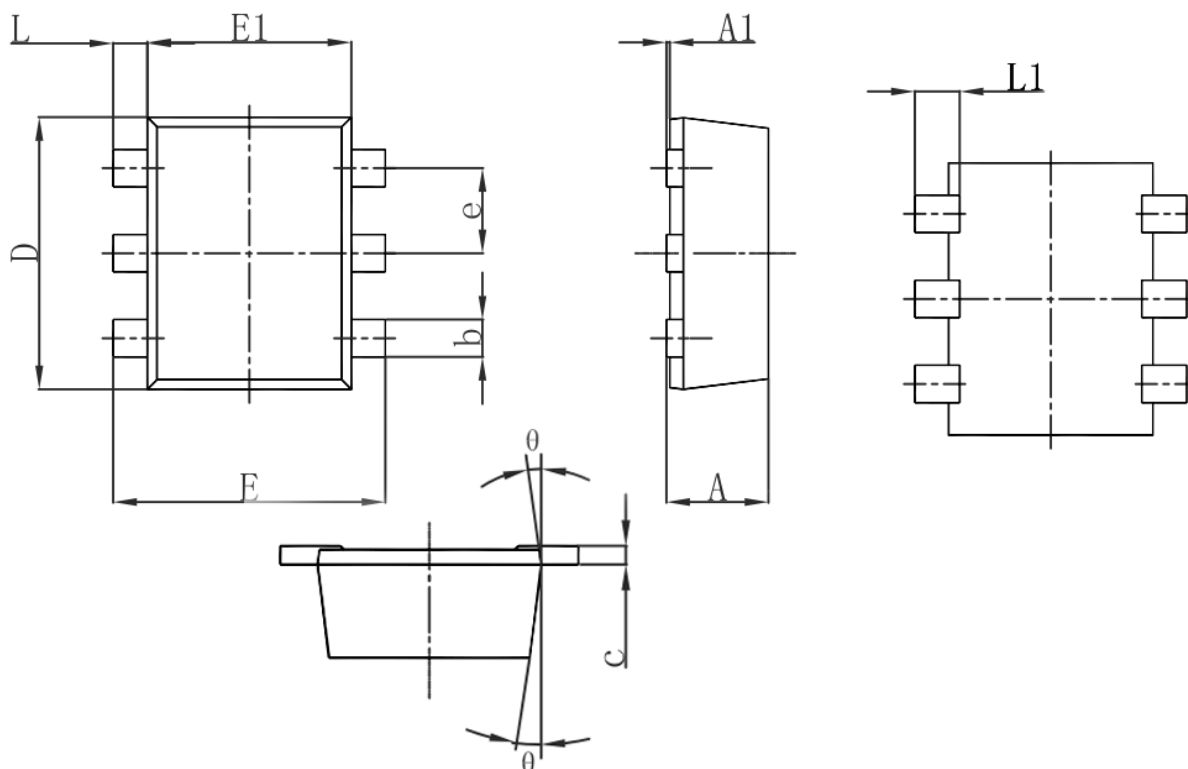


Transfer Characteristics





SOT-563 Package Outline Dimensions



Symbol	Dimensions in Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.525	0.600	0.021	0.024
A1	0.000	0.050	0.000	0.002
e	0.450	0.550	0.018	0.022
c	0.090	0.160	0.004	0.06
D	1.500	1.700	0.059	0.067
b	0.170	0.270	0.007	0.011
E1	1.100	1.300	0.043	0.051
E	1.500	1.700	0.059	0.067
L	0.100	0.300	0.004	0.012
L1	0.200	0.400	0.008	0.016
θ	10° REF.		10° REF.	