

## Description

The SX30N15HD uses advanced technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 10V. This device is suitable for use as a Battery protection or in other Switching application.

## General Features

$V_{DS} = 150V$   $I_D = 30A$

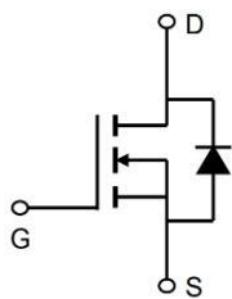
$R_{DS(ON)} < 78m\Omega @ V_{GS}=10V$

## Application

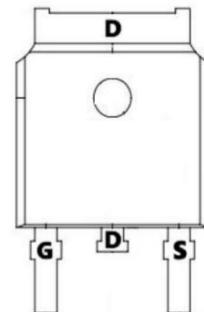
Automotive lighting

Load switch

Uninterruptible power supply



TO-252-3L



## Absolute Maximum Ratings (TC=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	150	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D @ T_c=25^\circ C$	Drain Current, $V_{GS} @ 10V$	30	A
$I_D @ T_c=100^\circ C$	Drain Current, $V_{GS} @ 10V$	21	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	90	A
$P_D @ T_c=25^\circ C$	Total Power Dissipation	60	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Maximum Thermal Resistance, Junctionambient	62.5	°C/W
$R_{\theta JC}$	Maximum Thermal Resistance, Junction-case	2.5	°C/W

**Electrical Characteristics@T<sub>j</sub>=25°C(unless otherwise specified)**

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	150	175	-	V
I <sub>GSS</sub>	Gate-body Leakage current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	V <sub>DS</sub> = 150V, V <sub>GS</sub> = 0V	-	-	1	uA
I <sub>DSS</sub>	Zero Gate Voltage Drain Current T <sub>J</sub> = 100°C		-	-	100	uA
V <sub>GS(th)</sub>	Gate-Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	3.0	4.5	V
R <sub>D(on)</sub>	Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A	-	63	78	mΩ
R <sub>D(on)</sub>	Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 8A		72	90	
g <sub>fs</sub>	Transconductance	V <sub>DS</sub> = 5V, I <sub>D</sub> = 10A	-	23	-	S
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 75V, V <sub>GS</sub> = 0V, f = 1MHz	-	630	-	pF
C <sub>oss</sub>	Output Capacitance		-	50	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	13.5	-	
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> = 0V, V <sub>DS</sub> Open, f = 1MHz	-	5	-	Ω
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 75V, I <sub>D</sub> = 10A	-	11	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	1.2	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	4	-	
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>GS</sub> = 10V, V <sub>DD</sub> = 75V, R <sub>G</sub> = 10Ω, I <sub>D</sub> = 10A	-	9.8	-	nS
t <sub>r</sub>	Rise Time		-	6	-	
t <sub>d(off)</sub>	Turn-Off Delay Time		-	15	-	
t <sub>f</sub>	Fall Time		-	4.1	-	
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	I <sub>S</sub> = 10A, V <sub>GS</sub> = 0V	-	-	1.2	V
t <sub>rr</sub>	Body Diode Reverse Recovery Time	V <sub>R</sub> = 75V, I <sub>F</sub> = 10A, dI/dt = 100A/μs	-	55	-	nS
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge		-	124	-	

**Note :**

- 1、The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
- 2、The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3、The EAS data shows Max. rating . The test condition is VDD=72V,VGS=10V,L=0.1mH,IAS=13A
- 4、The power dissipation is limited by 150°C junction temperature
- 5、The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

## Typical Characteristics

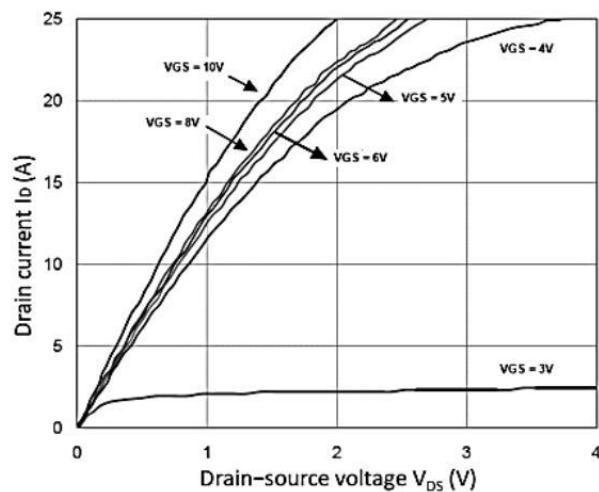


Figure 1. Output Characteristics

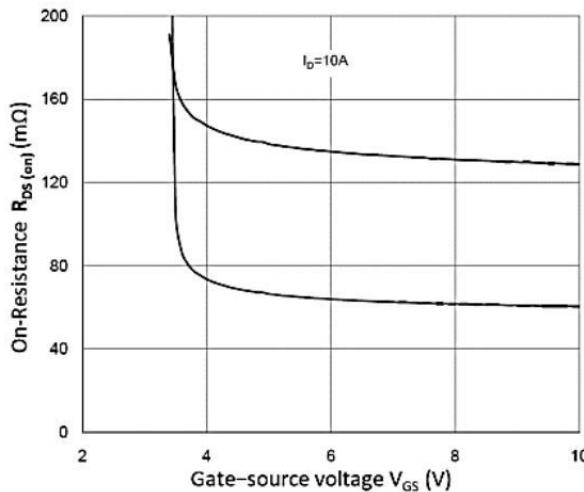


Figure 2.  $R_{DS(on)}$  vs.  $V_{GS}$

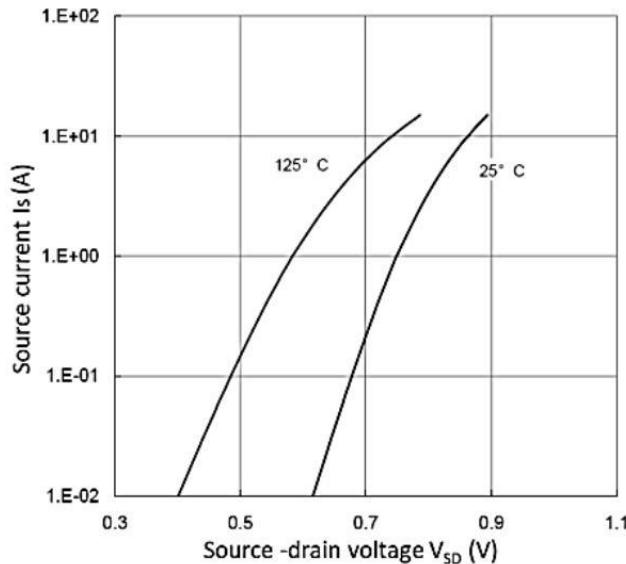


Figure 3. Forward Characteristics of Reverse

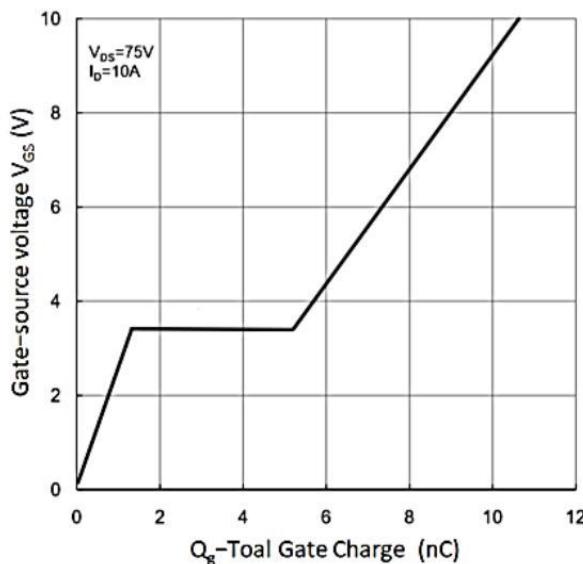


Figure 4. Gate Charge Characteristics

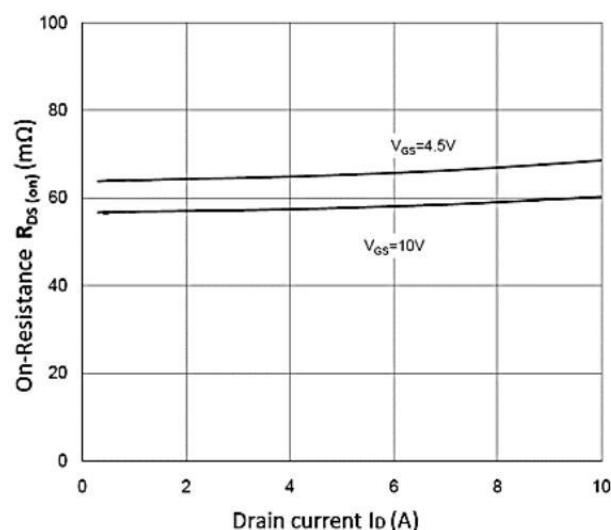


Figure 5.  $R_{DS(ON)}$  vs.  $I_D$

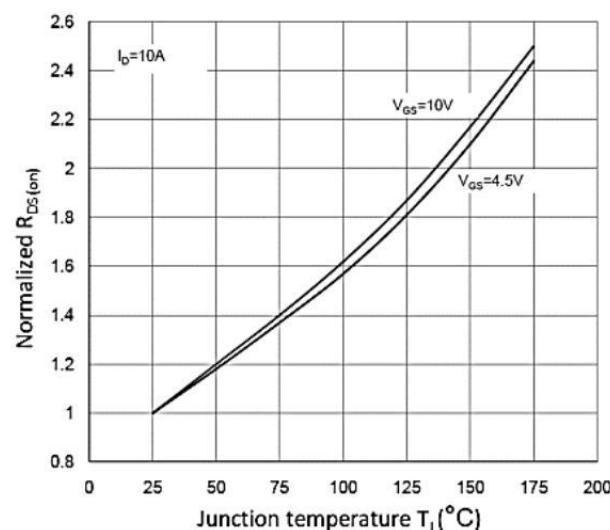


Figure 6. Normalized  $R_{DS(on)}$  vs.  $T_J$

## Typical Characteristics

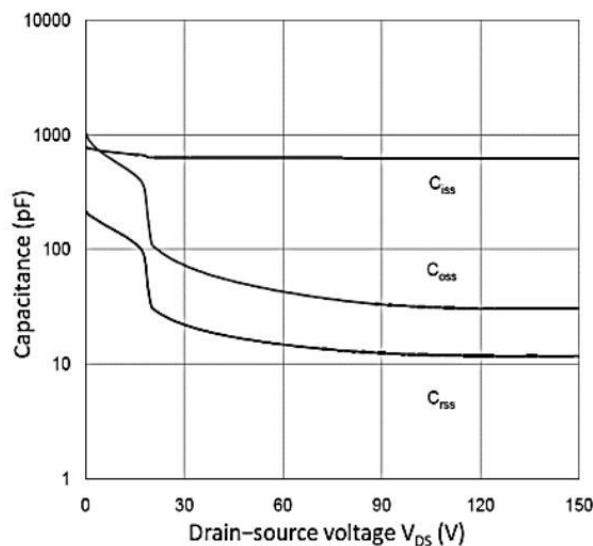


Figure 7. Capacitance Characteristics

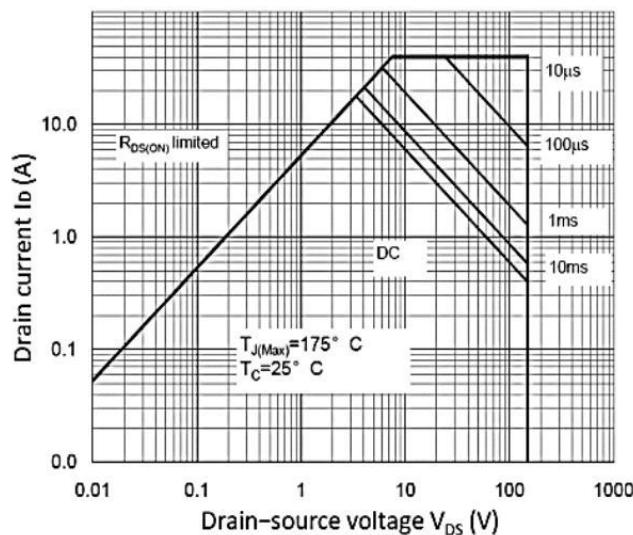


Figure 8. Safe Operating Area

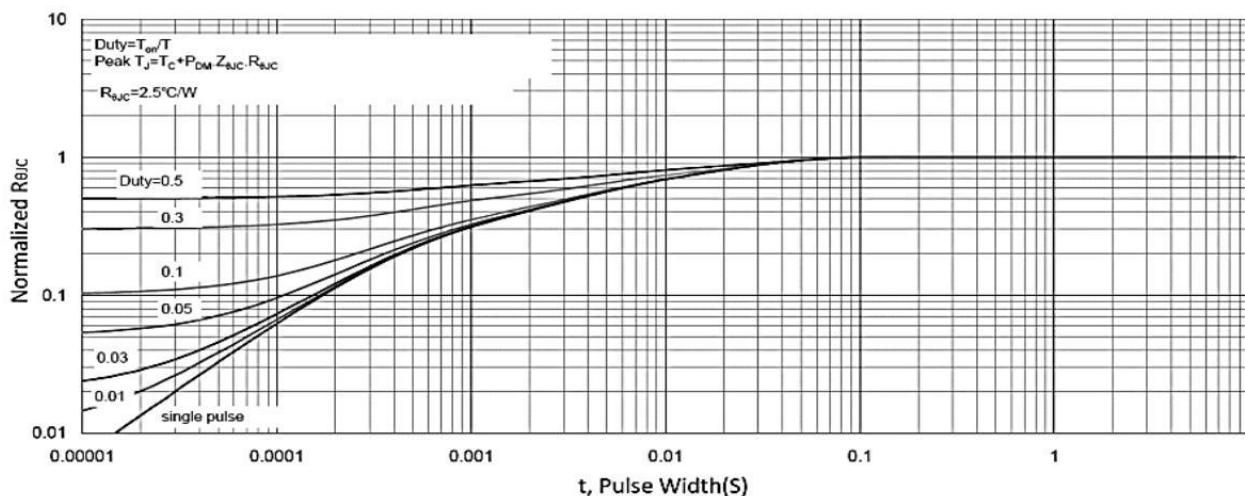


Figure 9. Normalized Maximum Transient Thermal Impedance

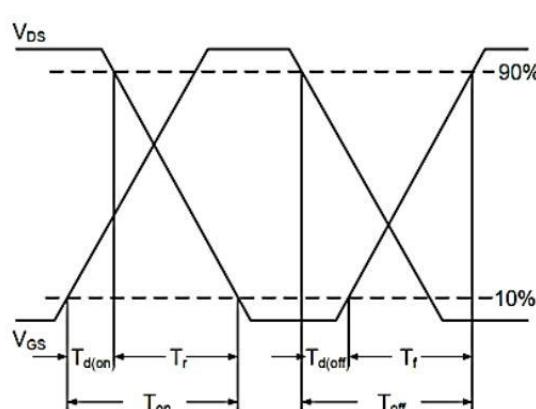


Figure 10. Switching Time Waveform

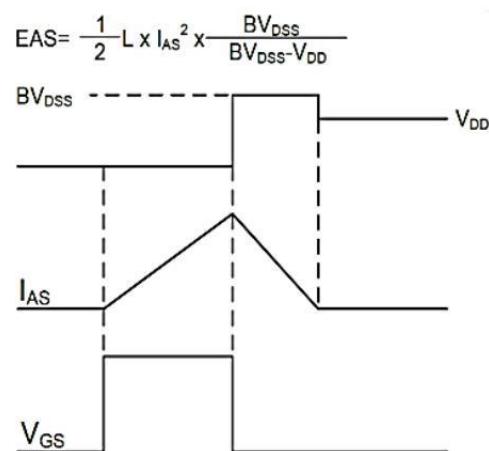
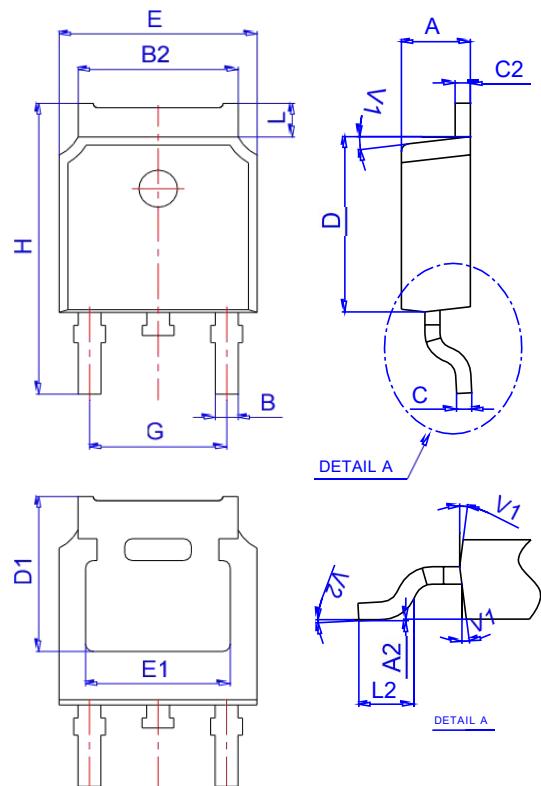


Figure 11. Unclamped Inductive Switching

## Package Mechanical Data: TO-252-3L



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

### Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)
TAPING	TO-252-3L		2500