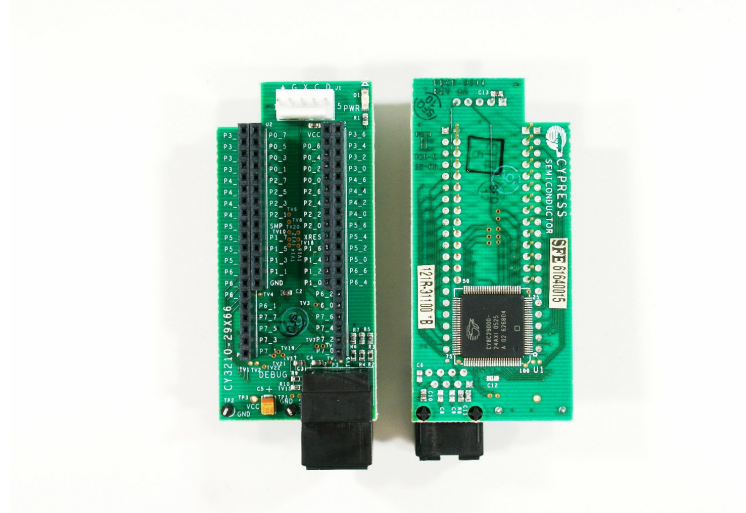


CY3210 - CY8C29x66 PSoC(R) Evaluation Pod (EvalPod)

Last Updated: Jan 25, 2018



CY3210-29x66 Kit Contents:

- One (1) CY8C29x66 PSoC EvalPod

Hardware Description

PSoC EvalPods are pods that connect to the ICE In-Circuit Emulator (CY3215-DK kit) to allow debugging capability. They can also function as a standalone device without debugging capability. The EvalPod has a 28-pin DIP footprint on the bottom for easy connection to development kits or other hardware. The top of the EvalPod has prototyping headers for easy connection to the devices pins. CY3210-29x66 provides evaluation of the CY8C29x66 PSoC device family.

Related Files

File Title	Language	Size	Last Updated
CY3210-29x66 EvalPod Gerber Files.pdf	English	281.42 KB	09/24/2008
CY3210-29x66 EvalPod Schematic.pdf	English	211.41 KB	09/24/2008
CY3210-29x66 EvalPod BOM.xls	English	63.5 KB	09/24/2008

Need help? Ask a question and find answers in the [Cypress Developer Community Forums](#).

Low/intermittent bandwidth users tip: Firefox and Chrome browsers will allow downloads to be resumed if your connection is lost during download.

Related Resources

Application Notes

AN2223 - PSoC® 1 - Approximating an Opamp with a Switched Capacitor Integrator	08/18/2015
AN2168 - PSoC® 1 - Understanding Switched Capacitor Filters	05/24/2018
AN2104 - PSoC® 1 - Dynamic Reconfiguration with PSoC Designer™	04/28/2017

Development Kits/Boards

PSoC® 1 Kits	08/29/2018
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RELATED RESOURCES

[Application Notes \(3\)](#)
[Development Kits/Boards \(1\)](#)

RELATED PAGES

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[PSoC Designer](#)

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