















**ESD** 

TVS

MOS

LDO

Diode

Sensor

DC-DC

# **Product Specification**

Domestic Part Number	IRLL2703
<ul><li>Overseas Part Number</li></ul>	IRLL2703
▶ Equivalent Part Number	IRLL2703





- ★ Green Device Available
- ★ Super Low Gate Charge

Description

- ★ Excellent Cdv/dt effect decline
- ★ Advanced high cell density Trench technology

## teermology

# The IRLL2703 is the high cell density trenched N-ch MOSFETs, which provides excellent RDSON and efficiency for most of the small power switching and load switch applications.

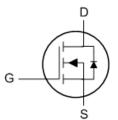
The IRLL2703 meet the RoHS and Green Product requirement with full function reliability approved.

#### **Product Summary**

BVDSS	RDSON	ID
30V	28mΩ	5.8A

#### **SOT223 Pin Configuration**





#### **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
V <sub>DS</sub>	Drain-Source Voltage	30	V	
V <sub>G</sub> s	Gate-Source Voltage	±20	V	
ID@TA=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	5.8	Α	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	4.7	А	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	30	Α	
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>3</sup>	1.5	W	
T <sub>STG</sub>	Storage Temperature Range -55 to 150		°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>		85	°C/W
R <sub>0</sub> JC	Thermal Resistance Junction-Case <sup>1</sup>		48	°C/W



#### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.025		V/°C
Dancer	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =5A		24	28	mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =4A		34	40	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V V I- 2500A	1.2	1.5	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-4.8		mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			5	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =5A		7		S
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.5	5	Ω
Qg	Total Gate Charge (4.5V)	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		6	8.4	
$Q_gs$	Gate-Source Charge			2.5	3.5	nC
$Q_{gd}$	Gate-Drain Charge			2.1	2.9	
T <sub>d(on)</sub>	Turn-On Delay Time			2.4	4.8	
Tr	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$		7.8	14	
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =5A		22	44	ns
Tf	Fall Time			4	8	
Ciss	Input Capacitance			572	800	
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		81	112	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			65	91	

#### **Diode Characteristics**

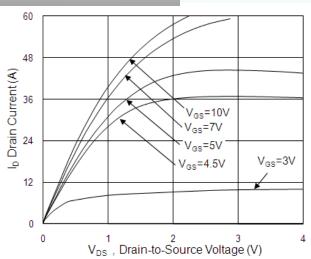
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,4</sup>	V- V- OV Force Current			5.8	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			30	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V
t <sub>rr</sub>	Reverse Recovery Time			19		nS
Qrr	Reverse Recovery Charge	IF=5A , dI/dt=100A/ $\mu$ s , T $_{J}$ =25 $^{\circ}$ C		1.04		nC

#### Note

- 1. The data tested by surface mounted on a 1 inch $^2\,\text{FR-4}$  board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\,\leq\,300\text{us}$  , duty cycle  $\,\leq\,2\%$
- 3.The power dissipation is limited by 150  $^{\circ}$ C junction temperature
- 4. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



#### **Typical Characteristics**



#### Fig.1 Typical Output Characteristics

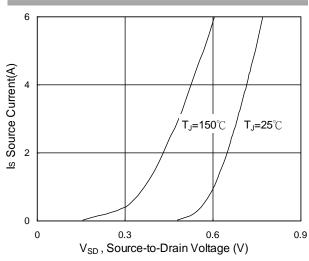


Fig.3 Forward Characteristics of Reverse

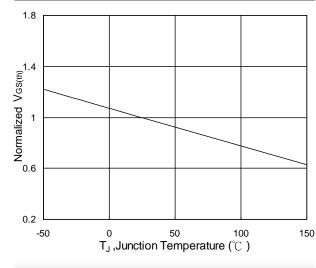


Fig.5 Normalized  $V_{\text{GS(th)}}$  vs.  $T_{\text{J}}$ 

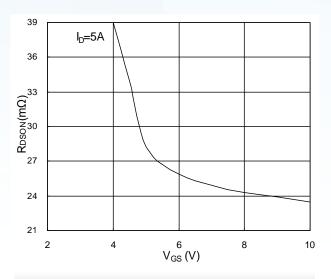


Fig.2 On-Resistance vs. Gate-Source

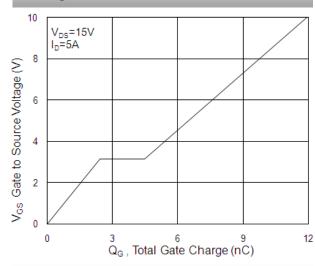


Fig.4 Gate-Charge Characteristics

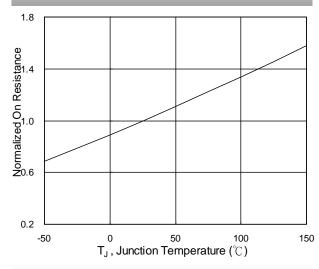
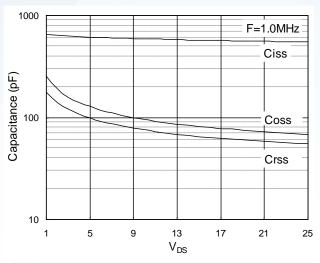
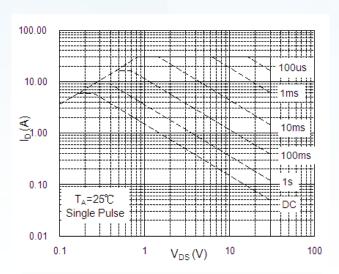
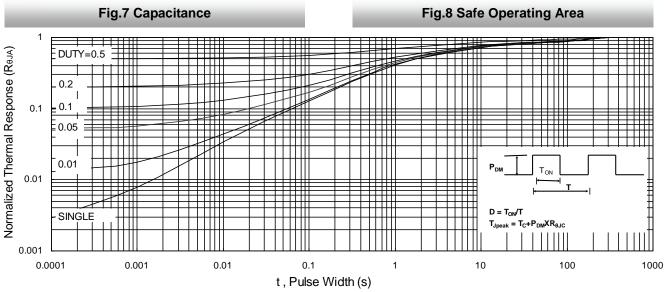


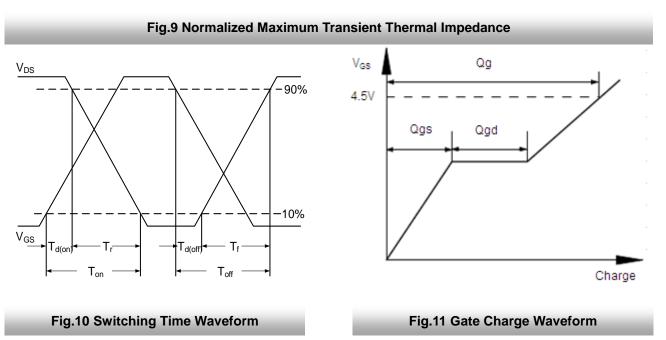
Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>













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