



Typical Unit

Output Voltage (V)	Output Current (A)	Input Voltage Range (V)
12	12.5	16-160
24	6.25	16-160
54	2.80	16-160

Optimized for harsh environments in industrial/railway applications, the IRQ-W80 DC-DC converter series offer regulated outputs in an industry-standard quarter brick fully encased package.

FEATURES

- Efficiency up to 89% @ 72Vin, 12Vout
- Ultra-wide input range: 16V-160V
- Output voltage: 12V, 24V or 54V
- Vout trim
- Output power 150W
- Quarter-Brick Package: 61.21x39.62x13 mm 2.41x1.56x0.51 inches
- OVP, OCP, OTP
- Positive or Negative Remote ON/OFF
- Operating Baseplate Temperature range -40°C to +100°C
- 4242VDC input to output isolation, reinforced
- Hold Up Time (10-30mS, with external Cap)
- UVLO Set up (resistor programmable)
- Meets requirements for EN50155

PRODUCT OVERVIEW

The IRQ-W80 series of isolated, regulated converter modules deliver an impressive 150W output power from an ultra-wide 10:1 input voltage range, complying with the 24V to 110V input battery voltages including transients as per EN50155 (2017) standard. The converter comes in a fully encased industry standard quarter brick package offering astonishing efficiencies. The fully isolated (4242Vdc) IRQ-W80 series features a 16 to 160 Volt DC input voltage range. Typical applications include industrial, railway and transportation.

The IRQ-W80 is an isolated power converter and fixed frequency operations means excellent efficiencies of up to 89%. A wealth of electronic protection features include input under voltage lockout, output over voltage protection, output current limit, short circuit hiccup, Vout overshoot, and over temperature shutdown.

The IRQ-W80 series is designed to meet all UL and IEC emissions and safety certifications.

SAFETY FEATURES

- Reinforced insulation
- UL/IEC 60950-1, 2nd Edition
- CAN/CSA C22.2 60950-1, 2nd edition
- RoHS compliant



Encapsulated Quarter-Brick

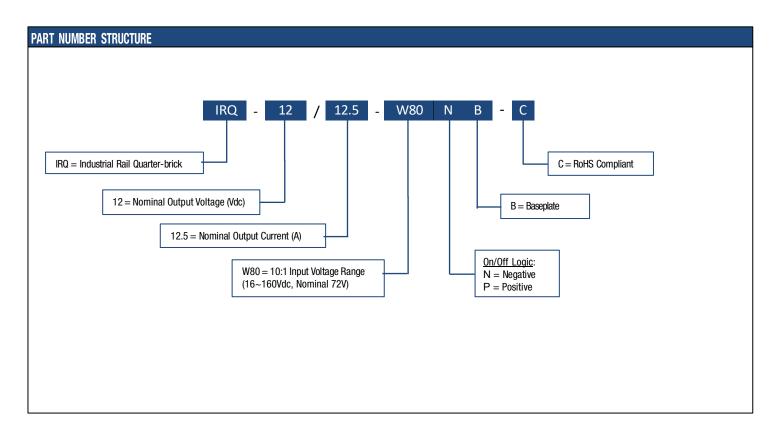




PERFORMANCE SPEC	PERFORMANCE SPECIFICATIONS SUMMARY AND ORDERING GUIDE 12												
				Out	put					Input			
Root Model ¹	Vout	lout (A,	Power	Ripple 8 (mV)	Noise ok-pk)	Regulation [3] (max.)		Vin Nom.		lin, no load	lin, full load	Efficiency	Package
	(V)	max.)	(W)	Тур.	Max.	Line	Load	(V)	(V)	(mA)	(mA) (A)	Тур.	Case (mm)
IRQ-12/12.5-W80	12	12.5	150	100	160	±0.5%	±0.5%	72	16-160	60	12	89%	61.21 x 39.62 x 13
IRQ-24/6.25-W80	24	6.25	150	100	200	±0.25%	±0.25%	72	16-160	70	12	89%	61.21 x 39.62 x 13
IRQ-54/2.8-W80	54	2.80	150	400	500	±0.25%	±0.25%	72	16-160	70	12	89%	61.21 x 39.62 x 13

Notes:

³ Regulation specifications describe output voltage deviations from a nominal/midpoint value to either extreme (50% load step).



Click here to view DCAN-71, User Guide for the MP-QW80EVAL-01 Evaluation Board.

¹ Refer to the Part Number Structure when ordering.

² All specifications are at nominal line voltage and full load, +25°C unless otherwise noted. See detailed specifications. Output capacitors are 1μF ceramic multilayer in parallel with 10μF. I/O caps are necessary for our test equipment and may not be needed for your application.



FUNCTIONAL SPECIFICATIONS: IRQ-12/12.5-W80

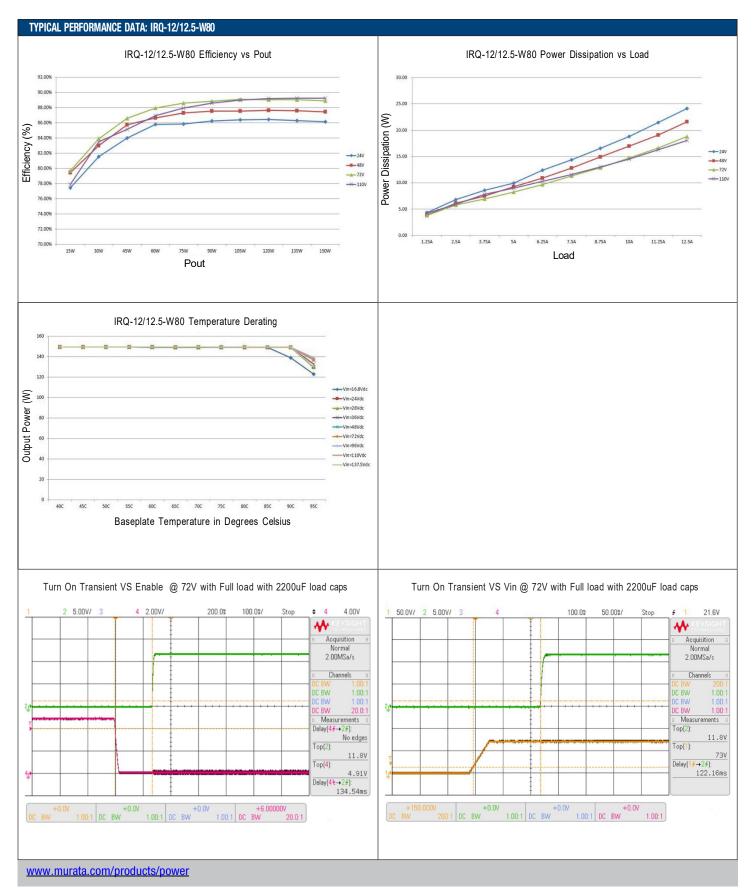
ABSOLUTE MAXIMUM RATINGS	NOTES AND CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input Voltage					
Non-Operating	Continuous	0		175	Vdc
Operating	Continuous	16		160	Vdc
Transient Operating	100mS	10		170	Vdc
Operating Ambient Temperature	Toomo	-40		85	°C
_ : _ : _ : _ : _ : _ : _ : _ : _ : _ :		-40		100	°C
Operating Baseplate Temperature					_
Storage Temperature		-55		125	°C
Altitude				3000	m
Input/Output Isolation Voltage				4242	Vdc
Voltage at ON/OFF input pin		0		15	Vdc
General conditions for device under Test unl Ambient temperature +25°C; Vin typical; Vo	less otherwise specified: ut nominal load; Encapsulated Package; With 1µF&10µF capacitors across output pins				
INPUT CHARACTERISTICS					
Operating Input Voltage Range		16	72	160	Vdc
Input Under-Voltage Lockout					1
Turn-On Voltage Threshold		14.0	15.0	16.0	Vdc
Turn-Off Voltage Threshold		11.0	12.0	13.0	Vdc
Lockout Voltage Hysteresis		11.0	3	13.0	Vdc
	Full Load Vin 10V		3	10	
Maximum Input Current	Full Load, Vin=16V		40	12	Α
No-Load Input Current	Vin=72V		40	60	mA
Disabled Input Current (N suffix)			10	15	mA
Disabled Input Current (Blank suffix)			10	15	mA
Input Reflected Ripple Current	RMS thru 220μF/250V, 12μF across source, 33μF/250V external capacitors across input pins		100	150	mA
Input Terminal Ripple Current	RMS, 20MHz bandwidth		4.5	5	Α
Recommended Input Fuse	See Technical Notes				Α
Recommended External Input Capacitance	See Figure 4 in the Technical Notes.		220		μF
Recommended BUS Capacitance	2pcs 120μF/250Vdc and MPN is EKXJ251ELL121ML25S. See Figure 4.		240		μF
Inrush Current (I²t)	Zpos 125µ1/250140 and with 10 Ethio251 EEE 121ME250. 000 Figure 4.		25	30	A ² S
OUTPUT CHARACTERISTICS			20	30	NO
Total Output Power	See Derating		150	150	W
Output Voltage Set Point	Vin=Nominal, lo=0A, Ta=25°C	11.88	12	12.12	Vdc
	VIII—NOTHINAL, 10—0A, 14—23 G	11.00	12	12.12	VUC
Output Voltage Regulation	View 70V In the sea Min to Many		0.5		0/
Over Load	Vin=72V, lout from Min to Max		±0.5		%
Over Line	lout=Full load, Vin from Min to Max		±0.5		%
Over Temperature	Vin=72V, Ta=-40°Cto 85°C		±0.004	±0.01	mV
Total Output Voltage Range	Over sample, line, load, temperature & life	11.64		12.36	Vdc
Output Voltage Ripple and Noise	20MHz bandwidth				
Peak-to-Peak	Full and 1 coversia 10 toutelum				mVp-p
	Full Load, 1µF ceramic, 10µF tantalum		100	160	
RMS	Full Load, 1µF ceramic, 10µF tantalum Full Load, 1µF ceramic, 10µF tantalum		100 30	160 50	mVrms
RMS Operating Output Current Range		0			
		0 14	30	50	mVrms
Operating Output Current Range	Full Load, 1μF ceramic, 10μF tantalum		30 12.5	50 12.5	mVrms A
Operating Output Current Range Output DC Current-Limit Inception	Full Load, 1μF ceramic, 10μF tantalum Output Voltage 10% Low	14	30 12.5	50 12.5 22	mVrms A A
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout:	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo)	14 0	30 12.5 18	50 12.5 22 2200	mVrms A A
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF	14 0	30 12.5 18	50 12.5 22 2200	mVrms A A µF
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: 24Vin: Ruvlo = open	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON	14 0 11.0 14.0	30 12.5 18 12.0 15.0	50 12.5 22 2200 13.0 16.0	mVrms A A µF Vdc Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout:	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF	14 0 11.0 14.0 19.5	30 12.5 18 12.0 15.0 20.5	50 12.5 22 2200 13.0 16.0 21.5	mVrms A A µF Vdc Vdc Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: 24Vin: Ruvlo = open	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF Vin_ON	14 0 11.0 14.0 19.5 22.5	30 12.5 18 12.0 15.0 20.5 23.5	50 12.5 22 2200 13.0 16.0 21.5 24.5	mVrms A A µF Vdc Vdc Vdc Vdc Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: 24Vin: Ruvlo = open	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF	14 0 11.0 14.0 19.5 22.5 28.0	12.0 15.0 15.0 20.5 23.5 29.0	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: 24Vin : Ruvlo = open 36Vin : Ruvlo = 20.5kΩ	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON	14 0 11.0 14.0 19.5 22.5 28.0 31.0	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: 24Vin : Ruvlo = open 36Vin : Ruvlo = 20.5kΩ	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF	11.0 11.0 14.0 19.5 22.5 28.0 31.0 41.5	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vd
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24Vin: Ruvlo = open$ $36Vin: Ruvlo = 20.5k\Omega$ $48Vin: Ruvlo = 10.7k\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON Vin_OFF Vin_ON	11.0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vd
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24Vin: Ruvlo = open$ $36Vin: Ruvlo = 20.5k\Omega$ $48Vin: Ruvlo = 10.7k\Omega$ $72Vin: Ruvlo = 5.9k\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF	11.0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5 58.5	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0 60.5	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5 62.5	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vd
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24Vin: Ruvlo = open$ $36Vin: Ruvlo = 20.5k\Omega$ $48Vin: Ruvlo = 10.7k\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON	14 0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5 58.5 61.8	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0 60.5 63.8	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5 62.5 65.8	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vd
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24Vin: Ruvlo = open$ $36Vin: Ruvlo = 20.5k\Omega$ $48Vin: Ruvlo = 10.7k\Omega$ $72Vin: Ruvlo = 5.9k\Omega$ $96Vin: Ruvlo = 3.74k\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF	14 0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5 58.5 61.8 62.3	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0 60.5 63.8 64.3	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5 62.5 65.8 66.3	mVrms A A µF Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24Vin: Ruvlo = open$ $36Vin: Ruvlo = 20.5k\Omega$ $48Vin: Ruvlo = 10.7k\Omega$ $72Vin: Ruvlo = 5.9k\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON	14 0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5 58.5 61.8	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0 60.5 63.8	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5 62.5 65.8	mVrms A A µF Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vdc Vd
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24Vin: Ruvlo = open$ $36Vin: Ruvlo = 20.5k\Omega$ $48Vin: Ruvlo = 10.7k\Omega$ $72Vin: Ruvlo = 5.9k\Omega$ $96Vin: Ruvlo = 3.74k\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF	14 0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5 58.5 61.8 62.3	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0 60.5 63.8 64.3	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5 62.5 65.8 66.3	mVrms A A µF Vdc
Operating Output Current Range Output DC Current-Limit Inception Output Capacitance Programmable Undervoltage Lockout: $24 \text{Vin}: \text{Ruvlo} = \text{open}$ $36 \text{Vin}: \text{Ruvlo} = 20.5 \text{k}\Omega$ $48 \text{Vin}: \text{Ruvlo} = 10.7 \text{k}\Omega$ $72 \text{Vin}: \text{Ruvlo} = 5.9 \text{k}\Omega$ $96 \text{Vin}: \text{Ruvlo} = 3.74 \text{k}\Omega$ $110 \text{Vin}: \text{Ruvlo} = 3.48 \text{k}\Omega$	Full Load, 1µF ceramic, 10µF tantalum Output Voltage 10% Low Nominal Vout at full load (CR load) (via R uvlo) Vin_OFF Vin_ON Vin_OFF	14 0 11.0 14.0 19.5 22.5 28.0 31.0 41.5 44.5 58.5 61.8 62.3	30 12.5 18 12.0 15.0 20.5 23.5 29.0 32.0 43.0 46.0 60.5 63.8 64.3	50 12.5 22 2200 13.0 16.0 21.5 24.5 30.0 33.0 44.5 47.5 62.5 65.8 66.3	mVrms A A µF Vdc



FUNCTIONAL SPECIFICATIONS: IRQ-12/12.5-W80

DYNAMIC CHARACTERISTICS					
Output Voltage During Load Current Transient					
Output voltage During Load Outfort Transient					
Step Change in Output Current (0.1A/uS)	50% to 75% to 50% lout max, 1uF+10uF load cap		250	400	mV
Settle Time	To within 1% Vout nom		75	150	uS
Turn-On Transient					
Start-up Time, From ON/OFF Control	To Vout=90% nominal			460	mS
Start-up Time, From Input	To Vout=90% nominal			460	mS
Rise Time	Time from 10% to 90% of nominal output voltage			25	mS
Output Voltage Overshoot				2	%
ISOLATION CHARACTERISTICS	NOTES AND CONDITIONS	MIN.	TYP.	MAX.	UNITS
Insulation Safety Rating	NOTES AND CONDITIONS	IVIIIV.	Reinforced	IVIAA.	Olaro
Input to Output			4242		Vdc
Input to Baseplate			2250		Vdc
			2250		Vdc
Output to Baseplate	Inquit/Outro		2250		MO
Isolation Resistance	Input/Output				
Isolation Capacitance	Input/Output		750		pF
TEMPERATURE LIMITS FOR POWER DERATING CU	RVES				
Semiconductor Junction Temperature				Tjmax-25	°C
Board Temperature	UL rated max operating temp 130°C			130	°C
Transformer/Inductor Temperature				130	°C
FEATURE CHARACTERISTICS					
Switching Frequency		190	210	230	kHz
ON/OFF Control (Blank suffix)					
Off-State Voltage		0		0.7	V
On-State Voltage	Open the ON/OFF pin = ON	2		15	V
ON/OFF Control (N suffix)					
Off-State Voltage	Open the ON/OFF pin = OFF	2		15	V
On-State Voltage		0		0.7	V
ON/OFF Control Current (Either Option)		'	1	1	
Current thru ON/OFF pin	Von/off=0V		1	2	mA
Current thru ON/OFF pin	Von/off=15V			1	mA
Remote Sense Compensation			10		%
Output Voltage Trim Range	Pout<=Max rated power	-10		10	%
Trim Up Equations	Please see TRIM functions in Technical Notes				70
Trim Down Equations	Please see TRIM functions in Technical Notes				
Output Over-Voltage Protection	Hiccup mode; over full temp range; % of nominal Vout	115	125	150	%
Over-Temperature Shutdown	Thocap mode, over fail temp range, 70 of normal voic	110	123	130	70
With Baseplate			125		°C
Restart Hysteresis			6		°C
RELIABILITY/SAFETY/ENVIRONMENTAL			0		U
NELIADILII 1/SAPET 1/ENVIRUNIVIENTAL					
Safety	Certified to UL 60950-1, CAN/CSA C22.2 60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR332, Issue 2, Method 1, Class 1		1.48		MHrs
Conducted Emissions	External filter is required, see Technical Notes		EN55011 CLASS	S A	
MECHANICAL					
Outline Dimensions			2.41 x 1.56 x 0.5	12	Inches
(Please refer to outline drawing)	LxWxH		61.21 x 39.62 x	13	mm
Weight			3	-	Ounces
			90		Grams
Through Hole Pin Diameter			0.04 & 0.06		Inches
mough hold i in Diamoldi			U.UT & U.UU		11101100











FUNCTIONAL SPECIFICATIONS: IRQ-24/6.25-W80

Post March Post	ABSOLUTE MAXIMUM RATINGS	NOTES AND CONDITIONS	MIN.	TYP.	MAX.	UNITS
Decenting Continuous	Input Voltage					•
Decenting Continuous	Non-Operating	Continuous	0		175	Vdc
Transmit Operating 100mS	Operating	Continuous	16		160	Vdc
Secretary Reserved Perspective 4-00 5-00 7-	. •	100mS	14.4		168	Vdc
100 100	. •				85	°C
Stronger Temperature						
Ambusine						
Institution	• ,		- 00			
Voltage at ON/OFF input pin						
General conditions for device under Test unless otherwise specified: Ambient interpretation - 25°°, Vin typical, Voot nominal load, Encapsulated Package; With 1µF810µF capacitors across output pins.	' '		0			
Ambient Imperature - 25°C; Vin Typicals; Vout nominal load; Encapsulated Package; With 1pf 20 full capacitors across output pins. ***PURIT CRABATICIPISTS** ***PURIT CRABATICIPISTS**	*	 s otherwise specified:	U		13	Vuc
Spenting (part Wildage Range) Import Under Village Lodord Turn- Or Village Theshold 13.0 14.0 15.0 Wold Turn- Or Village Threshold 11.0 12.0 13.0 Wold Lodovd Village Threshold Full Load, Vin-16V 11.0 12.0 13.0 Wold Maximum Ingit Clurrent Full Load, Vin-16V 9.0 6.0 7.0 mA Disable Ingit Current (Bark stiffs) 1.0 15.5 mA Disable Ingit Current (Bark stiffs) 1.0 15.5 mA Input Terminal Rigole Current RMS thru 220g/F/25OV, 12g/F across source, 33g/F/25OV extermal capacitors 1.0 15.5 mA Input Terminal Rigole Current RMS thru 220g/F/25OV, 12g/F across source, 33g/F/25OV extermal capacitors 1.0 4.0 4.5 A Recommended Extend Input Current (Bark stiffs) RMS thru 220g/F/25OV, 12g/F across source, 33g/F/25OV extermal capacitors 1.0 1.0 4.0 4.5 A Input Terminal Rigole Current RMS thru 220g/F/25OV 12g/F across source, 33g/F/25OV extermal capacitors 1.0 1.0 4.0 4.0 4.0 <td< td=""><td></td><td></td><td></td><td></td><td></td><td></td></td<>						
Spenting (part Wildage Range) Import Under Village Lodord Turn- Or Village Theshold 13.0 14.0 15.0 Wold Turn- Or Village Threshold 11.0 12.0 13.0 Wold Lodovd Village Threshold Full Load, Vin-16V 11.0 12.0 13.0 Wold Maximum Ingit Clurrent Full Load, Vin-16V 9.0 6.0 7.0 mA Disable Ingit Current (Bark stiffs) 1.0 15.5 mA Disable Ingit Current (Bark stiffs) 1.0 15.5 mA Input Terminal Rigole Current RMS thru 220g/F/25OV, 12g/F across source, 33g/F/25OV extermal capacitors 1.0 15.5 mA Input Terminal Rigole Current RMS thru 220g/F/25OV, 12g/F across source, 33g/F/25OV extermal capacitors 1.0 4.0 4.5 A Recommended Extend Input Current (Bark stiffs) RMS thru 220g/F/25OV, 12g/F across source, 33g/F/25OV extermal capacitors 1.0 1.0 4.0 4.5 A Input Terminal Rigole Current RMS thru 220g/F/25OV 12g/F across source, 33g/F/25OV extermal capacitors 1.0 1.0 4.0 4.0 4.0 <td< td=""><td>INPUT CHARACTERISTICS</td><td></td><td></td><td></td><td></td><td></td></td<>	INPUT CHARACTERISTICS					
Injust Imput Imp			16	72	160	Vdc
Turn-Off Wilage Presided					100	100
Turn-Off Witage Priversion			13.0	14.0	15.0	Vdc
Lockout Voltage Hysteresis	-					
Maximum Input Current Full Load, Vin=16V	•		11.0		10.0	
Non-Lead Input Current (N suffix)		Full Load Vin=16V		۷	10	
Disabled Injust Current (N suffix) 10 15 mA	•	· · · · · · · · · · · · · · · · · · ·		60		
Disabled Input Current (Blank suffix) RMS thru 220µF/250V, 12µF across source, 33µF/250V external capacitors across input pins 100 150 mA		VIII=12V				
Input Reflected Ripple Current						
Input Terminal Ripple Current ARMS, 20MHz bandwidth A.0 A.5 A.6	Disabled Input Current (Blank Suffix)			10	15	MA
Imput Terminal Ripple Current RMS, 20MHz bandwidth See Technical Notes See Technical Notes 220	Input Reflected Ripple Current			100	150	mA
Recommended Input Fuse See Technical Notes See Technical Notes See Figure 4 in the Technical Notes 220	Input Terminal Ripple Current	• • •		4.0	4.5	Α
Recommended External Input Capacitance 2pcs 120μF/250Vdc and MPN is EXX.J251ELL121ML25S. See Figure 4. 240		,				
Pecommended BUS Capacitance 2pcs 120μF/250Vdc and MPN is EKXJ251ELL121ML25S. See Figure 4. 240 25 30 AS	· ·					
Invash Current (Pt) See Derating See Deratin		0				
OUTPUT CHARACTERISTICS Total Output Voltage Pet Point See Deratting 150 150 W Output Voltage Set Point Vin=Nominal, lo=OA, Ta=25°C 23.76 24 24.24 Voc Setting Accuracy within: (measured № 50% Load) 1 %Vo nom %Vo nom Output Voltage Regulation ************************************	•	2pcs 120pi / 250v de and wit wis EtX.0251 EEE 12 twice 250. Occ 11guite 4.			30	
Total Output Power See Derating 150 150 W Output Voltage Set Priorit Vin=Nominal, Io=0A, Ta=25°C 23.76 24 24.24 Vdc	` '			23	30	AU
Output Voltage Set Point Vin=Nominal, Io=OA, Ta=25°C 23.76 24 24.24 Vide Setting Accuracy within: (measured @ 50% Load) 1 9%Vo nom Output Voltage Regulation ************************************		See Derating		150	150	W
Setting Accuracy within : (measured @ 50% Load) 1 %Wo nom Output Voltage Regulation Over Load Vin=72V, lout from Min to Max ±0.25 % Over Line lout=Full load, Vin from Min to Max ±0.25 % Over Temperature Vin=72V, Ta=-40°Cto 85°C ±0.004 ±0.02 mV Total Output Voltage Range Over sample, line, load, temperature & life 23.28 24.72 Vdc Output Voltage Ripple and Noise 20MHz bandwidth 100 200 mVp-p PRMS Full Load, 1µF ceramic, 10µF tantalum 100 200 mVrms Operating Output Current Range 0 6.25 6.25 A Output DC Current-Limit Inception Output Voltage 10% Low 7.5 9 11 A Output Capacitance Nominal Vout at full load (CR load) 0 6.25 6.25 A Programmable Undervoltage Lockout: (via R uvlo) 11.0 12.0 13.0 Vdc 24Vin : Ruvlo = open Vin_OFF 21.0 22.0 23.0 Vdc Vin_OFF <td></td> <td>•</td> <td>22.76</td> <td></td> <td></td> <td></td>		•	22.76			
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Over Line lout=Full load, Vin from Min to Max ±0.25 % Over Temperature Vin-72V, Ta40°Cto 85°C ±0.004 ±0.02 mV Total Output Voltage Range Over sample, line, load, temperature & life 23.28 24.72 Vdc Output Voltage Range 20MHz bandwidth - - - Peak-to-Peak Full Load, 1μF ceramic, 10μF tantalum 100 200 mVp-p RMS Full Load, 1μF ceramic, 10μF tantalum 30 50 mVrms Operating Output Current Range 0 6.25 6.25 A Output DC Current-Limit Inception Output Voltage 10% Low 7.5 9 11 A Output Capacitance Nominal Vout at full load (CR load) 0 6.25 6.25 A Programmable Undervoltage Lockout: (via R uvlo) 11.0 12.0 13.0 Vdc 24Vin : Ruvlo = open Vin_OFF 11.0 12.0 13.0 Vdc 48Vin : Ruvlo = 6.98kΩ Vin_OFF 29.0 30.0 31.0 Vdc <t< td=""><td></td><td>Vin 70V lout from Min to May</td><td>1</td><td>. 0.25</td><td></td><td>0/</td></t<>		Vin 70V lout from Min to May	1	. 0.25		0/
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Total Output Voltage Range Over sample, line, load, temperature & life 23.28 24.72 Vdc					. 0.00	
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RMS Full Load, 1μF ceramic, 10μF tantalum 30 50 mVms Operating Output Current Range 0 6.25 6.25 A Output DC Current-Limit Inception Output Voltage 10% Low 7.5 9 11 A Output Capacitance Nominal Vout at full load (CR load) 0 1500 μF Programmable Undervoltage Lockout: (via R uvlo) 11.0 12.0 13.0 Vdc 24Vin : Ruvlo = open Vin_OFF 11.0 12.0 13.0 Vdc 36Vin : Ruvlo = 12.7kΩ Vin_OFF 21.0 22.0 23.0 Vdc 48Vin : Ruvlo = 6.98kΩ Vin_OFF 29.0 30.0 31.0 Vdc 72Vin : Ruvlo = 3.92kΩ Vin_OFF 29.0 30.0 31.0 Vdc 72Vin : Ruvlo = 3.92kΩ Vin_OFF 42.5 44.0 45.5 Vdc 96Vin : Ruvlo = 2.49kΩ Vin_OFF 61.3 62.8 64.3 Vdc 11/Vin : Ruvlo = 2.49kΩ Vin_OFF 63.5 65.0 66.5 Vdc <	· · · · · · · · · · · · · · · · · · ·			100	200	
Operating Output Current Range 0 6.25 6.25 A Output DC Current-Limit Inception Output Voltage 10% Low 7.5 9 11 A Output Capacitance Nominal Vout at full load (CR load) 0 1500 μF Programmable Undervoltage Lockout: (via R uvlo) -						
Output DC Current-Limit Inception Output Voltage 10% Low 7.5 9 11 A Output Capacitance Nominal Vout at full Ioad (CR load) 0 1500 μF Programmable Undervoltage Lockout: (via R uvlo)		Full Load, TµF ceramic, ToµF tantalum	_			
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24Vin : Ruvlo = open Vin_OFF 11.0 12.0 13.0 Vdc		,	0		1500	μŀ
	Programmable Undervoltage Lockout:					
36Vin : Ruvlo = 12.7kΩ Vin_OFF 21.0 22.0 23.0 Vdc	24Vin · Ruylo = open					
	ETTIL Havio — opon					
Vin_ON 23.0 24.0 25.0 Vdc 48Vin : Ruvlo = 6.98kΩ Vin_OFF 29.0 30.0 31.0 Vdc 72Vin : Ruvlo = 3.92kΩ Vin_OFF 42.5 44.0 45.5 Vdc Vin_ON 44.5 46.0 47.5 Vdc 96Vin : Ruvlo = 2.49kΩ Vin_OFF 61.3 62.8 64.3 Vdc 110Vin : Ruvlo = 2.37kΩ Vin_OFF 63.5 65.0 66.5 Vdc	36Vin : Ruylo = 12.7kO	_				
Vin Nu Nu Nu Nu Nu Nu Nu N						
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Vin Nuvlo 3.92KΩ Vin ON 44.5 46.0 47.5 Vdc	10 VIII . 114 VIO — 0.30 M2		31.0			Vdc
96Vin : Ruvlo = 2.49kΩ Vin_OFF 61.3 62.8 64.3 Vdc 110Vin : Ruvlo = 2.37kΩ Vin_OFF 63.3 64.8 66.3 Vdc 110Vin : Ruvlo = 2.37kΩ Vin_OFF 63.5 65.0 66.5 Vdc	72\/in : Puylo = 2.02k0	Vin_OFF	42.5	44.0	45.5	Vdc
96Vin : Ruvlo = 2.49KΩ Vin_ON 63.3 64.8 66.3 Vdc 110Vin : Ruvlo = 2.37kΩ 56.5 65.0 66.5 Vdc	/ Z VIII . NUVIU = 3.92K1/	Vin_ON	44.5	46.0	47.5	Vdc
VII_ON 63.3 64.8 66.3 Vdc 110Vin : Puvlo = 2.37k0 Vin_OFF 63.5 65.0 66.5 Vdc		Vin OFF	61.3	62.8	64.3	Vdc
110Vin : Puylo = 2 37VO		VIII_OFF	01.0			
110Vin : Puylo = 2 37VO	96Vin : Ruvlo = 2.49 k Ω					Vdc
Vin_0N 65.5 67.0 68.5 Vdc		Vin_ON	63.3	64.8	66.3	



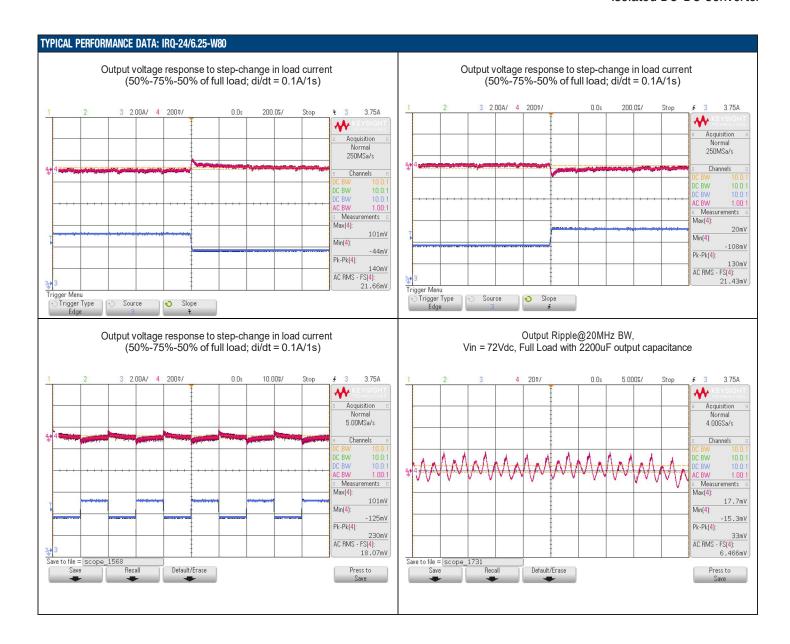
FUNCTIONAL SPECIFICATIONS: IRQ-24/6.25-W80

EFFICIENCY					
100% Load	Vin=24V; Details see Figures		85.5		%
100% Load	Vin=110V; Details see Figures		89		%
DYNAMIC CHARACTERISTICS					
Output Voltage During Load Current Transient					
Step Change in Output Current (0.1A/uS)	lout 50-75-50% nom, within 1% of Vout		150		uS
Peak deviation			±250	±350	mV
Turn-On Transient	T. W. J. 2007			100	_
Start-up Time, From ON/OFF Control	To Vout=90% nominal		50	460	mS
Start-up Time, From Input	To Vout=90% nominal		150	460	mS
Rise Time	Time from 10% to 90% of nominal output voltage			25	mS
Output Voltage Overshoot	Time from 1070 to 3070 of norminal output voltage			2	%
ISOLATION CHARACTERISTICS	NOTES AND CONDITIONS	MIN.	TYP.	MAX.	UNITS
Insulation Safety Rating	HOLES AND CONDITIONS	IVIIIV.	Reinforced	IVIAA.	UNITO
Input to Output			4242		Vdc
Input to Baseplate			2250		Vdc
Output to Baseplate			2250		Vdc
Isolation Resistance	Input/Output		20		MO
Isolation Capacitance	Input/Output		750		pF
TEMPERATURE LIMITS FOR POWER DERATING			700		рі
Semiconductor Junction Temperature	OSITELO			Tjmax-25	°C
Board Temperature	UL rated max operating temp 130°C			130	°C
Transformer/Inductor Temperature	on a second seco			130	°C
FEATURE CHARACTERISTICS				.00	
Switching Frequency		252	280	308	kHz
ON/OFF Control (Blank suffix)		202	200	000	
Off-State Voltage		0		0.7	V
On-State Voltage	Open the ON/OFF pin = ON	2		15	V
ON/OFF Control (N suffix)					-
Off-State Voltage	Open the ON/OFF pin = OFF	2		15	V
On-State Voltage	The second of th	0		0.7	V
ON/OFF Control Current (Either Option)					
Current thru ON/OFF pin	Von/off=0V		1	2	mA
Current thru ON/OFF pin	Von/off=15V			1	mA
Remote Sense Compensation			5		%
Output Voltage Trim Range	Pout<=Max rated power	-10		10	%
Trim Up Equations	Please see TRIM functions in Technical Notes				
Trim Down Equations	Please see TRIM functions in Technical Notes				
Output Over-Voltage Protection	Hiccup mode; over full temp range; % of nominal Vout	28	29	31	Vdc
Over-Temperature Shutdown					
With Baseplate			125		°C
Restart Hysteresis			6		°C
RELIABILITY/SAFETY/ENVIRONMENTAL				<u> </u>	
Safety	Certified to UL 60950-1, CAN/CSA C22.2 60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR332, Issue 2, Method 1, Class 1		1.48		MHrs
Conducted Emissions	External filter is required, see Technical Notes		EN5501	1 CLASS A	
MECHANICAL					
Outline Dimensions			2.41 x 1.56 x 0.5	512	Inches
(Refer to outline drawing)	LxWxH		61.21 x 39.62 x	13	mm
Weight			3		Ounces
					•
			90 0.04 & 0.06		Grams











FUNCTIONAL SPECIFICATIONS: IRQ-54/2.8-W80

ABSOLUTE MAXIMUM RATINGS	NOTES AND CONDITIONS	MIN.	TYP.	MAX.	UNITS
Input Voltage					
Non-Operating	Continuous	0		175	Vdc
Operating	Continuous	16		160	Vdc
Transient Operating	100mS	14.4		168	Vdc
Operating Ambient Temperature		-40		85	°C
Operating Baseplate Temperature		-40		100	°C
Storage Temperature		-55		125	°C
Altitude		-33		3000	
					m
Input/Output Isolation Voltage				4242	Vdc
Voltage at ON/OFF input pin		0		15	Vdc
General conditions for device under Test t Ambient temperature +25°C; Vin typical; \(^1\)	unless otherwise specified: Vout nominal load; Encapsulated Package; With 1µF&10µF capacitors across output pins.				
INPUT CHARACTERISTICS					
Operating Input Voltage Range		16	72	160	Vdc
Input Under-Voltage Lockout					
Turn-On Voltage Threshold		13.0	14.0	15.0	Vdc
Turn-Off Voltage Threshold		10.0	12.0	13.0	Vdc
Lockout Voltage Hysteresis		10.0	2	1010	Vdc
Maximum Input Current	Full Load, Vin=16V			12	A
No-Load Input Current	Vin=72V		60	70	mA
	VIII— 1		10	15	
Disabled Input Current (N suffix)					mA
Disabled Input Current (Blank suffix)			10	15	mA
Back Ripple Current (w/filtering)	Measured at the input of module with a simulated source impedance of 12μH, 220μF, 200V, across source, 220μF, 200V external capacitors across input pins.		1200	1500	mA
Back Ripple Current (w/o filtering)			6500	7500	mA
Input Terminal Ripple Current	RMS, 20MHz bandwidth		17		mA
Short Circuit Input Current	Timo, Zomi Z banawida		0.06		A
Recommended Input Fuse	See Technical Notes				A
Recommended External Input Capacitance			220		μF
	See Figure 4 in the Technical Notes.				
Recommended BUS Capacitance	2pcs 120μF/250Vdc and MPN is EKXJ251ELL121ML25S. See Figure 4.		240		μF
Inrush Current (I²t)			20		A ² S
OUTPUT CHARACTERISTICS			4=0	150	
Total Output Power	See Derating		150	150	W
Output Voltage Set Point	Vin=Nominal, Io=0A, Ta=25°C	53.46	54	54.54	Vdc
Setting Accuracy:	(measured @ 50% Load)		1		% Vo nom
Output Voltage Regulation					
Over Load	Vin=72V, lout from Min to Max		±0.25		%
Over Line	lout=Full load, Vin from Min to Max		±0.25		%
Temperature Coefficient	@ All Outputs:		0.02		%/°C
Total Output Voltage Range	Over sample, line, load, temperature & life	52.38		55.62	Vdc
Output Voltage Ripple and Noise	20MHz bandwidth				
Peak-to-Peak	Full Load, 1µF ceramic, 10µF tantalum		400	500	mV
RMS	Full Load, 1µF ceramic, 10µF tantalum		30	50	mV
Operating Output Current Range	Tail Load, The Columno, Topi Cantalani	0	2.8	2.8	A
Output DC Current-Limit Inception	Output Voltage 10% Low	3.3	4.1	5.5	A
			4.1		
Output Capacitance	Nominal Vout at full load (CR load)	0		470	μF
Programmable Undervoltage Lockout:	(via R uvlo)				
24Vin : Ruvlo = open	Vin_OFF	11.0	12.0	13.0	Vdc
24viii : Havio = opoii	Vin_ON	13.0	14.0	15.0	Vdc
36Vin : Ruvlo = 12.7kΩ	Vin_OFF	21.0	22.0	23.0	Vdc
JUVIII . NUVIU = 12./K1/	Vin_ON	23.0	24.0	25.0	Vdc
401° B. I. C.C.'.	Vin_OFF	29.0	30.0	31.0	Vdc
$48Vin: Ruvlo = 6.98k\Omega$	Vin_ON	31.0	32.0	33.0	Vdc
	Vin_OFF	42.5	44.0	45.5	Vdc
72Vin : Ruvlo = 3.92 k Ω	Vin_ON	44.5	46.0	47.5	Vdc
96Vin : Ruvlo = $2.49k\Omega$	Vin_OFF Vin_ON	61.3 63.3	62.8	64.3	Vdc
		n 3 3	64.8	66.3	Vdc
					17.
110Vin : Ruvlo = 2.37kΩ	Vin_ONF Vin_ON	63.5 65.5	65.0 67.0	66.5 68.5	Vdc Vdc

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FUNCTIONAL SPECIFICATIONS: IRQ-54/2.8-W80

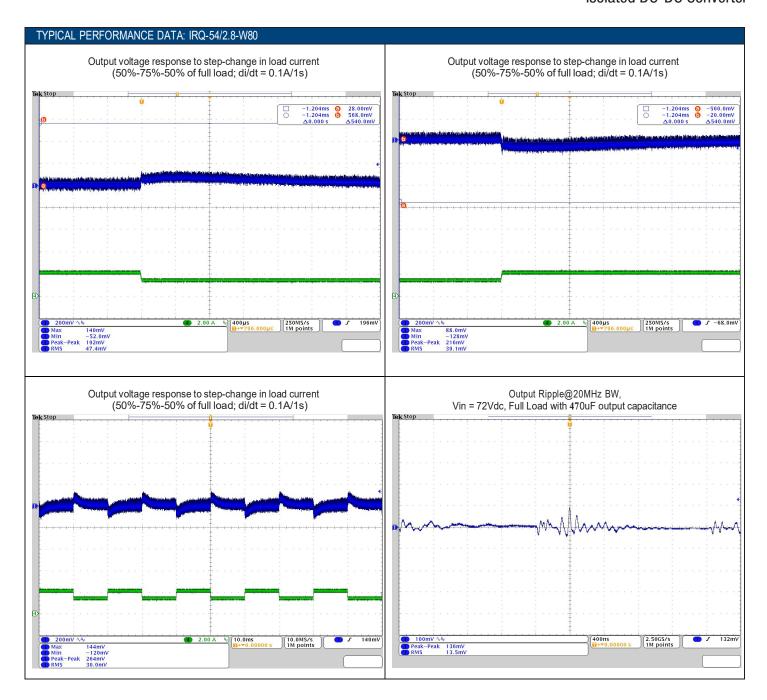
EFFICIENCY					
100% Load	Vin=24V; Details see Figures		85		%
100% Load	Vin=110V: Details see Figures		89		%
DYNAMIC CHARACTERISTICS	, , , , , , , , , , , , , , , , , , ,				
Output Voltage During Load Current Transient					
Step Change in Output Current (0.1A/uS)	lout 50-75-50% nom, within 1% of Vout		150		uS
Peak deviation	Full Load, 1µF ceramic, 10µF tantalum		±200	±400	mV
Turn-On Transient					
Start-up Time, From ON/OFF Control	See Figures		50	460	mS
Start-up Time, From Input	See Figures		150	460	mS
Rise Time	Time from 10% to 90% of nominal output voltage			100	mS
Output Voltage Overshoot				2	%
ISOLATION CHARACTERISTICS	NOTES AND CONDITIONS	MIN.	TYP.	MAX.	UNITS
Insulation Safety Rating			Reinforced		
Input to Output			4242		Vdc
Input to Baseplate			2250		Vdc
Output to Baseplate			2250		Vdc
Isolation Resistance	Input/Output		20		MO
Isolation Capacitance	Input/Output		750		pF
TEMPERATURE LIMITS FOR POWER DERATING CURV	ES Control of the con				
Semiconductor Junction Temperature				Tjmax-25	°C
Board Temperature	UL rated max operating temp 130°C			130	°C
Transformer/Inductor Temperature				130	°C
FEATURE CHARACTERISTICS					
Switching Frequency		230	255	280	kHz
ON/OFF Control (Blank suffix)					
Off-State Voltage		0		0.7	V
On-State Voltage	Open the ON/OFF pin = ON	2		15	V
ON/OFF Control (N suffix)					
Off-State Voltage	Open the ON/OFF pin = OFF	2		15	V
On-State Voltage		0		0.7	V
ON/OFF Control Current (Either Option)					
Current thru ON/OFF pin	Von/off=0V		1	2	mA
Current thru ON/OFF pin	Von/off=15V			1	mA
Remote Sense Compensation			4		%
Output Voltage Trim Range	Pout<=Max rated power	-20		10	%
Trim Up Equations	Please see TRIM functions in Technical Notes				
Trim Down Equations	Please see TRIM functions in Technical Notes				
Output Over-Voltage Protection	Hiccup mode; over full temp range; % of nominal Vout	59.4	65	70.2	Vdc
Over-Temperature Shutdown		1			
With Baseplate			125		°C
Restart Hysteresis			6		°C
RELIABILITY/SAFETY/ENVIRONMENTAL			·		
Safety	Certified to UL 60950-1, CAN/CSA C22.2 60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR332, Issue 2, Method 1, Class 1		1.48		MHrs
Conducted Emissions	External filter is required, see Technical Notes		EN55011 CLASS	S A	
MECHANICAL					_
Outline Dimensions			2.41 x 1.56 x 0.5	12	Inches
(Please refer to outline drawing)	LxWxH		61.21 x 39.62 x		mm
Weight			3	-	Ounces
			90		Grams
Through Hole Pin Diameter			0.04 & 0.06		Inches
	I .		3.0 . 0 0.00		



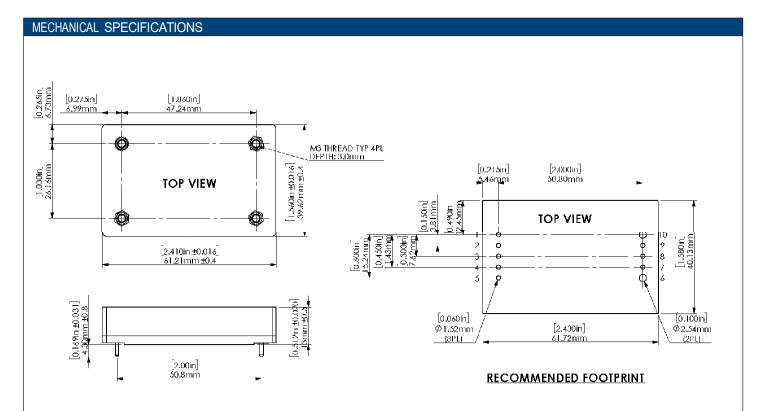


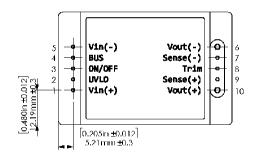






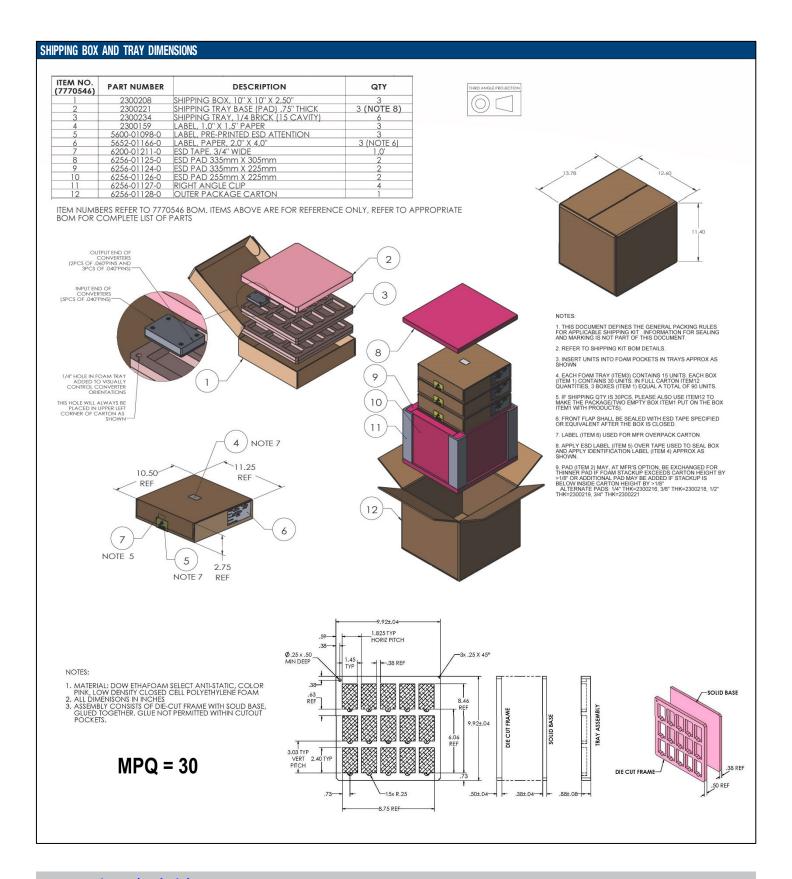






	INPUT/OUTPUT CONNECTIONS									
Pin	Designation	Function								
1	Vin (+)	Positive Input								
2	UVLO	Under Voltage Lockout								
3	ON/OFF	ENABLE								
4	BUS	BUS Voltage								
5	Vin (-)	Negative Input								
6	Vout (-)	Negative Output								
7	Sense (-)	Sense (-)								
8	Trim	Trim								
9	Sense (+)	Sense (+)								
10	Vout (+)	Positive Output								







TECHNICAL NOTES

On/Off Control

The input-side, remote On/Off Control function (pin 3) can be ordered to operate with either logic type:

Negative ("N" suffix): Negative-logic devices are off when pin 3 is left open (or pulled high, applying +2V to +15V), and on when pin 3 is pulled low (0 to 0.7V) with respect to -Input as shown in Figure 1.

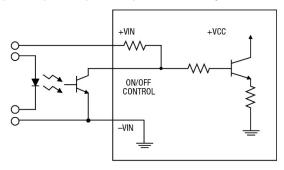


Figure 1. Driving the Negative Logic On/Off Control Pin

Dynamic control of the remote on/off function is best accomplished with a mechanical relay or an open-collector/open-drain drive circuit (optically isolated if appropriate). The drive circuit should be able to sink appropriate current (see Performance Specifications) when activated and withstand appropriate voltage when deactivated. Applying an external voltage to pin 3 when no input power is applied to the converter can cause permanent damage to the converter.

Input Fusing

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current-limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line.

Vin	Fuse Rate Current
24V	15A fast
48V	8A fast
72V	5A fast
110V	3A fast

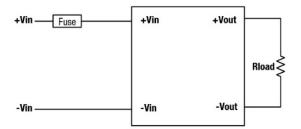


Figure 2. Input Fusing

Input Under-Voltage Shutdown and Start-Up Threshold

Under normal start-up conditions, converters will not begin to regulate properly until the rising input voltage exceeds and remains at the Start-Up Threshold Voltage (see Specifications). Once operating, converters will not turn off until the input voltage drops below the Under-Voltage Shutdown Limit. Subsequent restart will not occur until the input voltage rises again above the Start-Up Threshold. This built-in hysteresis prevents any unstable on/off operation at a single input voltage. Figure 3 shows a typical configuration.

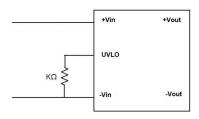


Figure 3. Under Voltage Lockout Configuration

The tables below shows UVLO values for various nominal input voltages and the required resistor for each.

The following parameters apply to Vo is 12V

Nominal Vin	24V	36V	48V	72V	96V	110V
Turn-off Threshold	12±1V	20.5±1V	29±1V	43±1.5V	60.5±2V	64.3±2V
Turn-on threshold	15±1V	23.5±1V	32±1V	46±1.5V	63.8±2V	67.3±2V
UVLO External Resistor	open	20.5kΩ	10.7kΩ	5.9kΩ	3.74kΩ	3.48kΩ

The following parameters apply to Vo is 24V or 54V

Nominal Vin	24V	36V	48V	72V	96V	110 V
Turn-off Threshold	12±1V	22±1V	30±1V	44±1.5V	62.8±1.5V	65±1.5V
Turn-on threshold	14±1V	24±1V	32±1V	46±1.5V	64.8±1.5V	67±1.5V
UVLO External Resistor	open	12.7kΩ	6.98kΩ	3.92kΩ	2.49kΩ	2.37kΩ



Hold-Up Time and BUS Capacitor

The BUS pin is for hold-up time function. It is designed to work with an external circuit comprises a cap (Chold), a resistor and a diode. (Hold up time is defined as the duration of time that the DC/DC converter output will remain active following a loss of input power). When this function is activated, the module will use the energy stored in external circuit to support operation. Please note that the BUS pin is a voltage source output of 80Vdc to allow external capacitors to be connected in order to provide hold up power of the converter. After 80Vdc input, it tracks the DC input voltage range up to 160Vdc.A typical configuration is shown in Figure 4 below.

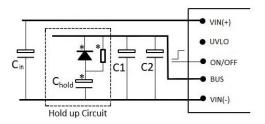


Figure 4. Connection of External Hold-Up Circuit

NOTE: Two low ESR electrolytic BUS capacitors connected between BUS and Vin- are necessary for stability (C1 and C2 are $120\mu F/250V$ caps). A $220\mu F$ input capacitor (Cin) is also recommended between Vin+ and Vin-.

This function provides energy that maintains the DC/DC converter in operation for 10mS/20mS/30mS of hold-up time. The capacitance (Chold) in the application is recommended in the hold-up table below.

Hold up Time	24V	36V	48V	72V	96V	110V
10ms	1800uF	1800uF	1800uF	1800uF	550uF	330uF
20ms	3600uF	3600uF	3600uF	3600uF	1100uF	660uF
30ms	5400uF	5400uF	5400uF	5400uF	1650uF	990uF

NOTE: The resistor value recommended is 100ohm.

Start-Up Time

Assuming that the output current is set at the rated maximum, the Vin to Vout Start-Up Time (see Specifications) is the time interval between the point when the rising input voltage crosses the Start-Up Threshold and the fully loaded output voltage enters and remains within its specified accuracy band. Actual measured times will vary with input source impedance, external input capacitance, input voltage slew rate and final value of the input voltage as it appears at the converter.

These converters include a soft start circuit to moderate the duty cycle of its PWM controller at power up, thereby limiting the input inrush current.

The On/Off Remote Control interval from On command to Vout (final $\pm 2\%$) assumes that the converter already has its input voltage stabilized above the Start-Up Threshold before the On command. The

interval is measured from the On command until the output enters and remains within its specified accuracy band. The specification assumes that the output is fully loaded at maximum rated current. Similar conditions apply to the On to Vout regulated specification such as external load capacitance and soft start circuitry.

Recommended Input Filtering

Ensure that the input source has low AC impedance to provide dynamic stability and that the input supply has little or no inductive content, including long distributed wiring to a remote power supply. The converter will operate with no additional external capacitance if these conditions are met.

For best performance, we recommend installing a low-ESR capacitor immediately adjacent to the converter's input terminals. The capacitor should be a ceramic type such as the Murata GRM32 series or a polymer type. Make sure that the input terminals do not go below the undervoltage shutdown voltage at all times. More input bulk capacitance may be added in parallel (either electrolytic or tantalum) if needed.

Recommended Output Filtering

The converter will achieve its rated output ripple and noise with no additional external capacitor. However, the user may install more external output capacitance to reduce the ripple even further or for improved dynamic response. Again, use low-ESR ceramic (Murata GRM32 series) or polymer capacitors. Mount these close to the converter. Measure the output ripple under your load conditions.

Use only as much capacitance as required to achieve your ripple and noise objectives. Excessive capacitance can make step load recovery sluggish or possibly introduce instability. Do not exceed the maximum rated output capacitance listed in the specifications.

Input Ripple Current and Output Noise

All models in this converter series are tested and specified for input reflected ripple current and output noise using designated external input/output components, circuits and layout as shown in the figures below. The Cbus and Lbus components simulate a typical DC voltage bus.

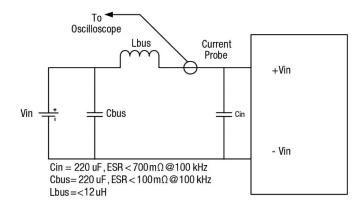


Figure 5. Measuring Input Ripple Current



PWM controller will shut down. Following a time-out period, the PWM will restart, causing the output voltage to begin rising to its appropriate value. If the short-circuit condition persists, another shutdown cycle will initiate. This rapid on/off cycling is called "hiccup mode." The hiccup cycling reduces the average output current, thereby preventing excessive internal temperatures and/ or component damage.

The "hiccup" system differs from older latching short circuit systems because you do not have to power down the converter to make it restart. The system will automatically restore operation as soon as the short circuit condition is removed.

Output Capacitive Load

These converters do not require external capacitance added to achieve rated specifications. Users should only consider adding capacitance to reduce switching noise and/or to handle spike current load steps. Install only enough capacitance to achieve noise objectives. Excess external capacitance may cause degraded transient response and possible oscillation or instability.

Remote Sense Input

Use the Sense inputs with caution. Sense is normally connected at the load. Sense inputs compensate for output voltage inaccuracy delivered at the load. This is done by correcting IR voltage drops along the output wiring and the current carrying capacity of PC board etch. This output drop (the difference between Sense and Vout when measured at the converter) should not exceed 0.5V. Consider using heavier wire if this drop is excessive. Sense inputs also improve the stability of the converter and load system by optimizing the control loop phase margin.

NOTE: The Sense input and power Vout lines are internally connected through low value resistors to their respective polarities so that the converter can operate without external connection to the Sense. Nevertheless, if the Sense function is not used for remote regulation, the user should connect +Sense to +Vout and - Sense to -Vout at the converter pins.

The remote Sense lines carry very little current. They are also capacitively coupled to the output lines and therefore are in the feedback control loop to regulate and stabilize the output. As such, they are not low impedance inputs and must be treated with care in PC board layouts. Sense lines on the PCB should run adjacent to DC signals, preferably Ground. In cables and discrete wiring, use twisted pair, shielded tubing or similar techniques.

Any long, distributed wiring and/or significant inductance introduced into the Sense control loop can adversely affect overall system stability. If in doubt, test your applications by observing the converter's output transient response during step loads. There should not be any appreciable ringing or oscillation. You may also adjust the output trim slightly to compensate for voltage loss in any external filter elements. Do not exceed maximum power ratings.

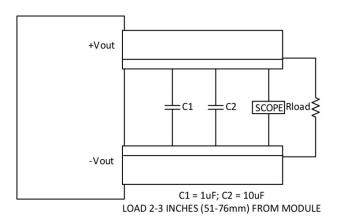


Figure 6. Measuring Output Ripple and Noise (PARD)

Output Over-Voltage Protection

The IRQ-W80 output voltage is monitored for an over-voltage condition using a comparator. The signal is optically coupled to the primary side and if the output voltage rises to a level which could be damaging to the load, the sensing circuitry will disable the PWM controller drive causing the output voltage to decrease. It is referred to as "latch" mode.

Minimum Output Loading Requirements

All models regulate within specification and are stable under no load to full load conditions. Operation under no load might however slightly increase output ripple and noise.

Over-Temperature Protection

The over-temperature protection consists of circuitry that provides protection from thermal damage. If the over-temperature is detected the module will shut down, and restart after the temperature is within specification.

Output Fusing

The converter is extensively protected against current, voltage and temperature extremes. However, your output application circuit may need additional protection. In the extremely unlikely event of output circuit failure, excessive voltage could be applied to your circuit. Consider using an appropriate fuse in series with the output.

Output Current Limiting

The modules include an internal output over-current protection circuit, which endure current limiting for an unlimited duration during output overload. If the output current exceeds the OCP set point, the module will shut down, and always try to restart (hiccup mode) until the over current condition is corrected.

Output Short Circuit Condition

When a converter is in current-limit mode, the output voltage will drop as the output current demand increases. If the output voltage drops too low (approximately 97% of nominal output voltage for most models), the



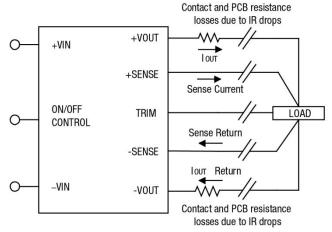


Figure 7. Remote Sense Circuit Configuration

NOTE: Observe Sense inputs tolerance to avoid improper operation: The value of the Output Sense Range depends on the Output voltage, which decreases as the increases of the output voltage.

Output overvoltage protection is monitored at the output voltage pin, not the Sense pin. Therefore, excessive voltage differences between Vout and Sense together with trim adjustment of the output can cause the overvoltage protection circuit to activate and shut down the output.

Power derating of the converter is based on the combination of maximum output current and the highest output voltage. Therefore, the designer must ensure: (Vout at pins) x (lout) \le (Max. rated output power)

 $[Vout(+)\text{-}Vout(\text{-})] \text{ - } [Sense(+)\text{-}Sense(\text{-})] \leq Output \text{ Sense Range}$

Vo	12V	24V	54V
Output Sense Range	10%	5%	4%

Through-Hole Soldering Guidelines

Murata Power Solutions recommends the TH soldering specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Your production environment may differ; therefore, please thoroughly review these guidelines with your process engineers.

Wave Solder Operations for Through-H	ole Mounted Products
For Sn/Ag/Cu based solders: Maximum Preheat Temperature Maximum Pot Temperature Maximum Solder Dwell Time	115° C. 270° C. 7 seconds
For Sn/Pb based solders: Maximum Preheat Temperature Maximum Pot Temperature Maximum Solder Dwell Time	105° C. 250° C. 6 seconds

Trimming the Output Voltage

The Trim input to the converter allows the user to adjust the output voltage over the rated trim range (please refer to the Specifications). In the trim equations and circuit diagrams that follow, trim adjustments use either a trimpot or a single fixed resistor connected between the Trim input and either the +Sense or -Sense terminals. Trimming resistors should have a low temperature coefficient (±100 ppm/deg.C or less) and be mounted close to the converter. Keep leads short. If the trim function is not used, leave the trim unconnected. Withno trim, the converter will exhibit its specified output voltage accuracy.

There are two CAUTIONs to observe for the Trim input:

CAUTION: To avoid unplanned power down cycles, do not exceed EITHER the maximum output voltage OR the maximum output power when setting the trim. Be particularly careful with a trimpot. If the output voltage is excessive, the OVP circuit may inadvertently shut down the converter. If the maximum power is exceeded, the converter may enter current limiting. If the power is exceeded for an extended period, the converter may overheat and encounter overtemperature shut down.

CAUTION: Be careful of external electrical noise. The Trim input is a sensitive input to the converter's feedback control loop. Excessive electrical noise may cause instability or oscillation. Keep external connections short to the Trim input. Use shielding if needed.

Trim Equations

<u>Trim Down</u>: Connect trim resistor between trim pin and -Sense When Vnom = 12V

RTrimdn
$$(k\Omega) = 1 * \frac{Vnom}{Vnom-Vo} - 2$$

When Vnom = 24V or 54V

RTrimdn
$$(k\Omega) = 10 * \frac{Vnom}{Vnom - Vo} - 20$$

Trim Up: Connect trim resistor between trim pin and +Sense

When Vnom = 12V

RTrimup
$$(k\Omega) = \frac{1*Vnom*(1+\frac{Vo-Vnom}{Vnom})}{1. * \frac{Vo-Vnom}{Vnom}} - 1 * \frac{Vnom}{Vo-Vnom}$$
 2

When Vnom = 24V or 54V

$$RTrimup (k\Omega) = \frac{10*Vnom*(1+\frac{Vo\cdot Vnom}{Vnom})}{1. * \frac{Vo\cdot Vnom}{Vnom}} - 10 * \frac{Vnom}{Vo\cdot Vnom}$$
 20



When Vnom = 12V

Output Voltage	10.8V	11.4V	12.6V	13.2V
Rtrim up (kΩ)	NA	NA	183.71	95.76
Rtrim down (kΩ)	8	18	NA	NA

When Vnom = 24V

Output Voltage	21.6V	22.8V	25.2V	26.4V
Rtrim up (kΩ)	NA	NA	3894.26	2035.10
Rtrim down (kΩ)	80	180	NA	NA

When Vnom = 54V

Output Voltage	48.6V	51.3V	56.7V	59.4 V
Rtrim up (kΩ)	NA	NA	9037.14	4728.98
Rtrim down (kΩ)	80	180	NA	NA

NOTE: Do not exceed the specified trim range or maximum power ratings when adjusting trim. Use 1% precision resistors mounted close to the converter on short leads. If sense is not installed, connect the trim resistor to the respective Vout pin.

Trim Circuits

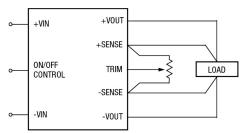


Figure 8. Trim Connections Using a Trimpot

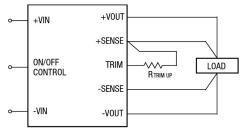


Figure 9. Trim Connections to Increase Output Voltage

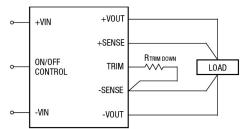


Figure 10. Trim Connections to Decrease Output Voltage

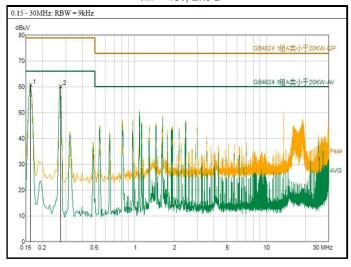


Qualification Tests

Parameters	Test Conditions	Operating
Vibration	EN 61373:2010 Clause 8, Bogie mounted	Yes
Mechanical Shock	EN 61373:2010 Clause 10, Bogie mounted	Yes
DMTBF (Life Test)	Vin nom, units at derating point, 35 days	Yes
Temperature Cycling Test (TCT)	-40°C to 125°C, unit temp. ramp 15°C/min., 500 cycles	Yes
Temperature, Humidity and Bias (THB)	85°C, 85RH, Vin=max, Load=min load, 1072 Hour (72 hours with a pre-conditioning soak, unpowered)	No
Damp heat test, cyclic	EN60068-2-30: Temperature +55°C and +25°C; Number of cycles 2 (respiration effect); Time 2 x 24 hours; Relative Humidity 95%	No
Dry heat test	EN60068-2-2, Vin=nom, Full load, 85°C for 6 hours.	Yes
Low Temperature operating	Vin=nom, Full load, -40°C for 2 hours.	Yes
Highly Accelerated Life Test (HALT)	High temperature limits, low temperature limits, Vibration limits, Combined Environmental Tests.	Yes
EMI	CISSPR 22 Class A, or IEC62236-3-2 (GB/T 24338.4)	Yes
ESD	IEC6100-4-2: ±6kV contact discharge / ±8kV air discharge	Yes
Surge Protection	IEC/EN 61000-4-5	Yes
Solderability	IPC/EIAJ-STD-002B (Solderability Tests for Component Leads, Terminations, Lugs, Terminals and Wires)	No

Conducted Emissions Test Results (IRQ-24Vout)

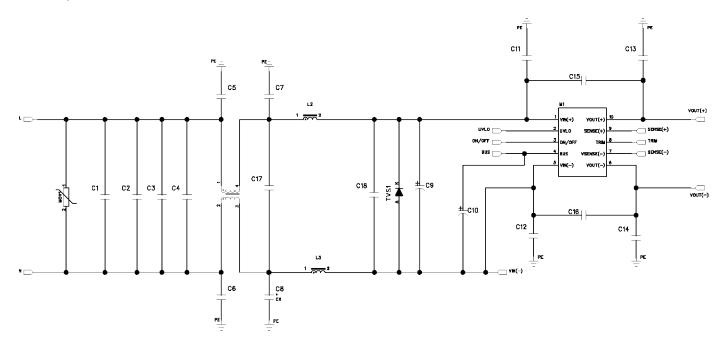
Vin = 48V, Line L



序号	频率 (MHz)	读值 (dBuV)	修正因子 (dB)	结果 (dBuV)	限值 (dBuV)	余量 (dB)	备注
1	0.162	36.67	24.07	60.74	66	5.26	平均值
2	0.274	36.22	24.01	60.23	66	5.77	平均值



EMI Filter, Schematic and Parts List



Part	Manufacturer	MPN	Description
MOV	Epcos	B72214S0141K101	Varistor, 180V
C1	Faratronic	C212E475K9AC000	4.7uF, 250V
C2, C3, C4	Murata	GRM43DR72E474KW01L	0.47uF, 250V
C5, C6	Murata	DE1E3RA102MA4BQ01F	1000pF, 300VAC
C11, C12	Murata	DE2E3KY222MA3BM02F	2200pF
C13, C14	Murata	DE1E3KX102MB4BP01F	1000pF
C15, C16	Murata	DE1E3RA472MA4BQ01F	4700pF, 250V
L1	Wurth	7448262013	2x1.3mH CMC
L2	Bourns	2101-V-RC	10uH
C9, C10	United Chemicon	EKXJ251EXX271ML40S	270uF, 250V low ESR
C7, C8, C17, C18		NA	Not used

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Refer to: http://www.murata.com/products/power/requirements/

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