

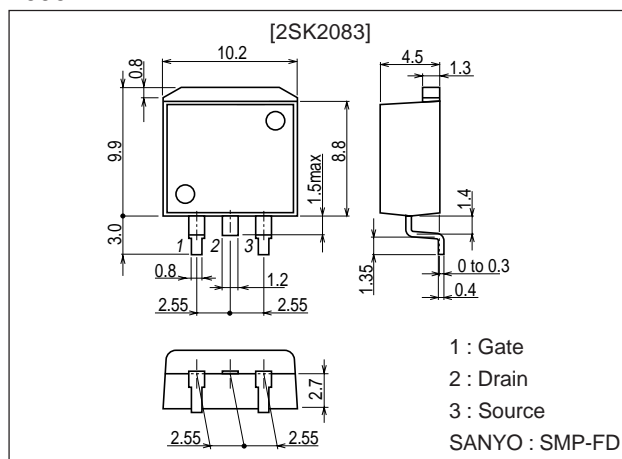
ON Semiconductor DATA SHEET

N-Channel Silicon MOSFET

- Low ON resistance.
- Ultrahigh-speed switching.
- Micaless package facilitating mounting.

unit:mm

2090A



Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		900	V
Gate-to-Source Voltage	V_{GSS}		± 30	V
Drain Current (DC)	I_D		5	A
Drain Current (pulse)	I_{DP}	$PW \leq 10\mu s$, duty cycle $\leq 1\%$	10	A
Allowable Power Dissipation	P_D	$T_c = 25^\circ C$	70	W
			1.65	W
Channel Temperature	T_{ch}		150	$^\circ C$
Storage Temperature	T_{stg}		-55 to +150	$^\circ C$

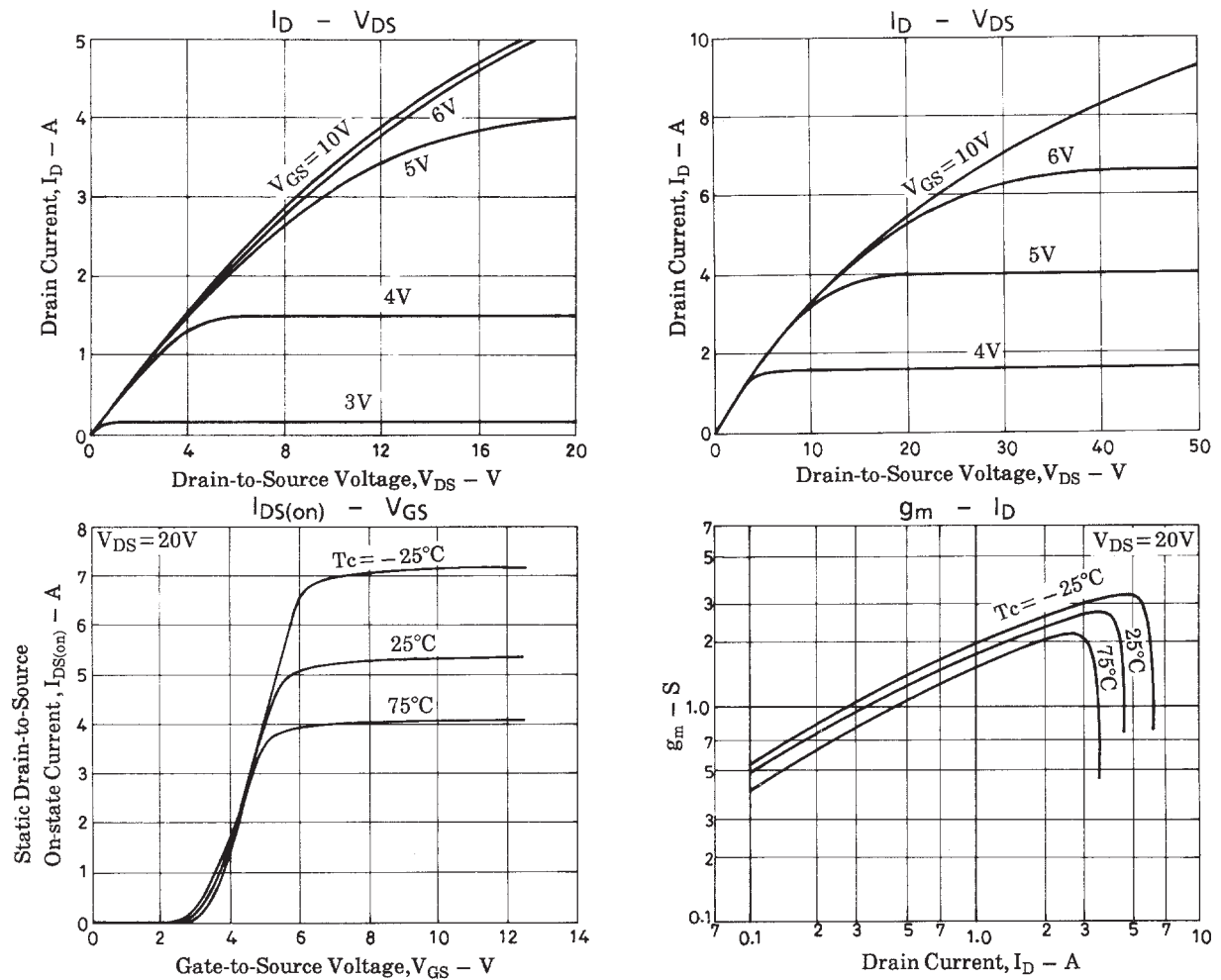
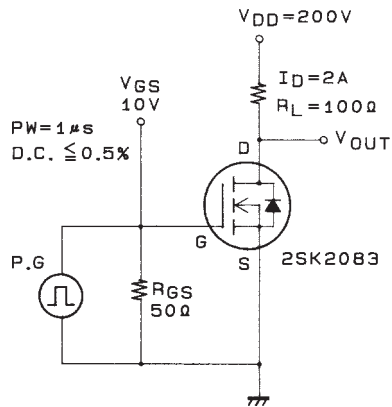
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1mA, V_{GS}=0$	900			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS}=900V, V_{GS}=0$			1.0	mA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0$			± 100	nA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10V, I_D=1mA$	2.0		3.0	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=20V, I_D=2A$	1.0	2.0		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)}$	$I_D=2A, V_{GS}=10V$		2.8	3.6	Ω

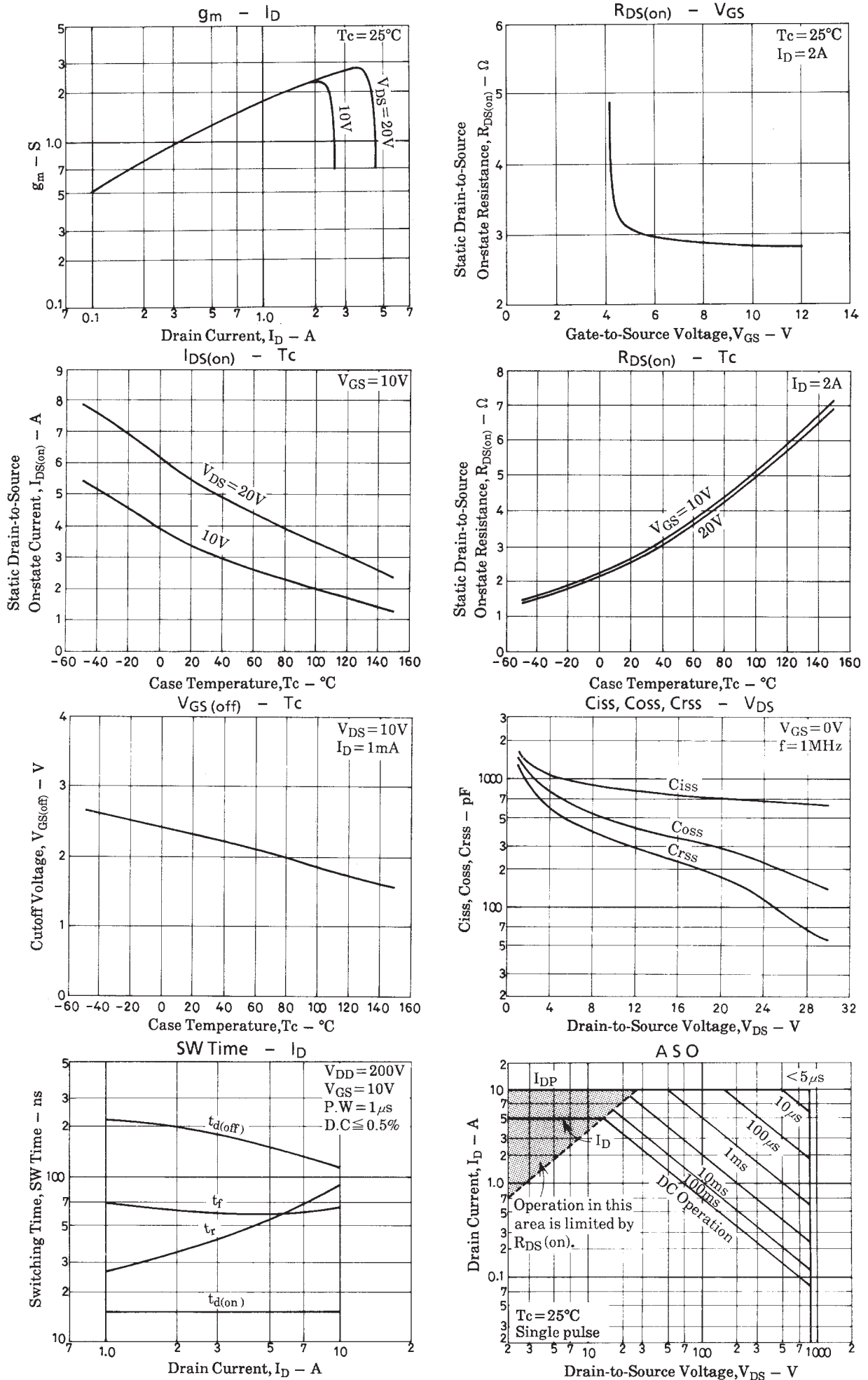
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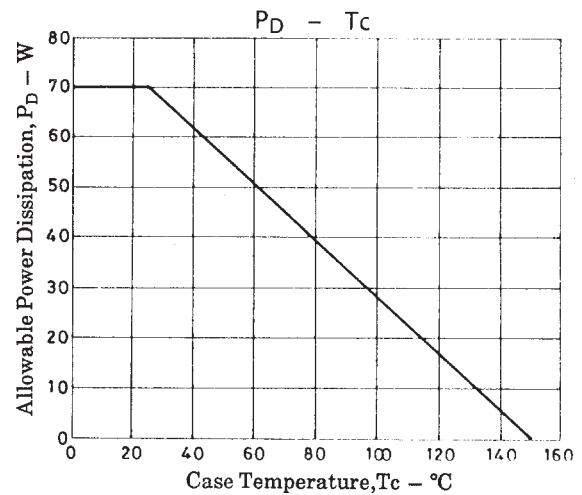
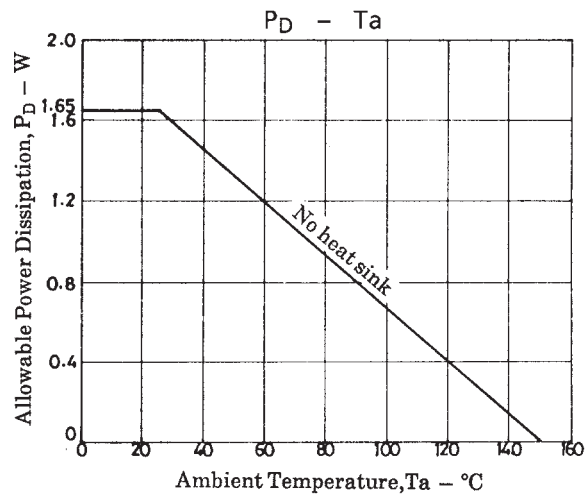
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	C_{iss}	$V_{DS}=20V, f=1MHz$		700		pF
Output Capacitance	C_{oss}	$V_{DS}=20V, f=1MHz$		300		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS}=20V, f=1MHz$		170		pF
Turn-ON Delay Time	$t_{d(on)}$	$I_D=2A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		15		ns
Rise Time	t_r	$I_D=2A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		35		ns
Turn-OFF Delay Time	$t_{d(off)}$	$I_D=2A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		200		ns
Fall Time	t_f	$I_D=2A, V_{GS}=10V, V_{DD}=200V, R_{GS}=50\Omega$		65		ns
Diode Forward Voltage	V_{SD}	$I_S=5A, V_{GS}=0$			1.8	V

Switching Time Test Circuit







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