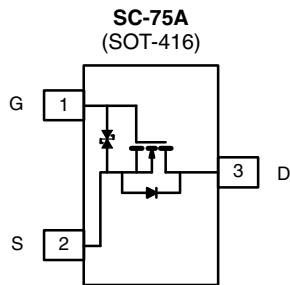


RYE002N05-VB Datasheet

N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY

$V_{DS(min.)}$ (V)	$R_{DS(on)}$ (Ω)	$V_{GS(th)}$ (V)	I_D (mA)
60	1.25 at $V_{GS} = 10$ V	1 to 2.5	330



FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Trench Power MOSFETs
- Low On-Resistance: 1.25 Ω
- Low Threshold: 2.5 V
- Low Input Capacitance: 30 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- Miniature Package
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories, Transistors, etc.
- Battery Operated Systems
- Solid State Relays

BENEFITS

- Low Offset Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Error Voltage
- Small Board Area

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ^a	$T_A = 25$ °C	I_D 330	mA
	$T_A = 85$ °C	240	
Pulsed Drain Current ^a	I_{DM}	650	
Power Dissipation ^a	$T_A = 25$ °C	P_D 250	mW
	$T_A = 85$ °C	130	
Thermal Resistance, Maximum Junction-to-Ambient ^a	R_{thJA}	500	°C/W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

Notes:

a. Surface mounted on FR4 board, power applied for $t \leq 10$ s.

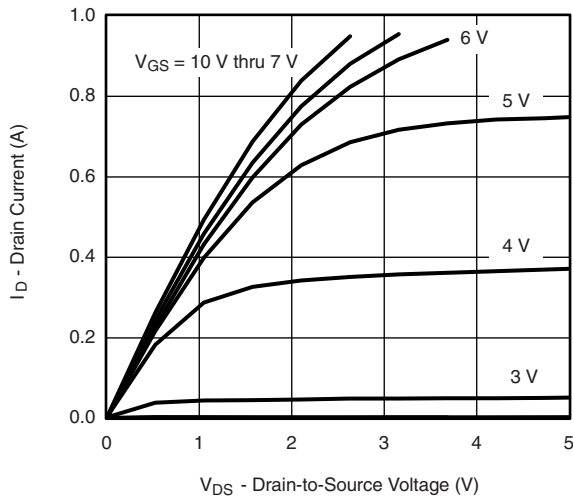
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 10 μA	60			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 0.25 mA	1		2.5	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 10 V			± 150	nA
		T _J = 85 °C			± 500	
		V _{DS} = 0 V, V _{GS} = ± 5 V			± 20	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 50 V, V _{GS} = 0 V			10	
		T _J = 85 °C			100	
		V _{DS} = 60 V, V _{GS} = 0 V			1	μA
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 10 V, V _{GS} = 4.5 V	500			mA
		V _{DS} = 7.5 V, V _{GS} = 10 V	800			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 200 mA		3.0		Ω
		T _J = 125 °C		5.0		
		V _{GS} = 10 V, I _D = 500 mA		1.25		
		T _J = 125 °C		2.25		
Forward Transconductance ^a	g _{fs}	V _{DS} = 10 V, I _D = 200 mA	100			mS
Diode Forward Voltage ^a	V _{SD}	V _{GS} = 0 V, I _S = 200 mA			1.3	V
Dynamic^b						
Input Capacitance	C _{iss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		30		pF
Output Capacitance	C _{oss}			6		
Reverse Transfer Capacitance	C _{rss}			2.5		
Gate Charge	Q _g	V _{DS} = 10 V, I _D = 250 mA, V _{GS} = 4.5 V			0.6	nC
Switching^{b, c}						
Turn-On Time	t _(on)	V _{DD} = 30 V, R _L = 150 Ω, I _D = 200 mA, V _{GEN} = 10 V, R _g = 10 Ω			25	ns
Turn-Off Time	t _(off)				35	

Notes:

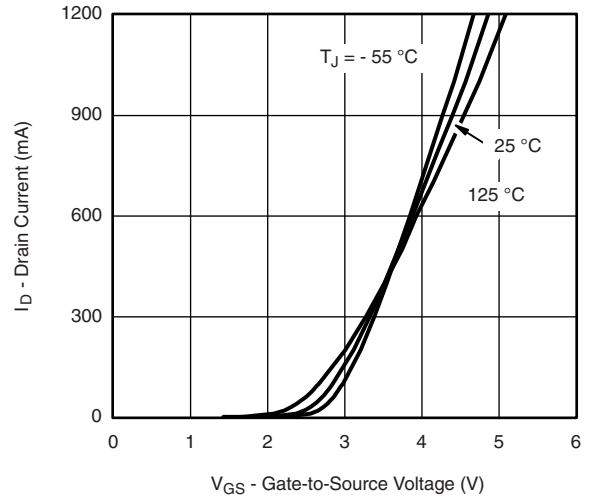
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
b. For DESIGN AID ONLY, not subject to production testing.
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

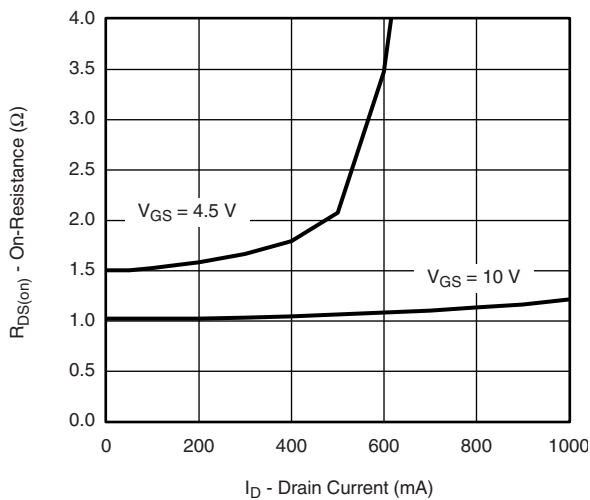
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



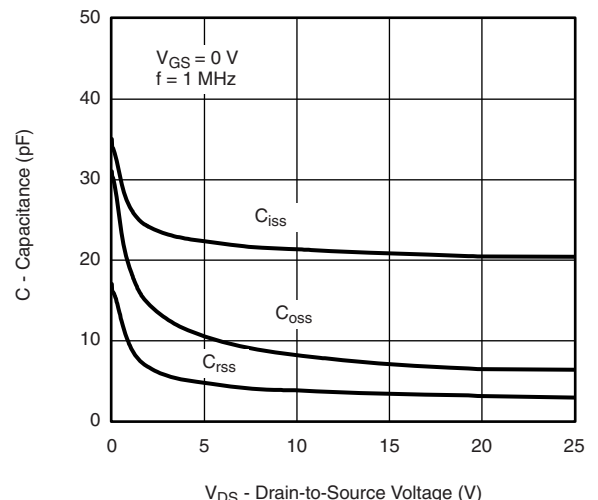
Output Characteristics



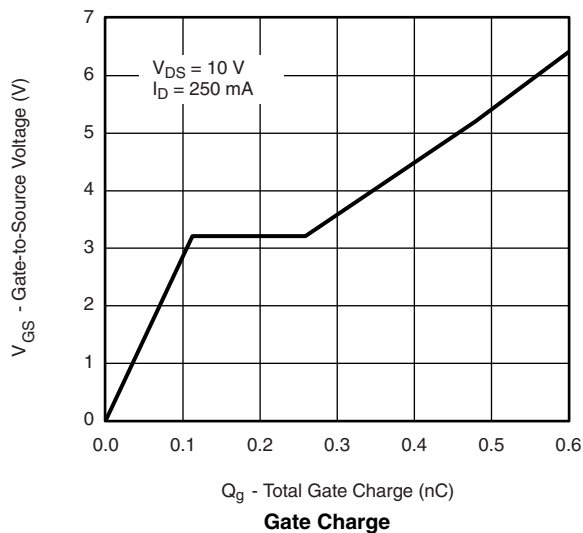
Transfer Characteristics



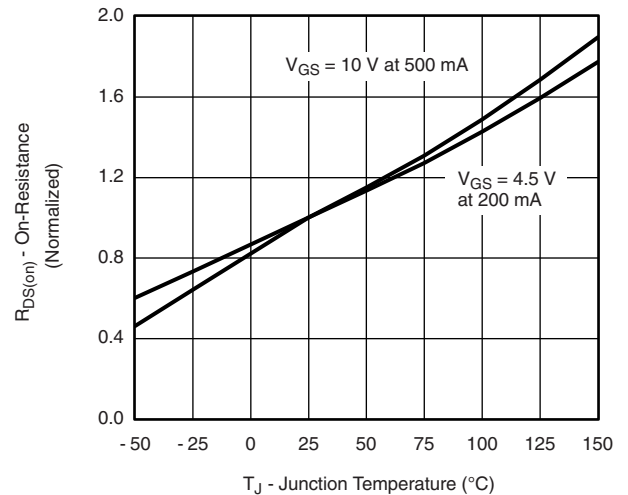
On-Resistance vs. Drain Current



Capacitance

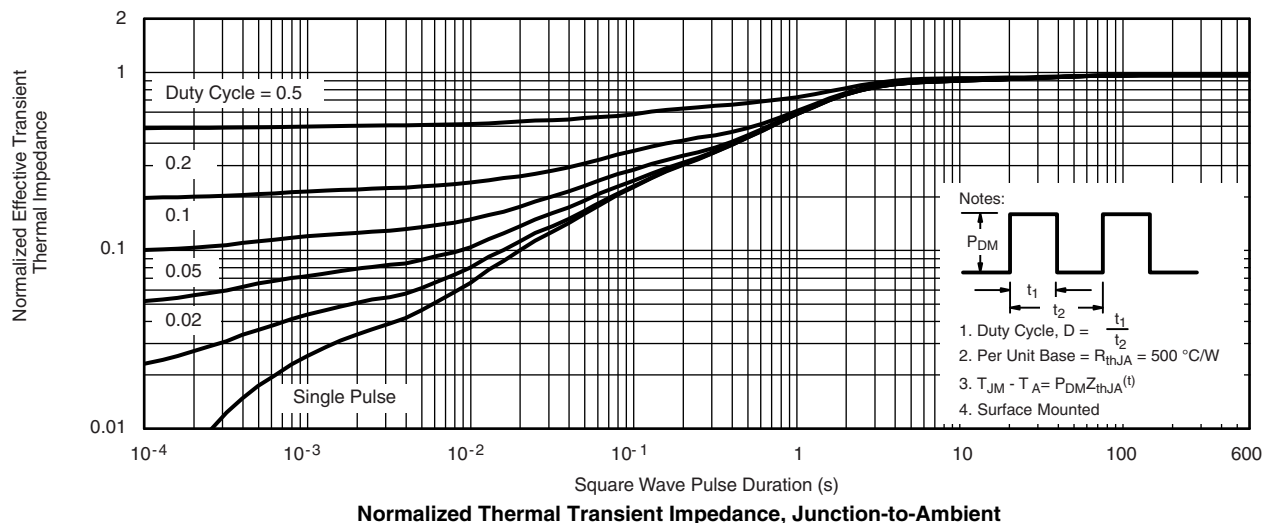
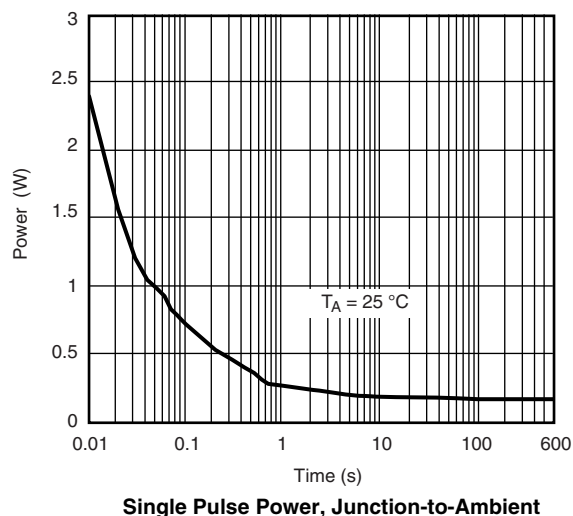
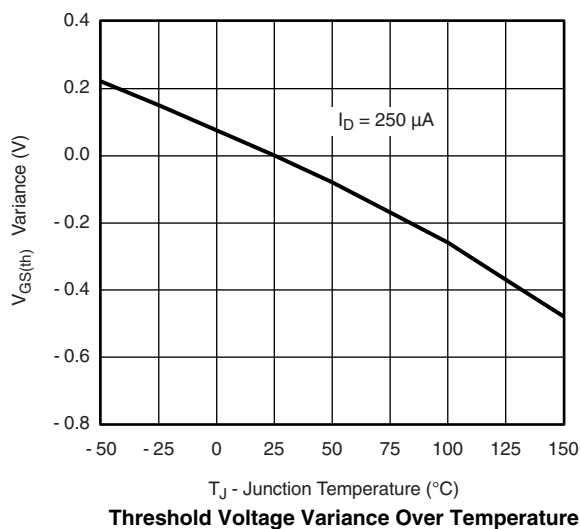
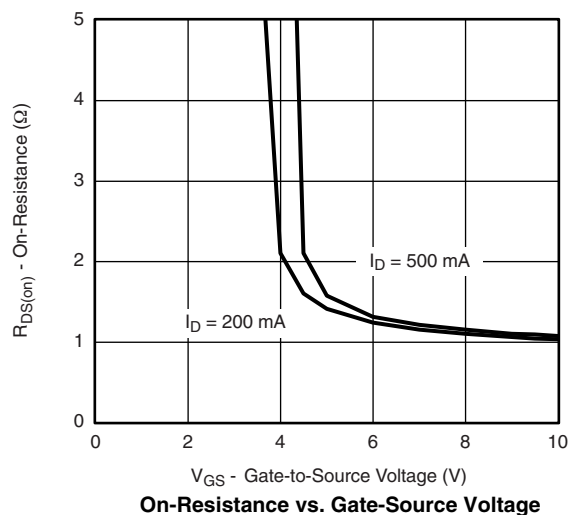
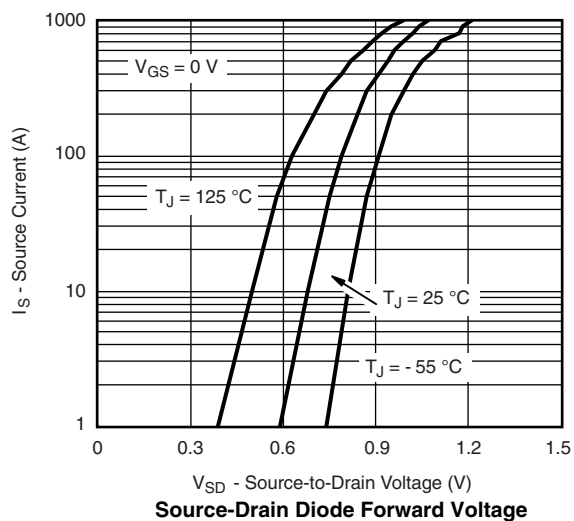


Gate Charge

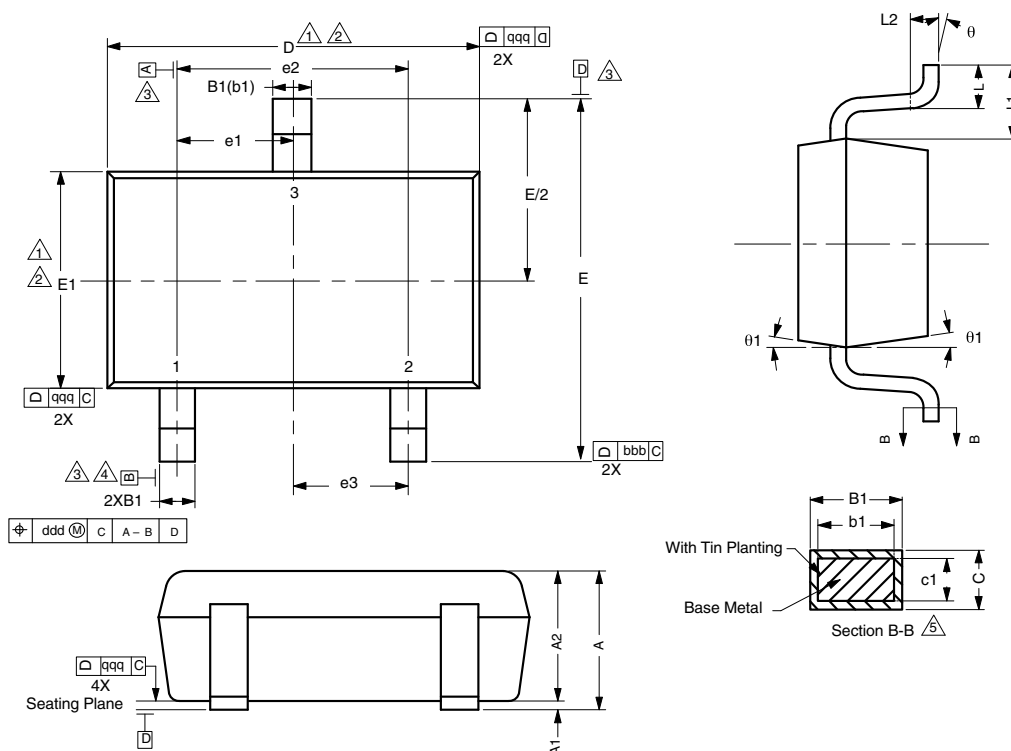


On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



SC-75A: 3 Leads



DWG: 5868

Notes

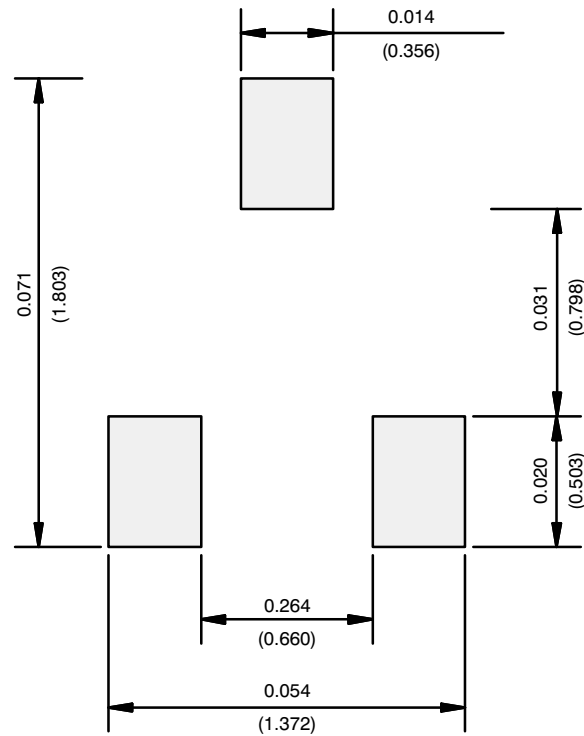
Dimensions in millimeters will govern.

1. Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
2. Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
3. Datums A, B and D to be determined 0.10 mm from the lead tip.
4. Terminal positions are shown for reference only.
5. These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES
aaa	0.10
bbb	0.10
ccc	0.10
ddd	0.10

DIM.	MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	
A	-	-	0.80	
A1	0.00	-	0.10	
A2	0.65	0.70	0.80	
B1	0.19	-	0.24	5
b1	0.17	-	0.21	
c	0.13	-	0.15	5
c1	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E1	0.66	0.76	0.86	1, 2
e1	0.50 BSC			
e2	1.00 BSC			
e3	0.50 BSC			
L	0.15	0.205	0.30	
L1	0.40 ref.			
L2	0.15 BSC			
q	0°	-	8°	
q1	4°	-	10°	

RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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