

General Description

The Sanrise SRC60R040BT-GA is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency.

The SRC60R040BT-GA break down voltage is 600V and it has a high rugged avalanche characteristics.

The SRC60R040BT-GA is available in TO-247 package.

Features

- Ultra Low $R_{DS(ON)} = 40m\Omega @ V_{GS} = 10V$.
- Ultra Low Gate Charge, $Q_g = 210nC$ typ.
- Fast switching capability
- Robust design with better EAS performance
- EMI Improved
- AEC-Q101 Qualified
- 100% Avalanche tested
- Green Product (RoHS compliant)

Application

- On-Board Charger
- DC/DC Converter
- Auxiliary Inverter

Symbol

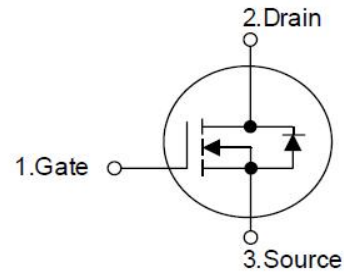


Figure 1 Symbol of SRC60R040BT-GA

Package Type

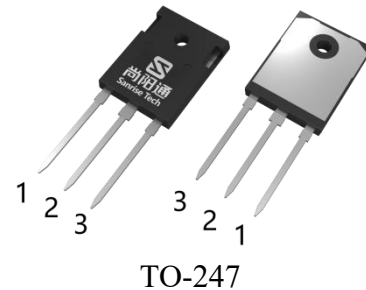


Figure 2 Package Type of SRC60R040BT-GA

Ordering Information

		SRC60R040BT□-GA	
Circuit Type	_____	_____	A: Automotive
Package	_____	_____	G: Green
T: TO-247		_____	Blank: Tube

Package	Part Number	Marking ID	Packing Type
TO-247	SRC60R040BT-GA	SRC60R040BTGA	Tube

Absolute Maximum Ratings^{Note 1}

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	600	V
Gate-Source Voltage (static)		V_{GSS}	±20	V
Gate-Source Voltage (dynamic), AC ($f > 1$ Hz)		V_{GSS}	±30	V
Power Dissipation($T_C=25^{\circ}\text{C}, T_O=247$)		P_{tot}	568	W
Continuous Drain Current	$T_C=25^{\circ}\text{C}$	I_D	77	A
	$T_C=100^{\circ}\text{C}$		48	
	$T_C=125^{\circ}\text{C}$		34	
Pulsed Drain Current (Note 2)		I_{DM}	231	A
Avalanche Energy, Single Pulse (Note 3)		E_{AS}	264	mJ
Avalanche Energy, Single Pulse (Note 4)		E_{AS}	2160	mJ
Avalanche Energy, Repetitive (Note 2)		E_{AR}	0.2	mJ
Avalanche Current, Repetitive (Note 2)		I_{AR}	2.8	A
Continuous Diode Forward Current		I_S	77	A
Diode Pulse Current		$I_{S,PULSE}$	231	A
Maximum diode commutation speed(Note 5)		di_F/dt	900	A/us
MOSFET dv/dt Ruggedness, $V_{DS} \leq 480\text{V}$		dv/dt	80	V/ns
Reverse Diode dv/dt , $V_{DS} \leq 480\text{V}$, $I_{SD} \leq I_D$		dv/dt	50	V/ns
Operating Junction Temperature		T_J	150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 to 150	$^{\circ}\text{C}$
Lead Temperature (Soldering, 10 sec)		T_{LEAD}	260	$^{\circ}\text{C}$

Note:

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.
Absolute maximum ratings are stress ratings only and functional device operation is not implied.
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $I_{AS}=2.8\text{A}$, $V_{DD}=60\text{V}$, $R_G=25\Omega$, Finish goods test condition.
4. $I_{AS}=8.0\text{A}$, $V_{DD}=60\text{V}$, $R_G=25\Omega$, Typical Eas.
5. $V_{DS}=0\sim 400\text{V}$, $I_{SD} \leq 34\text{A}$, $T_J=25^{\circ}\text{C}$

Thermal characteristics

Parameter (TO247-package)	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	R_{thJC}	-		0.22	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	R_{thJA}	-		62	

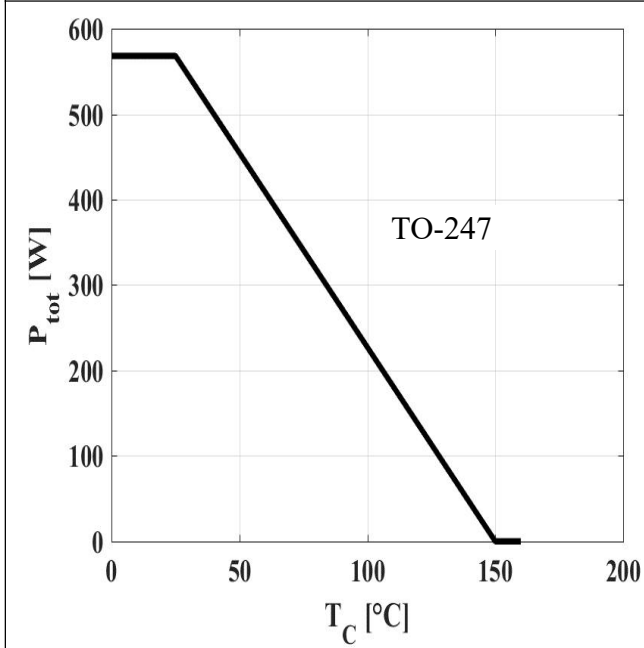
40mΩ, 600V, Super Junction N-Channel Power MOSFET
SRC60R040BT-GA
Electrical Characteristics
 $T_J = 25^{\circ}\text{C}$, unless otherwise specified.

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250uA	600			V
Zero Gate Voltage Drain Current		I _{DSS}	V _{DS} =600V, V _{GS} =0V			20	uA
Gate-Body Leakage Current	Forward	I _{GSSF}	V _{GS} =20V, V _{DS} =0V			100	nA
	Reverse	I _{GSSR}	V _{GS} =-20V, V _{DS} =0V			-100	
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =1.85mA	3.5	4.3	5.0	V
Static Drain-Source On-Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =34A		35	40	mΩ
Gate Resistance		R _G	f=1MHz, Open Drain		2.0		Ω
Dynamic Characteristics							
Input Capacitance		C _{ISS}	V _{DS} =400V, V _{GS} =0V, f=100kHz		5.5		nF
Output Capacitance		C _{OSS}			137		pF
Effective output capacitance, energy related ^{NOTE6}		C _{O(er)}	V _{GS} =0V, V _{DS} =0...480V		185		pF
Effective output capacitance, time related ^{NOTE7}		C _{O(tr)}			1264		
Turn-on Delay Time		t _{d(on)}	V _{DD} =400V, I _D =34A R _G =3.3Ω, V _{GS} =10V		14		ns
Rise Time		t _r			8		
Turn-off Delay Time		t _{d(off)}			102		
Fall Time		t _f			6.1		
Gate Charge Characteristics							
Gate to Source Charge		Q _{gs}	V _{DD} =480V, I _D =34A V _{GS} =0 to 10V		31.7		nC
Gate to Drain Charge		Q _{gd}			104		
Gate Charge Total		Q _g			210		
Gate Plateau Voltage		V _{plateau}			6.3		V
Reverse Diode Characteristics							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} =0V, I _{SD} =34A		0.86	1.1	V
Reverse Recovery Time		t _{rr}	V _R =400V, I _F =34A dI _F /dt=100A/us		176		ns
Reverse Recovery Charge		Q _{rr}			1.28		uC
Peak Reverse Recovery Current		I _{rrm}			14.5		A

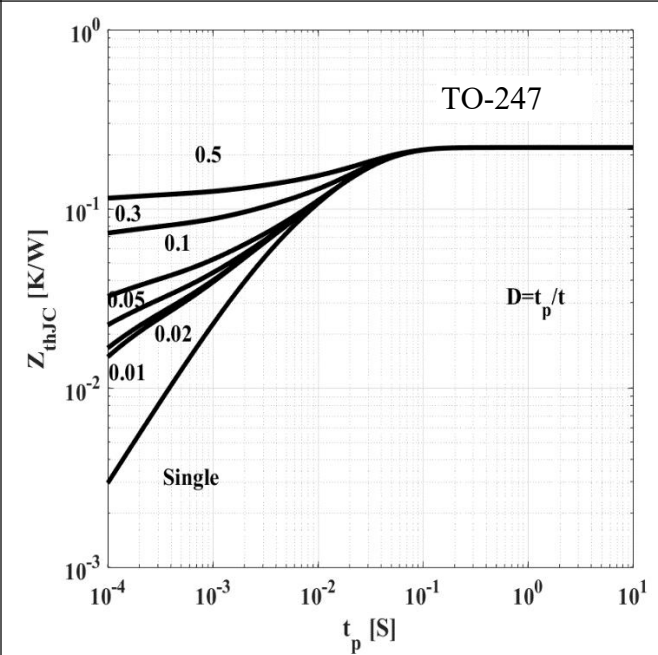
Note:

 6. $C_{O(er)}$ is a fixed capacitance that gives the same stored energy as C_{OSS} while V_{DS} is rising from 0 to 480V

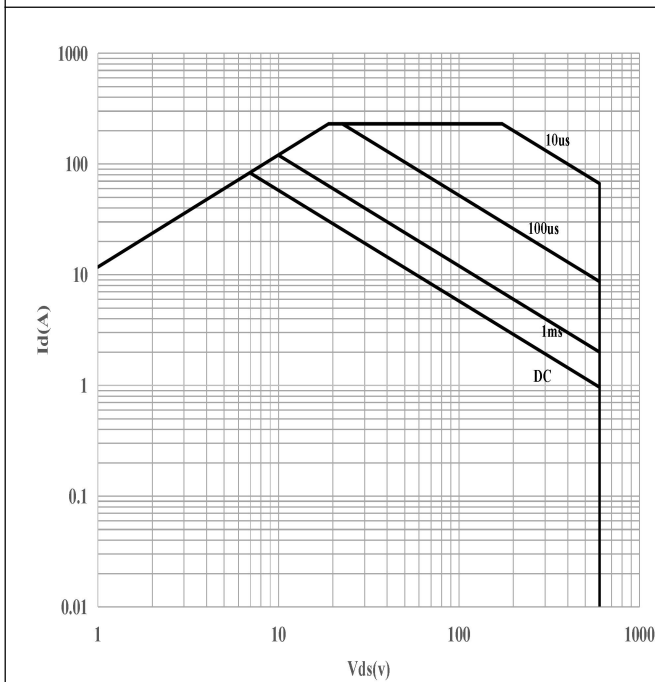
 7. $C_{O(tr)}$ is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 480 V

Typical Performance Characteristics
Figure 3: Power Dissipation


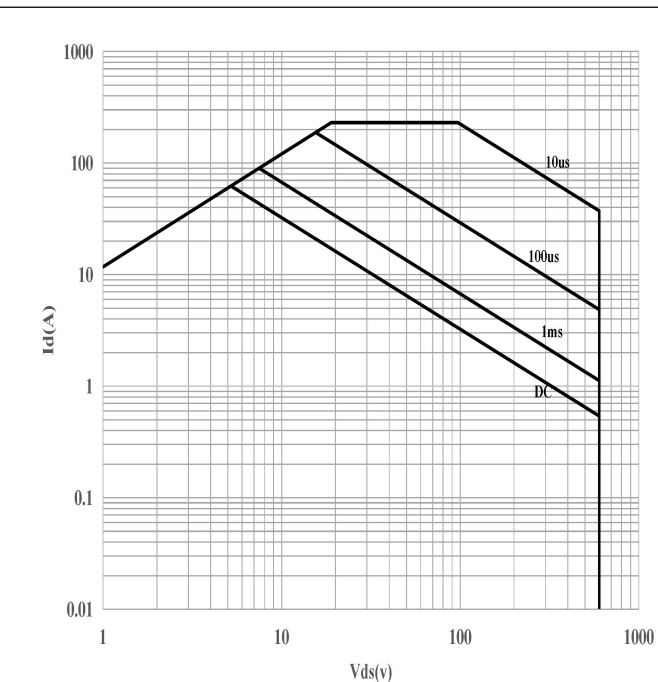
$$P_{tot} = f(T_C)$$

Figure 4: Max. Transient Thermal Impedance


$$Z_{(th)JC} = f(t_p); \text{ parameter: } D = t_p/T$$

Figure 5: Safe Operating Area


$$I_D = f(V_{DS}); T_c = 25^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$

Figure 6: Safe Operating Area


$$I_D = f(V_{DS}); T_c = 80^\circ\text{C}; V_{GS} > 7\text{V}; \text{ parameter } t_p$$

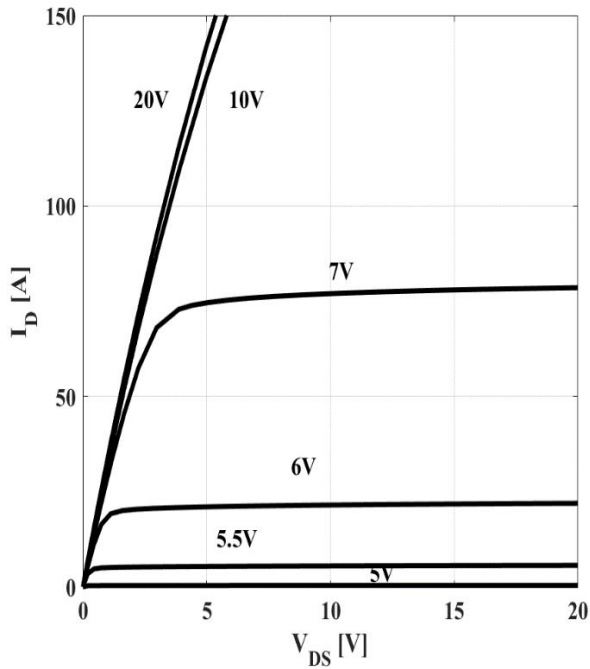
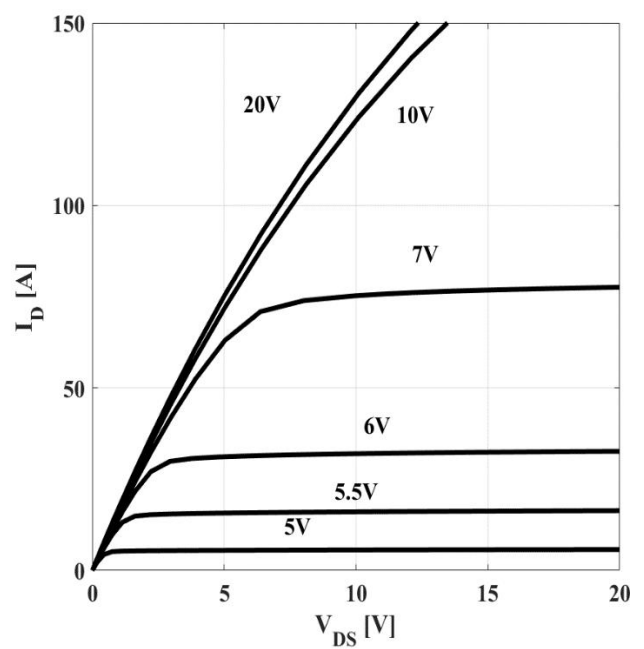
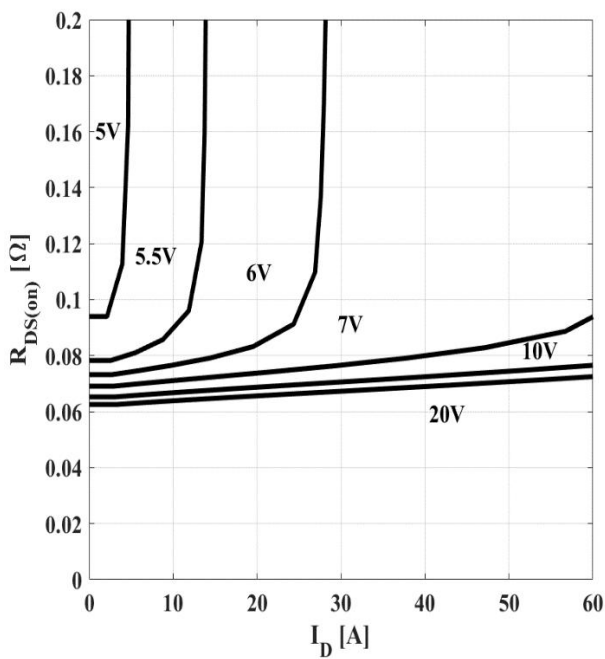
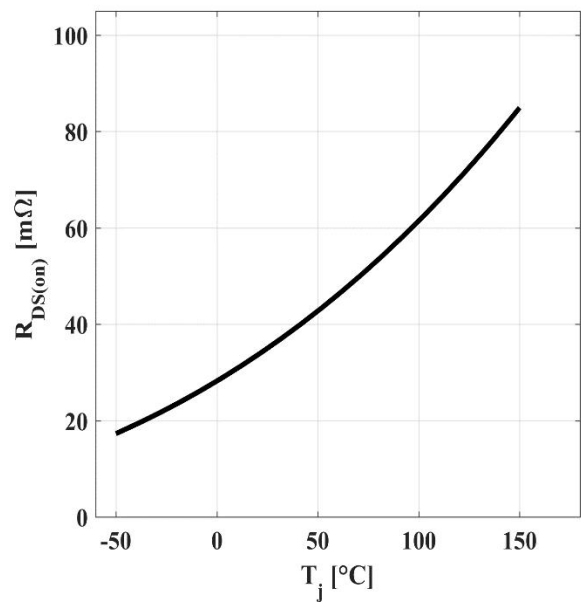
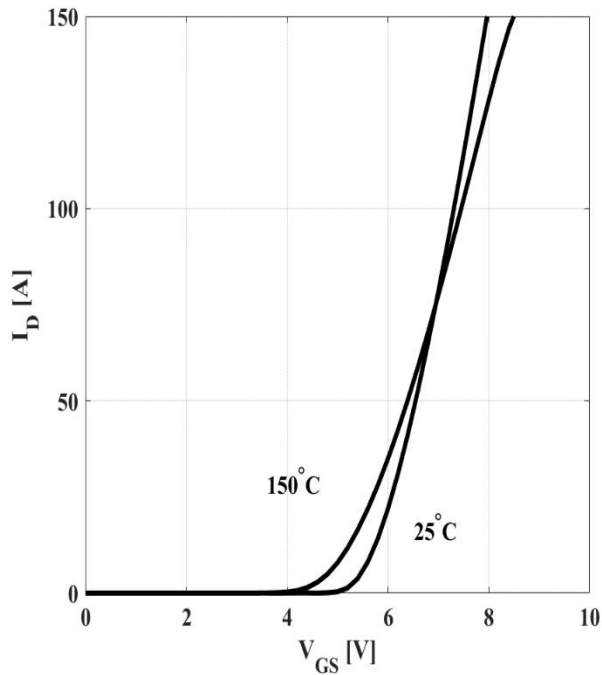
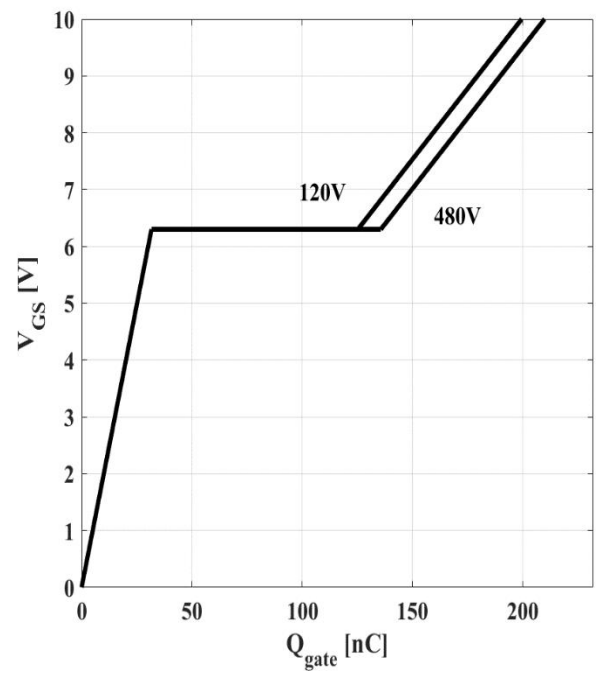
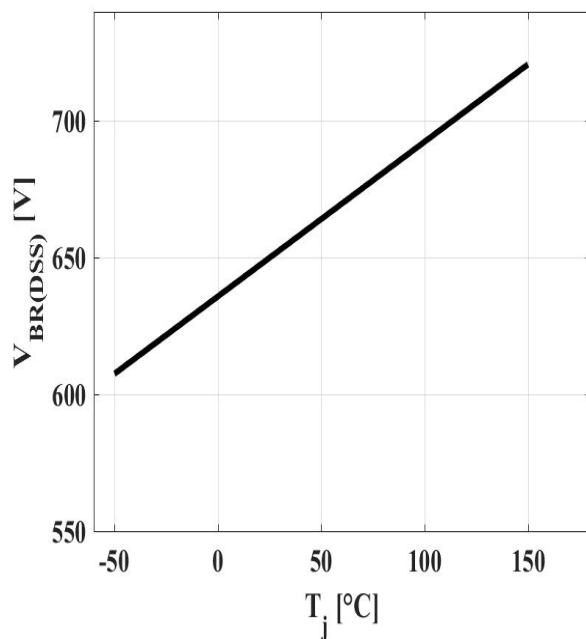
Figure 7: Typ. Output Characteristics

 $I_D = f(V_{DS}); T_j = 25^\circ\text{C}; \text{parameter: } V_{GS}$
Figure 8: Typ. Output Characteristics

 $I_D = f(V_{DS}); T_j = 125^\circ\text{C}; \text{parameter: } V_{GS}$
Figure 9: Typ. Drain-Source On-State Resistance

 $R_{DS(ON)} = f(I_D); T_j = 125^\circ\text{C}; \text{parameter: } V_{GS}$
Figure 10: Typ. Drain-Source On-State Resistance

 $R_{DS(ON)} = f(T_j); I_D = 34\text{A}; V_{GS} = 10\text{V}$

Figure 11: Typ. Transfer Characteristics


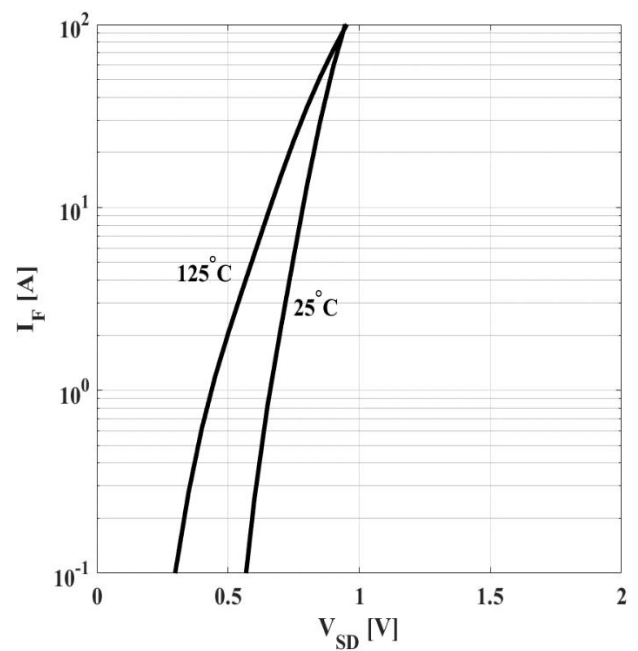
$$I_D = f(V_{GS}); V_{DS} = 20V$$

Figure 12: Typ. Gate Charge


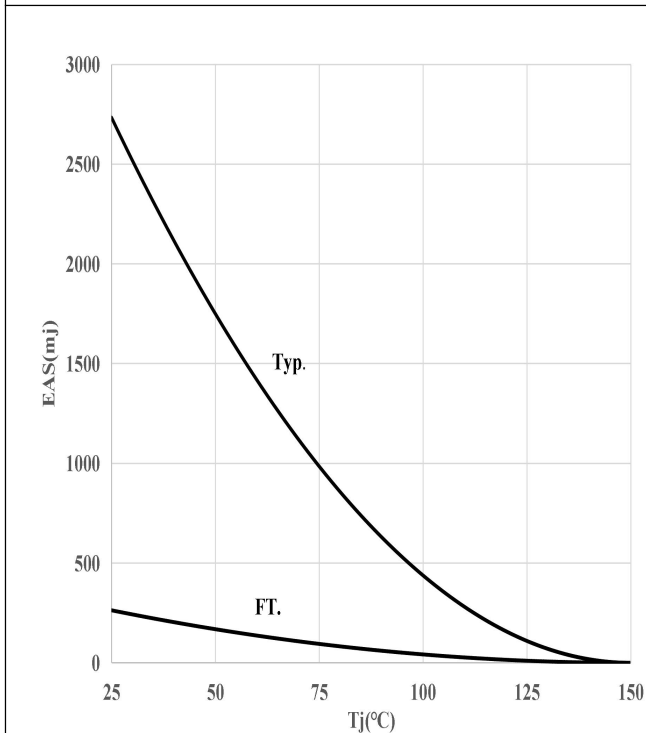
$$V_{GS} = f(Q_{gate}), I_D = 34A \text{ pulsed}$$

Figure 13: Drain-Source Breakdown Voltage


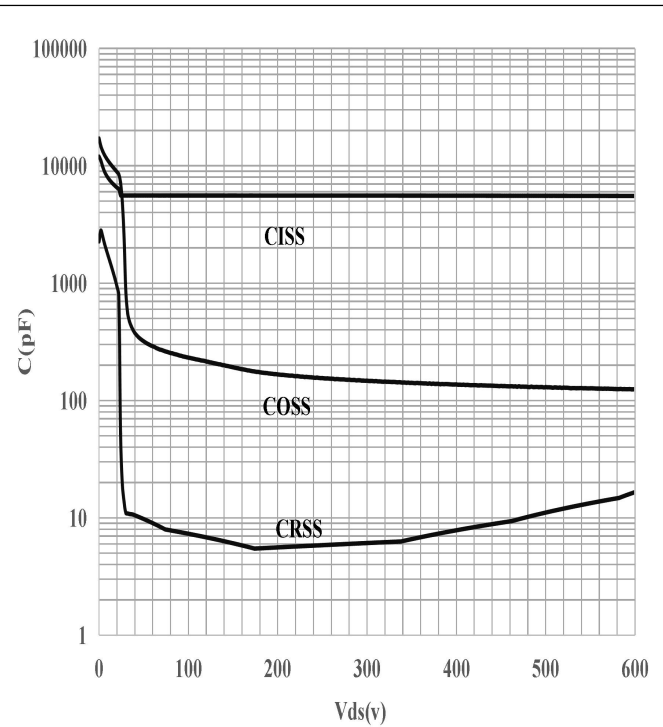
$$\text{Minimum } V_{BR(DSS)} = f(T_j); I_D = 10mA$$

Figure 14: Forward Characteristics of Reverse Diode


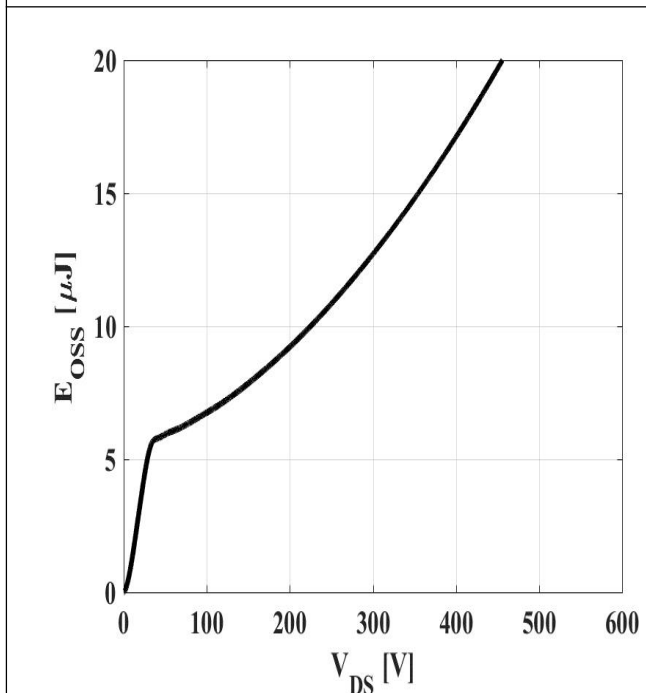
$$I_F = f(V_{SD}); \text{parameter: } T_j$$

40mΩ, 600V, Super Junction N-Channel Power MOSFET
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Figure 15: Avalanche Energy


$$E_{AS}=f(T_j); V_{DD}=60V$$

Figure 16: Typ. Capacitances


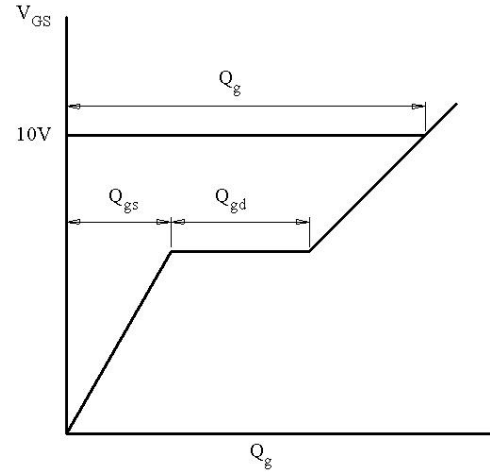
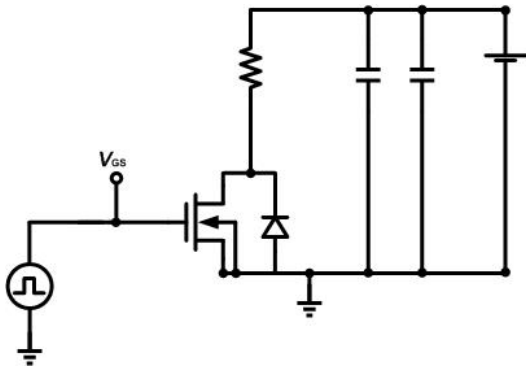
$$C=f(V_{DS}); V_{GS}=0; f=100kHz$$

Figure 17: Coss Stored Energy


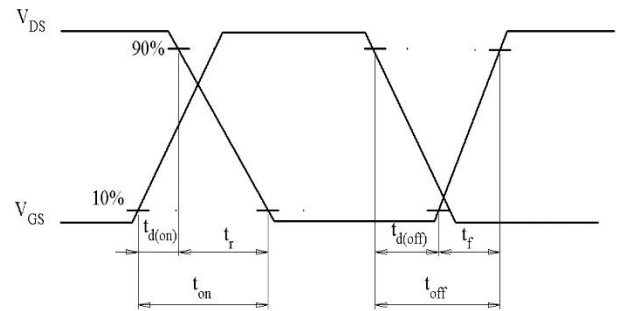
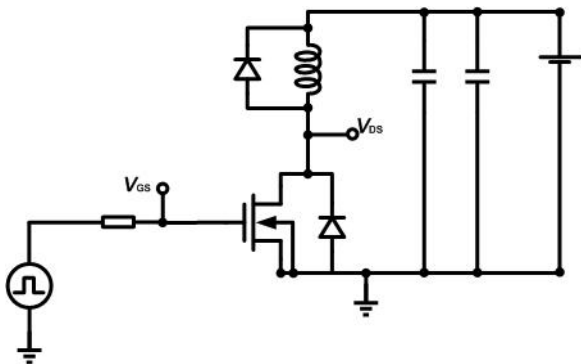
$$E_{oss}=f(V_{DS})$$

Test Circuits

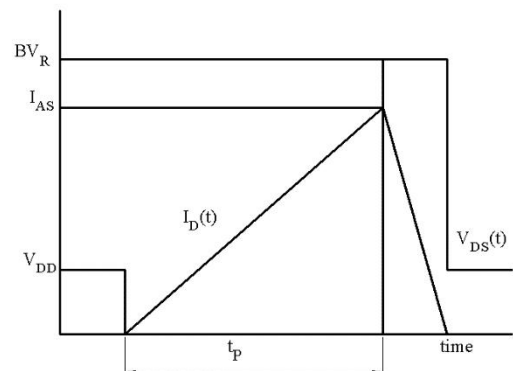
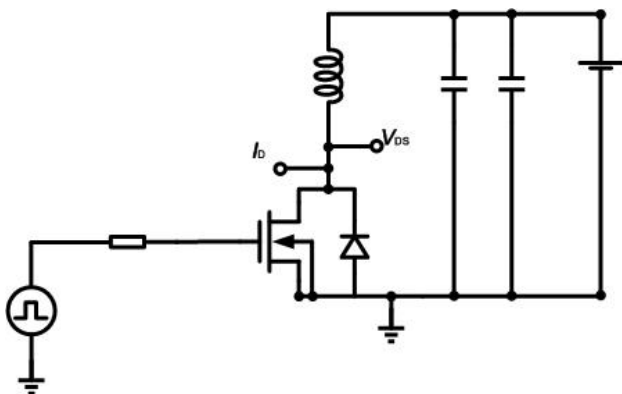
1. Gate Charge Test Circuit & Waveform



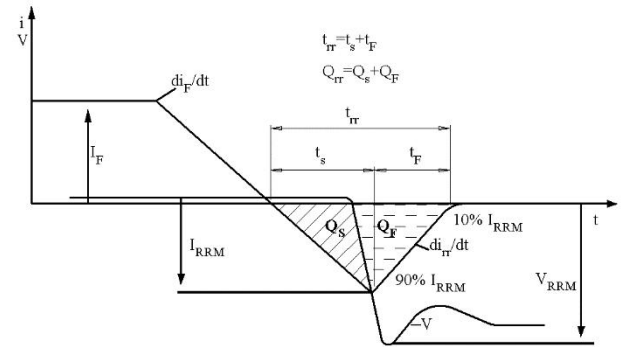
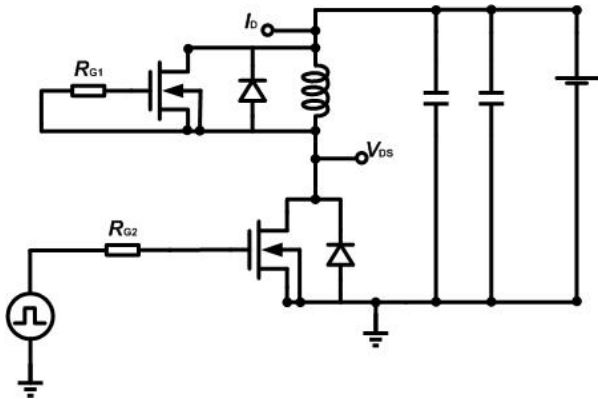
2. Switch Time Test Circuit



3. Unclaimed Inductive Switching Test Circuit & Waveforms



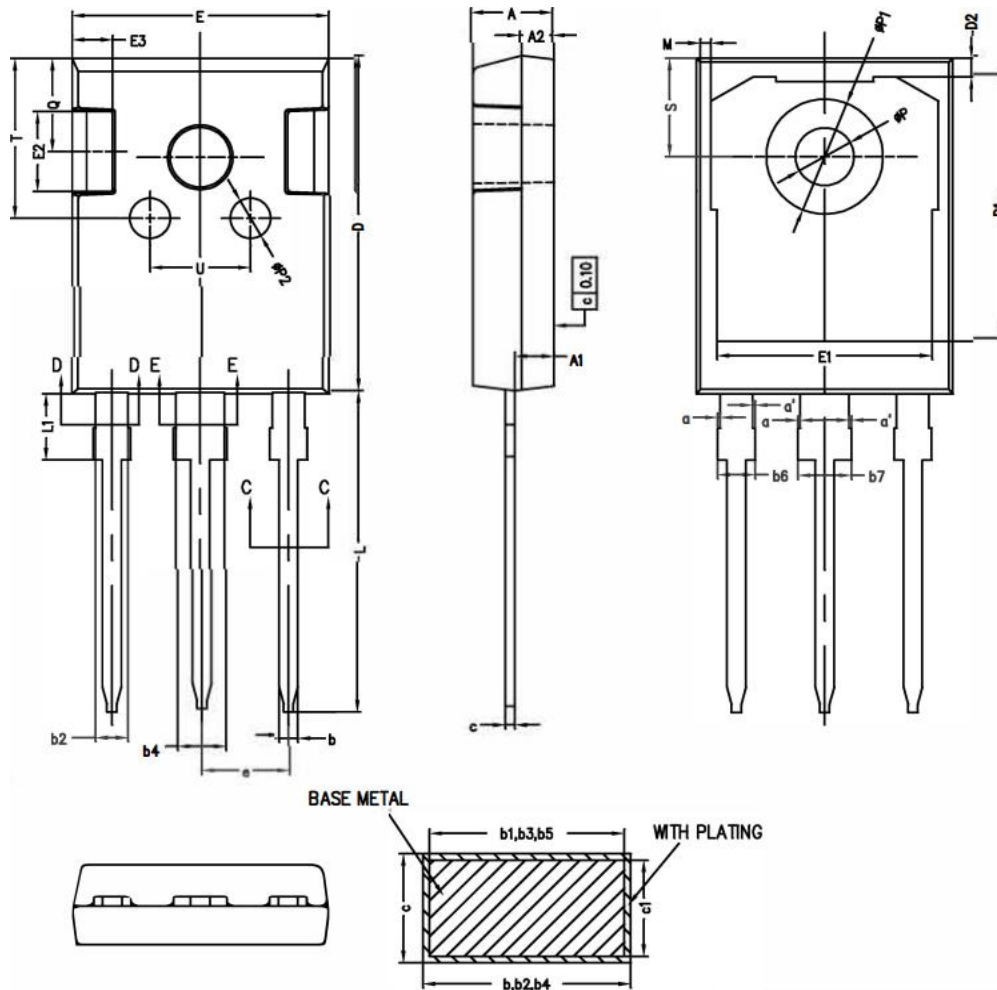
4. Test Circuit and Waveform for Diode Characteristics



Mechanical Dimensions

TO-247

Unit: mm



Symbol	Dimensions(mm)			Symbol	Dimensions(mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.90	5.00	5.10	E2	4.90	5.00	5.10
A1	2.31	2.41	2.51	E3	2.40	2.50	2.60
A2	1.90	2.00	2.10	e	5.34	5.44	5.54
b	1.12	-	1.22	L	19.80	19.92	20.10
b1	1.11	1.16	1.18	L1	3.95	4.13	4.30
b2	1.96	-	2.06	P	3.50	3.60	3.70
c	0.59	-	0.66	P1	7.00	-	7.40
D	20.90	21.00	21.10	P2	2.40	2.50	2.60
D1	16.25	16.55	16.85	Q	5.60	-	6.00
D2	1.05	1.20	1.35	S	6.05	6.15	6.25
E	15.70	15.80	15.90	T	9.80	-	10.20
E1	13.10	13.30	13.50	U	6.00	-	6.40



Shenzhen Sanrise Technology Co., LTD

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