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SPC-F005.DWG

REVISIONS

DOC. NO. SPC-F005 * Effective: 7/8/02 * DCP No: 1398

DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
1885	A	RELEASED	BYF	02/03/06	HO	2/6/06	JWM	2/6/06

Description:

Medium Power Plastic PNP, TO-126, Silicon Transistor. Designed for driver circuits, switching, and amplifier applications.

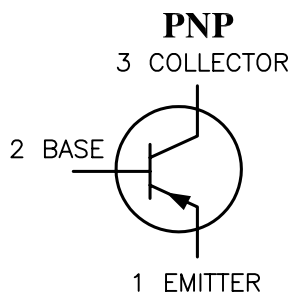


Features:

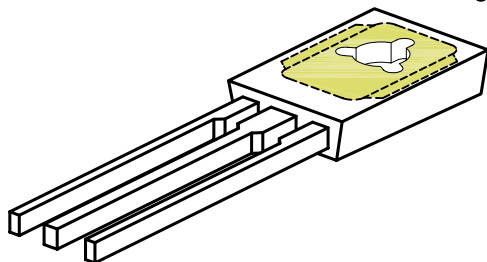
- Low Saturation Voltage: $V_{CE(sat)} 0.6V_{dc}$ $I_C = 1A$
- Excellent Power Dissipation Due to Thermopad Construction $P_D = 30$ @ $T_C = 25^\circ C$

Absolute Maximum Ratings:

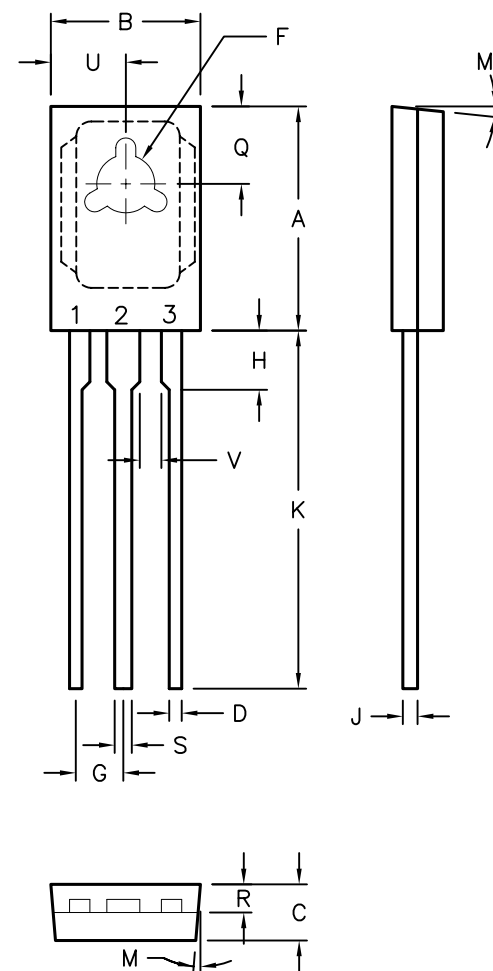
- Collector-Base Voltage, $V_{CBO} = 80V$
- Collector-Emitter Voltage, $V_{CEO} = 80V$
- Emitter-Base Voltage, $V_{EBO} = 5V$
- Continuous Collector Current, $I_C = 1A$
- Base Current, $I_B = 1A$
- Total Device Dissipation ($T_C = +25^\circ C$), $P_D = 30W$
Derate above $25^\circ C = 0.24mW/^\circ C$
- Operating Junction Temperature Range, $T_J = -65^\circ C$ to $+150^\circ C$
- Storage Temperature Range, $T_{stg} = -65^\circ C$ to $+150^\circ C$



STYLE 1
PIN 1. EMITTER
2. COLLECTOR
3. BASE



Dim	Min	Max
A	10.80	11.05
B	7.49	7.75
C	2.41	2.67
D	0.51	0.66
F	2.92	3.18
G	2.31	2.46
H	1.27	2.41
J	0.38	0.64
K	15.11	16.64
M	3	TYP
Q	3.76	4.01
R	1.14	1.40
S	0.64	0.89
U	3.68	3.94
V	1.02	-



DISCLAIMER:
ALL STATEMENTS AND TECHNICAL INFORMATION CONTAINED HEREIN ARE BASED UPON INFORMATION AND/OR TESTS WE BELIEVE TO BE ACCURATE AND RELIABLE. SINCE CONDITIONS OF USE ARE BEYOND OUR CONTROL, THE USER SHALL DETERMINE THE SUITABILITY OF THE PRODUCT FOR THE INTENDED USE AND ASSUME ALL RISK AND LIABILITY WHATSOEVER IN CONNECTION THEREWITH.

TOLERANCES:

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE FOR REFERENCE PURPOSES ONLY.

DRAWN BY:	DATE:
BASAM YOUSIF	02/03/06
CHECKED BY:	DATE:
HISHAM ODISH	2/6/06
APPROVED BY:	DATE:
JEEF MCVICKER	2/6/06

DRAWING TITLE:

Medium Power Transistor, Plastic, Silicon, PNP, TO-126

SIZE	DWG. NO.	ELECTRONIC FILE	REV
A	2N4920	01H1377.DWG	A
SCALE: NTS		U.O.M.: MILLIMETERS	SHEET: 1 OF 2

Electrical Characteristics: ($T_C = +25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Max	Unit
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OFF Characteristics

Collector–Emitter Breakdown Voltage (Note 1)	$V_{(BR)CEO}$	$I_C = 100\text{mA}, I_B = 0$	80	–	V
Collector Cut–Off Current	I_{CEX}	$V_{CE} = 80\text{V}, V_{EB(off)} = 1.5\text{V}$	–	0.1	mA
	I_{CEO}	$V_{CB} = 40\text{V}, I_B = 0$	–	0.5	mA
Collector Cut–Off Current	I_{CBO}	$V_{EB} = 80\text{V}, I_E = 0$	–	0.1	mA
Emitter Cut–Off Current	I_{EBO}	$V_{EB} = 5\text{V}, I_C = 0$	–	1	mA

ON Characteristics (Note 1)

DC Current Gain	h_{FE}	$V_{CE} = 1\text{V}, I_C = 50\text{mA}$	40	–	–
		$V_{CE} = 1\text{V}, I_C = 1500\text{mA}$	30	150	–
		$V_{CE} = 1\text{V}, I_C = 1\text{A}$	10	–	–
Collector–Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 1\text{A}, I_B = 100\text{mA}$	–	0.6	V
Base–Emitter Saturation Voltage	$V_{BE(on)}$	$I_C = 1\text{A}, I_B = 1\text{V}$	–	1.3	V
	$V_{BE(sat)}$	$I_C = 1\text{A}, I_B = 100\text{mA}$	–	1.3	V

Small-Signal Characteristics

Current Gain–Bandwidth Product	f_T	$V_{CE} = 10\text{V}, I_C = 250\text{mA}, f = 1\text{kHz}$	3	–	MHz
Output Capacitance	C_{obo}	$V_{CB} = 10\text{V}, I_E = 0, f = 100\text{kHz}$	–	100	pF
Input Impedance	h_{ie}	$V_{CE} = 10\text{V}, I_C = 1\text{mA}, f = 1\text{kHz}$	–	–	kOhm
		$V_{CE} = 10\text{V}, I_C = 10\text{mA}, f = 1\text{kHz}$	–	–	kOhm
Small–Signal Current Gain	h_{fe}	$V_{CE} = 10\text{V}, I_C = 250\text{mA}, f = 1\text{kHz}$	25	–	–

Note 1. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.