

Features

- Low Output Noise
- No Output Capacitor Required
- Tolerates Capacitive Loads
- Low Temperature Deviation: 4mV(typ)
- Low Temperature Coefficient of 100 ppm/°C (max)
- Fixed Reverse-Breakdown Voltages 1.24V
- Output Voltage Tolerance ($\pm 0.3\%$)
- Package appearance SOP-8

Applications

- Battery-Powered Equipment
- Data Acquisition Systems
- Precision Audio Components
- Instrumentation
- Process Control
- Energy Management
- Product Testing

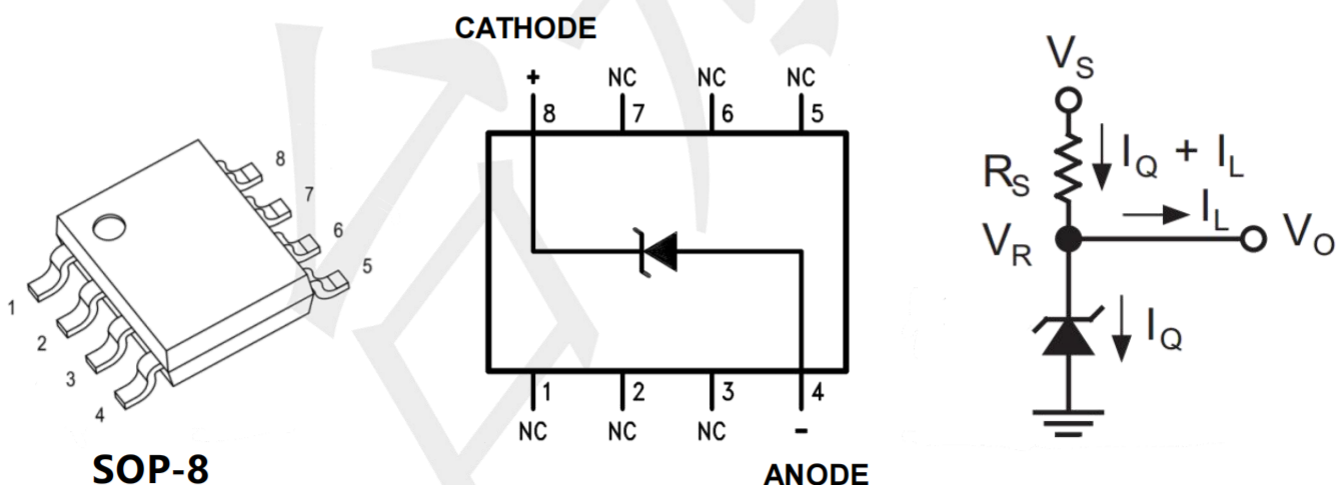
General Description

This is a parallel voltage reference with diverse functions, easy to use, and suitable for Various applications. The pin fixed output device does not require external capacitors Operable and stable when working with all capacitive loads. Here Moreover, this benchmark has low dynamic impedance, low noise, and low temperature coefficient, which can Ensure stable output voltage within a wide range of operating currents and temperatures.

This is packaged in SOP-8, which saves space,

The minimum current is 25 μ A (typical value), making it suitable for portable applications The ideal choice. Its rated operating temperature range is - 40 ° C to +125 ° C.

Circuit diagram and pin information



Note : Pin NC is attached to substrate and must be connected to ANODE or open.

Absolute Maximum Ratings

Within the range of $T_A=25\text{ }^{\circ}\text{C}$ (unless otherwise specified)

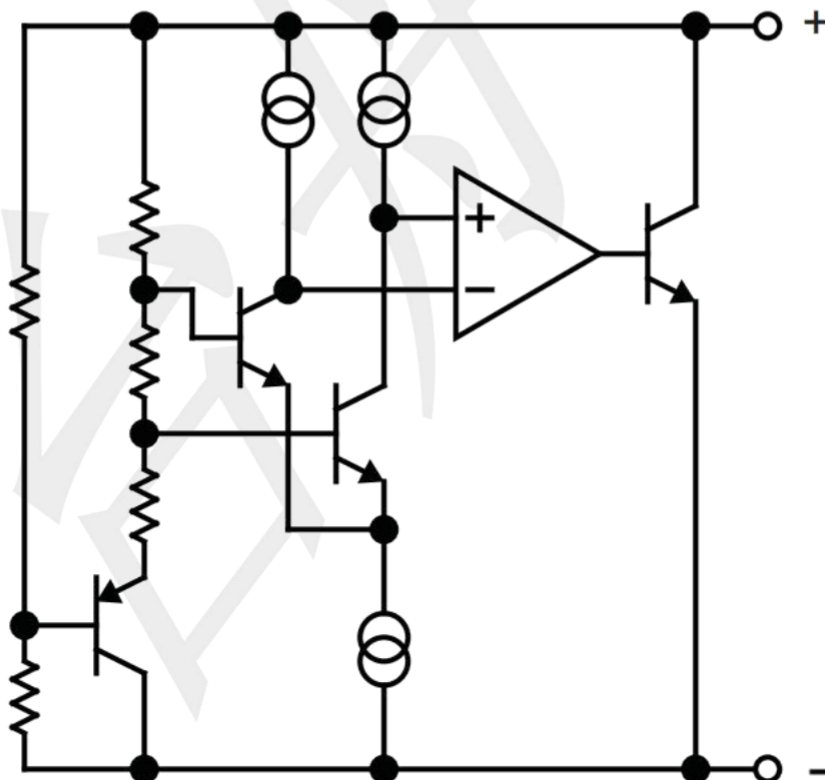
Parameter		Value	UNIT
P_M	Power Rating	0.25	W
R_C	Reverse Current	20	mA
F_C	Forward Current	10	mA
T_{OA}	Operating Ambient Temperature Range	$-40 \sim +125$	$^{\circ}\text{C}$
T_J	Operating virtual junction temperature	+150	$^{\circ}\text{C}$
T_{stg}	Storage temperature range	$-55 \sim +150$	$^{\circ}\text{C}$
ESD	Human Body Model	6	KV
ESD	Machine Model	0.4	KV

(1) Exceeding the absolute maximum rated pressure listed may result in permanent damage to the equipment. These are only rated stresses and do not imply any of them or any of them

The functional operation of the equipment under his conditions (beyond the conditions indicated under the 'recommended working conditions'). Long term exposure to absolute maximum rated conditions can It can affect the reliability of the equipment.

(2) Unless otherwise specified, all voltage values are related to the anode.

BLOCK DIAGRAM



Electrical Characteristics

($I_R = 100\mu A$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted. Typical values are at $T_A = +25^\circ C$.) (Note 1)

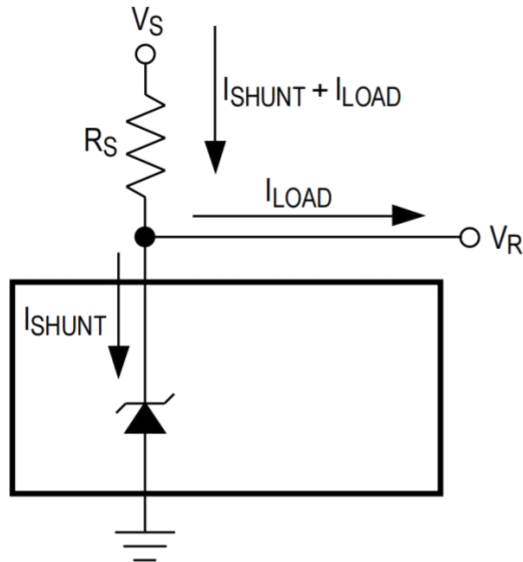
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Reverse Breakdown Voltage	V_R	$T_A = +25^\circ C$	1.200	1.240	1.280	V
Reverse Breakdown Voltage Tolerance (Note 2)	V_R		--	± 4.0	± 19	mV
Minimum Operating Current	I_{RMIN}		--	25	65	μA
Average Reverse Voltage Temperature Coefficient (Notes 2 and 3)	$\Delta V_R / \Delta T$	$I_R = 10mA$		± 20		ppm/ $^\circ C$
		$I_R = 1mA$	--	± 15	± 100	
		$I_R = 100\mu A$		± 15		
Reverse Breakdown Voltage Change with Operating Current Change		$I_{RMIN} \leq I_R \leq 1mA$	--	0.3	1.0	mV
		$1mA \leq I_R \leq 15mA$	--	2.5	8.0	
Reverse Dynamic Impedance (Note 3)	Z_R	$I_R = 1mA$, $f = 120Hz$, $I_{AC} = 0.1I_R$	--	0.3	0.8	Ω
Wide band Noise	e_N	$I_R = 100\mu A$, $10Hz \leq f \leq 10kHz$		60		μV_{RMS}
Reverse Breakdown Voltage Long-Term Stability	ΔV_R	$T = 1000h$		20		ppm

Note 1: All devices are 100% production tested at $T_A = +25^\circ C$ and are guaranteed by design for $T_A = T_{MIN}$ to T_{MAX} , as specified.

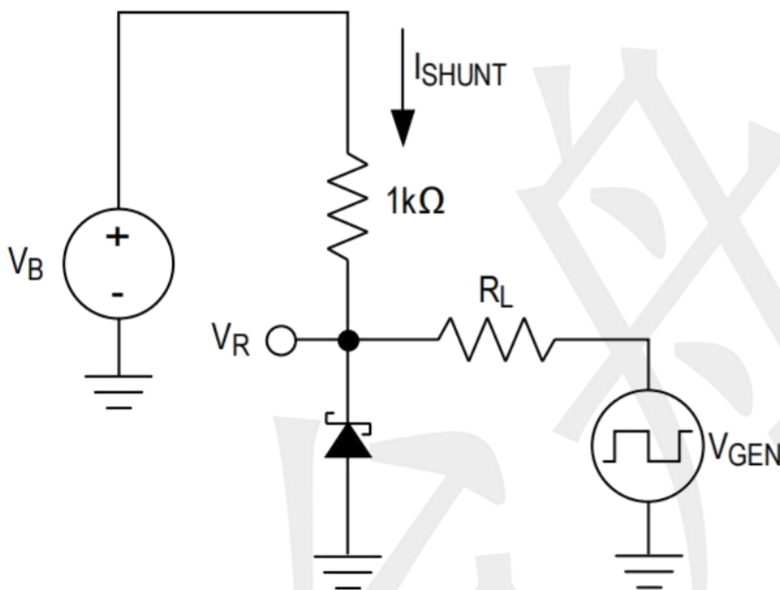
Note 2: The over temperature limit for Reverse Breakdown Voltage Tolerance is defined as the room-temperature Reverse Breakdown Voltage Tolerance $\pm[(\Delta V_R / \Delta T)(\max \Delta T)(V_R)]$, where $\Delta V_R / \Delta T$ is the VR temperature coefficient, $\max \Delta T$ is the maximum difference in temperature from the reference point of $+25^\circ C$ to T_{MIN} or T_{MAX} , and V_R is the reverse breakdown voltage.

Note 3: Guaranteed by design.

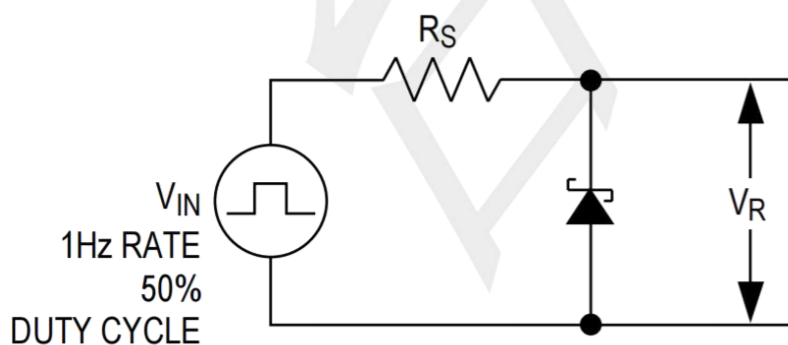
Typical Application Circuits



Typical Operating Circuit



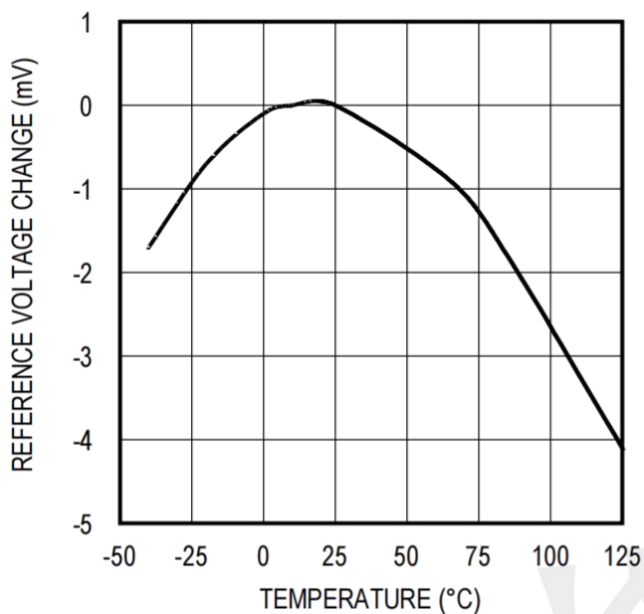
Load-Transient Test Circuit



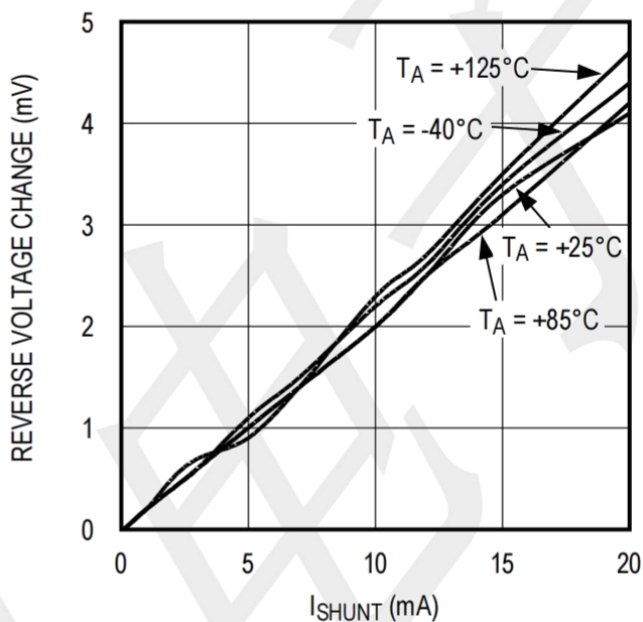
Startup Characteristics Test Circuit

Typical Operating Characteristics

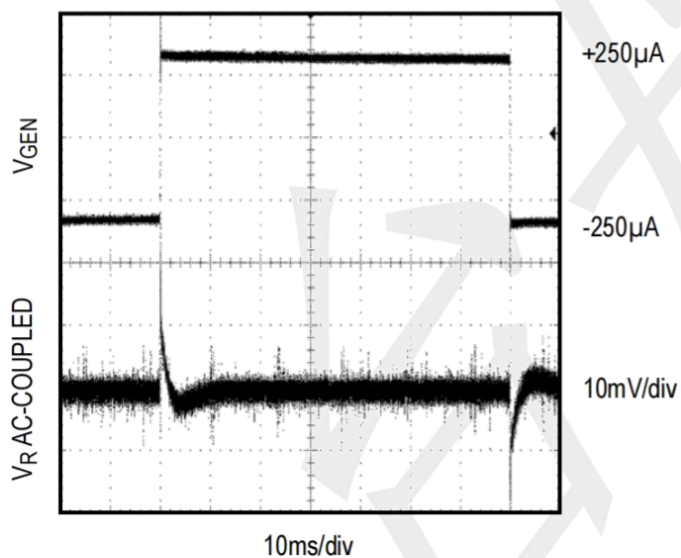
TEMPERATURE DRIFT



REVERSE VOLTAGE vs. I_{SHUNT}

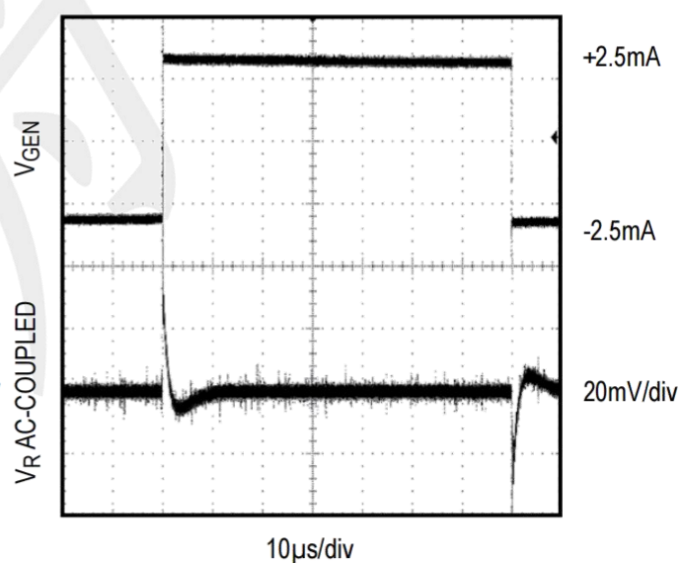


LOAD-TRANSIENT RESPONSE



$I_{SHUNT} = 1\text{mA} \pm 250\mu\text{A}$
 $R_L = 10\text{k}\Omega$, SEE FIGURE 1

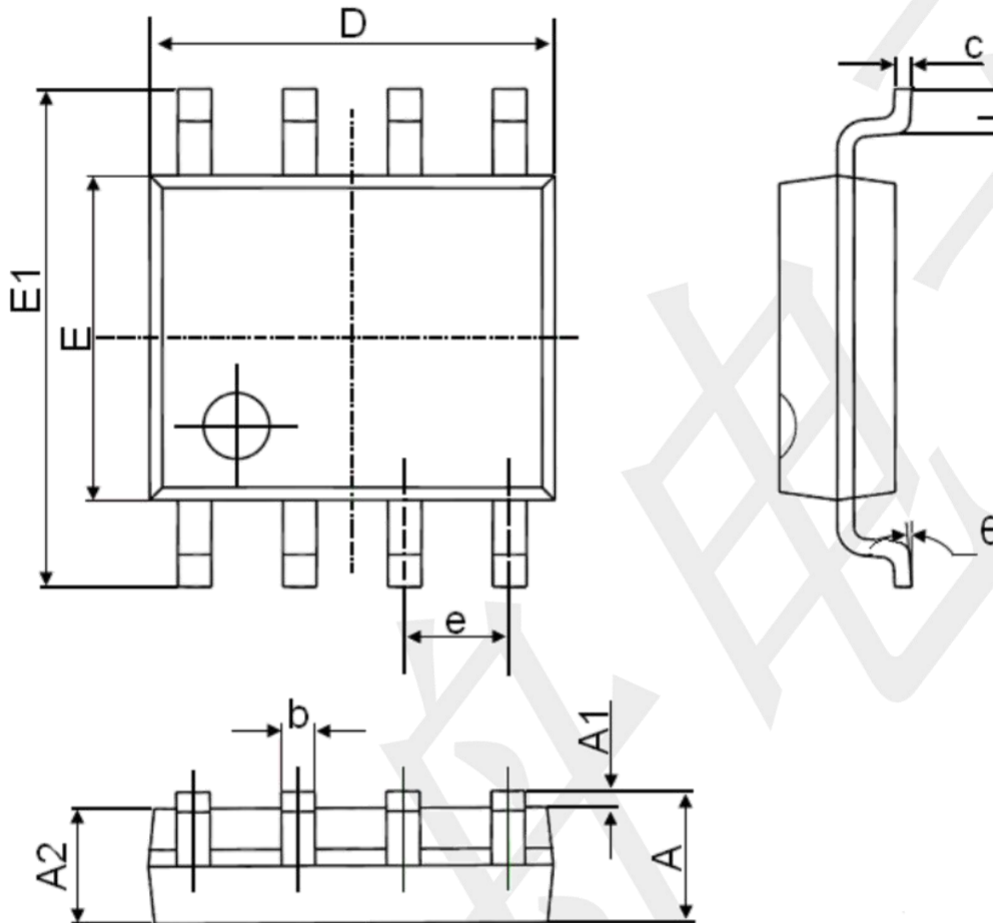
LOAD-TRANSIENT RESPONSE



$I_{SHUNT} = 10\text{mA} \pm 2.5\text{mA}$
 $R_L = 1\text{k}\Omega$, SEE FIGURE 1

Package Outline Dimensions

SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°