

MAX5717 Evaluation Kit

Evaluates: MAX5717/MAX5719

General Description

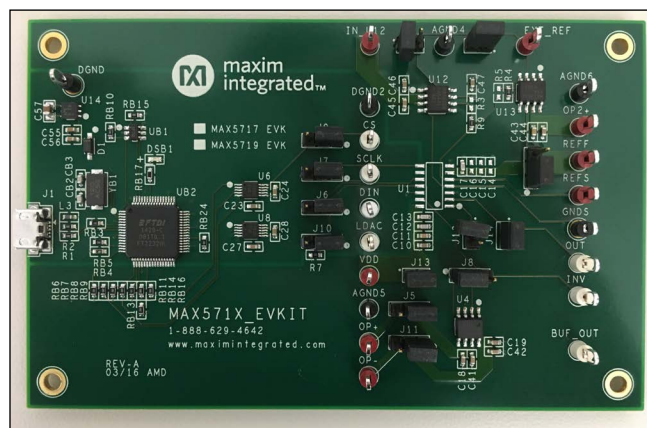
The MAX5717/MAX5719 evaluation kit (EV kit) demonstrates the MAX5717/MAX5719, 16/20-bit DAC. The EV kit includes a graphical user interface (GUI) that provides communication over SPI with an on-board master IC.

The MAX5717 EV kit comes with the MAX5717GSD+ installed and the MAX5719 comes with the MAX5719GSD+ installed.

Features

- On-Board SPI Controller
- On-Board Output Buffer (MAX9632)
- On-Board +4.096V Reference Voltage (MAX6126)
- Windows XP®, Windows® 7/8/8.1/10-Compatible Software

Board Photo



Quick Start

Required Equipment

- MAX5717/MAX5719 EV kit (includes micro-USB cable)
- Windows PC
- Digital Oscilloscope

Note: In the following sections, software-related items are identified by **bolding**. Text in **bold** refers to items directly from the EV kit software. Text in **bold and underlined** refers to items from the Windows operating system.

Procedure

The EV kit is fully assembled and tested. Follow the steps below to verify board operation:

- 1) Visit [HERE](#) to download the latest version of the EV kit software, *5717EVKit.ZIP*. Save the EV kit software to a temporary folder and uncompress the ZIP file.
- 2) Verify that all jumpers on the EV kit are in their default positions, as shown in [Table 1](#).
- 3) Connect a probe from the oscilloscope at the OUT test point.
- 4) Connect the USB cable from the PC to the MAX5717/MAX5719 EV kit board.
- 5) Open the EV kit GUI, *MAX5717EVKit.exe* and select **Device->MAX5717PMB** option (or MAX5719PMB).
- 6) Click the **Scan Adapters** button. Then select the option **PMODxxxxxx** (where xxxxxx is numeric) and click the **Connect** button. See [Figure 1](#) and [Figure 2](#).
- 7) Click the **Sample Once** button and verify the oscilloscope waveform form matches that of the GUI.

[Ordering Information](#) appears at end of data sheet.

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Windows XP is a registered trademark and registered service mark of Microsoft Corporation.

Table 1. Jumper Descriptions

JUMPER	SHUNT POSITION	DESCRIPTION
J2	Installed	Used for a buffered reference voltage. Connects the output voltage from the MAX6126 to the input of the MAX9632.
	Not installed*	Disconnects the output voltage from the MAX6126 to the input of the MAX9632.
J3	Installed*	Powers the MAX6126 using USB supply.
	Not installed	Disconnects the USB supply from the MAX6126. User must apply 5V at the IN_U12 test point to use the MAX6126.
J4	1-2*	Connects the output voltage from the MAX6126.
	2-3	User-Supplied Voltage Reference. Connects to the MAX9632 output. User must apply voltage reference at the EXT_REF test point and 5V at the OP2+ test point to power the MAX9632 (U13).
J5	1-2*	Powers the MAX9632's V _{CC} pin using USB supply.
	2-3	User-supplied power to the MAX9632 (U4). Apply 5V to 15V at the OP+ test point. Recommended for use in bipolar mode.
J6	1-2*	DIN from on-board master.
	2-3	User-supplied DIN. Apply respective signal at DIN test point.
J7	1-2*	SCLK from on-board master.
	2-3	User-supplied SCLK. Apply respective signal at SCLK test point.
J8	1-2*	Connects the negative input to the output of the MAX9632.
	2-3	Connects the MAX5717/MAX5719's INV pin to the negative input of the MAX9632.
J9	1-2*	CS from on-board master.
	2-3	User-Supplied CS. Apply respective signal at CS test point.
J10	1-2*	LDAC from On-Board Master.
	2-3	User-Supplied LDAC. Apply respective signal at LDAC test point.
J11	1-2	Do Not Use
	2-3*	User-Supplied Power to the MAX9632 (U4). Apply -5V to -15V at the OP- test point. Recommended for use in bipolar mode.
J12	Installed*	Connects the MAX5717/MAX5719's RFB pin to the MAX9632's output.
	Not installed	Disconnects the MAX5717/MAX5719's RFB pin to the MAX9632's output.
J13	Installed*	Powers the MAX5717/MAX5719 using the USB power.
	Not installed	User-supplied power. Apply 5V at the V _{DD} test point.
J14	Installed*	Connects the MAX5717/MAX5719's OUT pin to the MAX9632's IN+ pin.
	Not installed	Disconnects the MAX5717/MAX5719's OUT pin to the MAX9632's IN+ pin.

*Default position.

General Description of Software

The main window of the MAX5717/MAX5719 EV kit software contains controls to evaluate the MAX5717 and MAX5719 ICs. Included is a waveform generator that allows the user to quickly evaluate the device.

USB2PMB Adapter

The controls within the **USB2PMB** groupbox allow the user to select the appropriate USB2PMB devices. When **Scan Adapters** button is pressed, it will update the dropdown list with all USB2PMB devices. With the EV kit connected to the PC, **PMODxxxxxx** (where xxxxxx is numeric) will appear within the dropdown list. Make the appropriate selection respective of the IC and press on the **Connect** button.

Sample

The Sample groupbox contains the sample rate and SPI SCLK options applicable to EV kit. Sample rate ranges from 5000sps to 200000sps. The SPI SCLK is selectable from 80kHz to 15MHz. Once configured, the user can either press the Sample Once or Sample Continuous button.

Transfer Function

The EV kit can work in either unipolar or bipolar mode. Please refer to Unipolar and Bipolar section of the data sheet.

Plot Configuration

The Scope display can plot the waveform generator's data in units of LSBs or voltage in a time domain. In addition, provides an FFT plot of the raw data for the MAX5717 or MAX5719.

Signal Setup

The **Signal Setup** controls are similar to a function generator and can be used to quickly evaluate the EV kit. It provides waveforms in sine, left and right sawtooth, triangle, square, and white noise. Amplitude, Offset, and Frequency can be adjusted for each waveform.

General Description of Hardware

The MAX5717/MAX5719 EV kit demonstrates the MAX5717/MAX5719, 16/20-bit DAC. The EV kit includes an on-board master IC for all SPI and I/O communication.

User-Supplied SPI

To evaluate the EV kit with a user-supplied SPI bus, place shunts on the 2-3 position of jumper J6, J7, J9, and J10. Apply the user-supplied SPI signals to the SCLK, CS, DIN, and LDAC test points. Make sure the return ground is the same as the MAX5717/MAX5719's ground.

User-Supplied V_{DD}

The MAX5717/MAX5719 is powered through USB by default. For user-supplied V_{DD}, remove the shunt of the jumper J13 and apply +5V at the V_{DD} test point.

User-Supplied Power for Voltage Reference (U12)

The voltage reference is powered through USB by default. For user-supplied power, remove the shunt of the jumper J3 and apply +5V at the IN_U12 test point.

User-Supplied Voltage Reference

The MAX9632 (U13) is an optional buffer for the reference of the MAX5717/MAX5719. Apply only +5V to the OP2+ test point when a voltage reference is applied at the EXT_REF test point.

User-Supplied Power for Buffer (U4)

The MAX9632 is an optional buffer for the output of the MAX5717/MAX5719. Place shunts on the 2-3 position of jumpers J5 and J11. Apply only +5V to +15V at the OP+ test point and -5V to -15V at the OP- test point.

Unipolar and Bipolar Output

When in unipolar output and the output buffer is in use, a shunt should be placed in the 2-3 position of jumper J5, 1-2 position of the jumper J8, 2-3 position of jumper J11, and open position of jumper 12. When in bipolar output, a shunt should be placed in the 2-3 position of jumper J5, 2-3 position of the jumper J8, 2-3 position of jumper J11, and closed position of jumper J12.

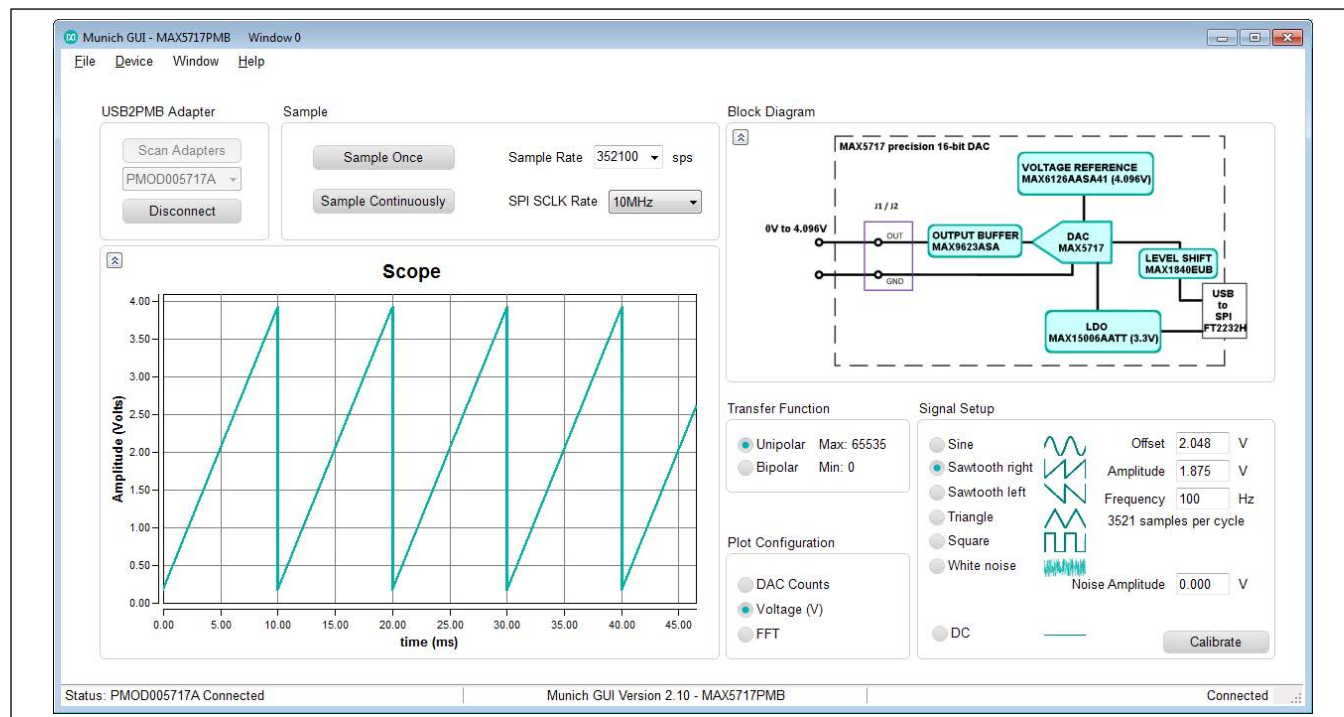


Figure 1. MAX5717 EV Kit Main Window

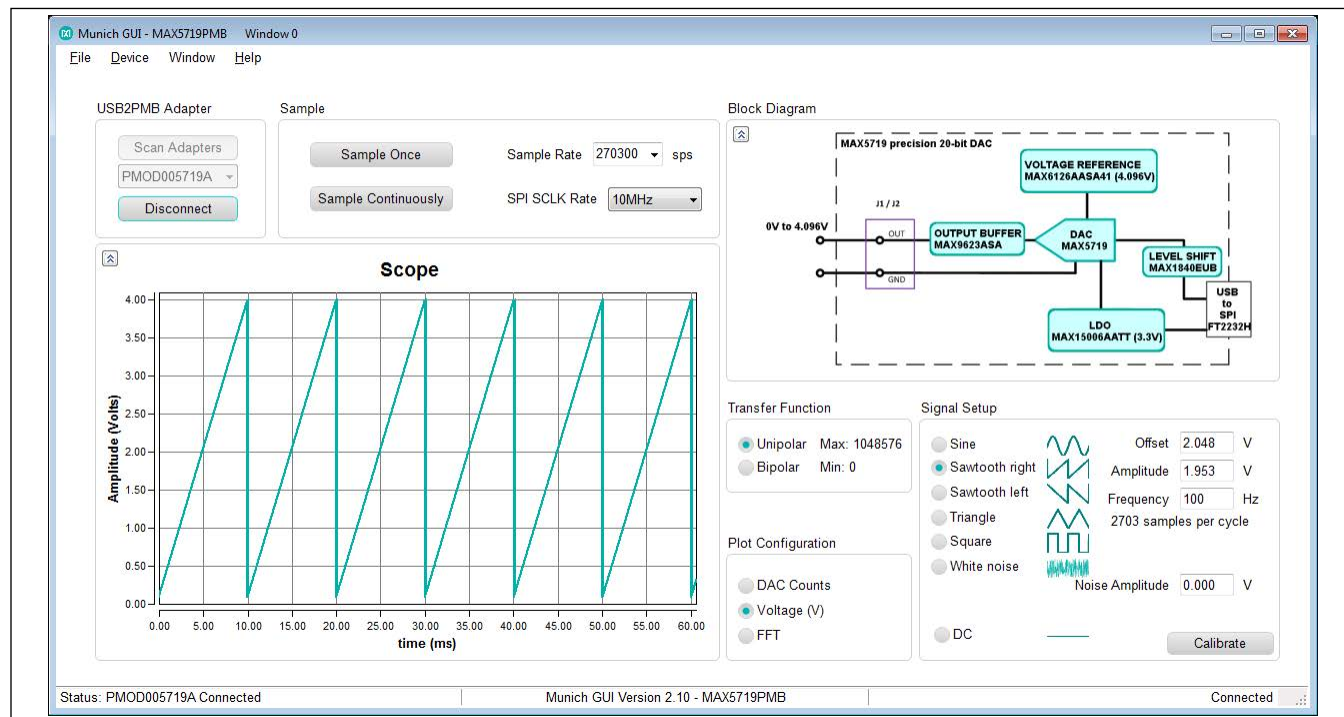


Figure 2. MAX5719 EV Kit Main Window

Component Information, PCB Layout, and Schematics

See the following links for component information, PCB layout diagrams, and schematic.

- [MAX5717/MAX5719 EV BOM](#)
- [MAX5717/MAX5719 EV PCB Layout](#)
- [MAX5717/MAX5719 EV Schematic](#)

Ordering Information

Part	TYPE
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MAX5719EVKIT#	EV KIT

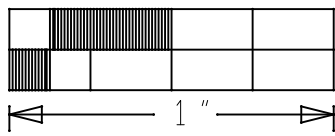
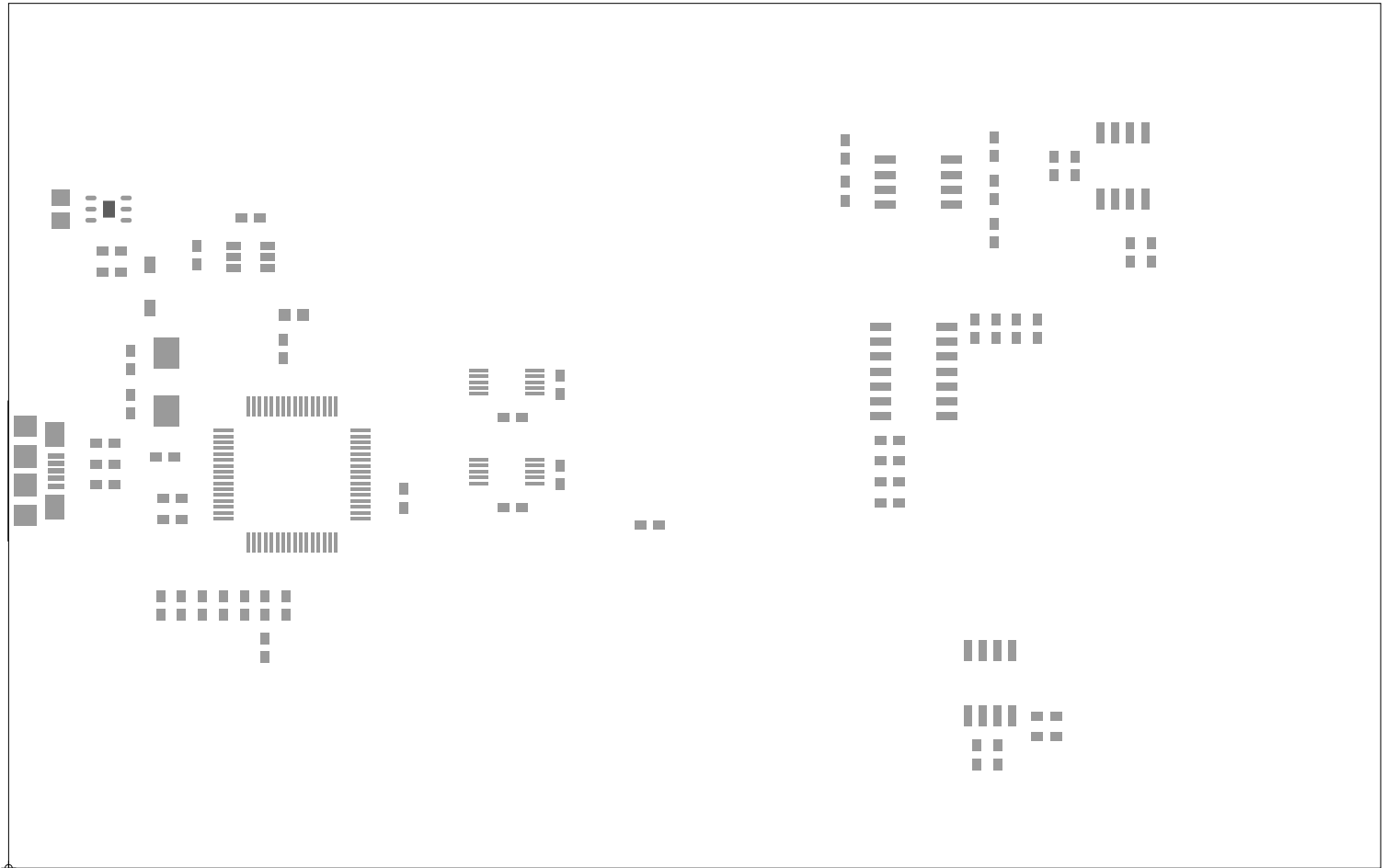
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Revision History

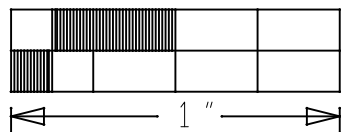
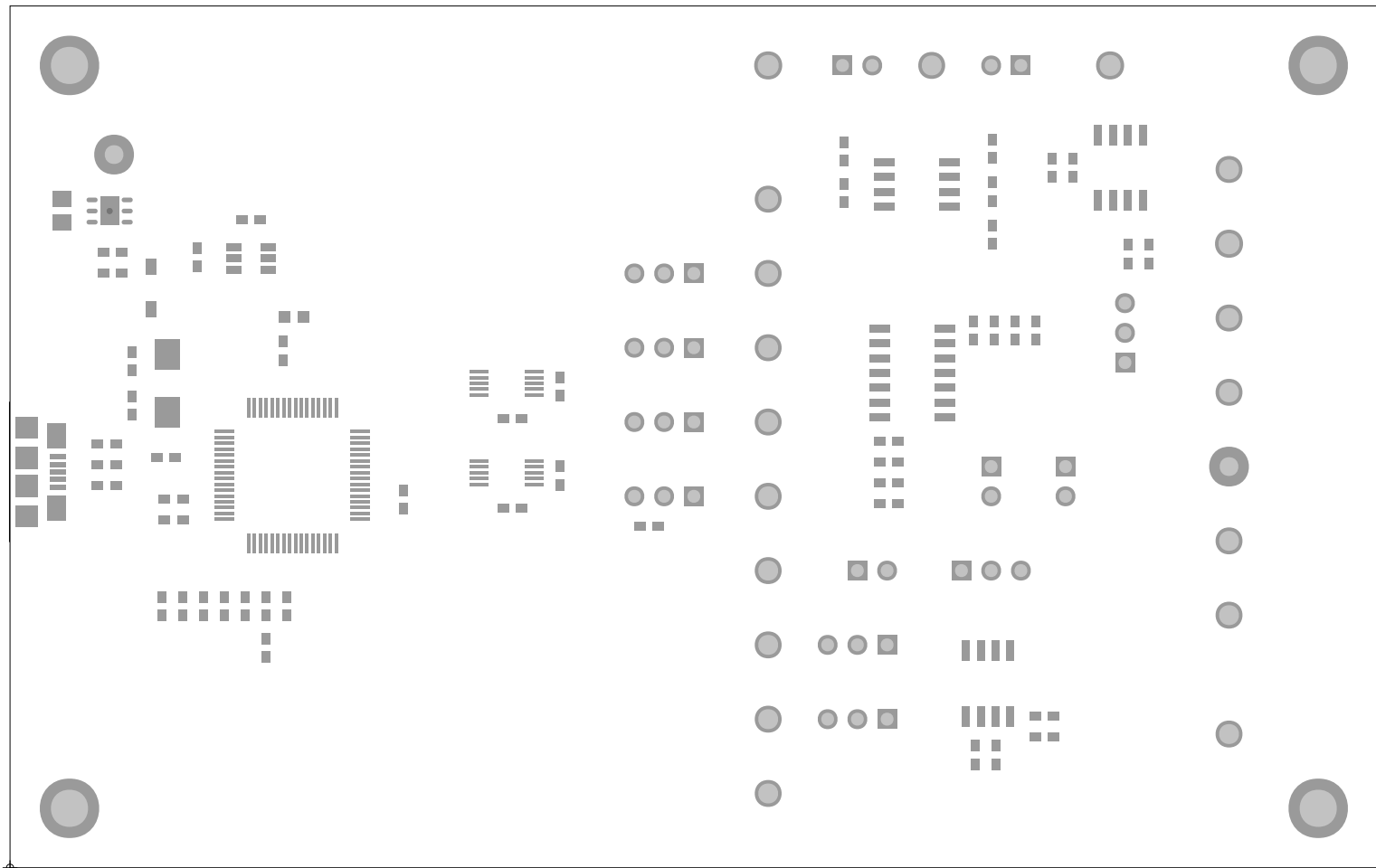
REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	6/16	Initial Release	—

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

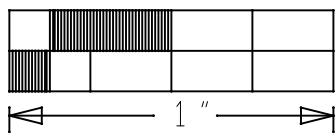
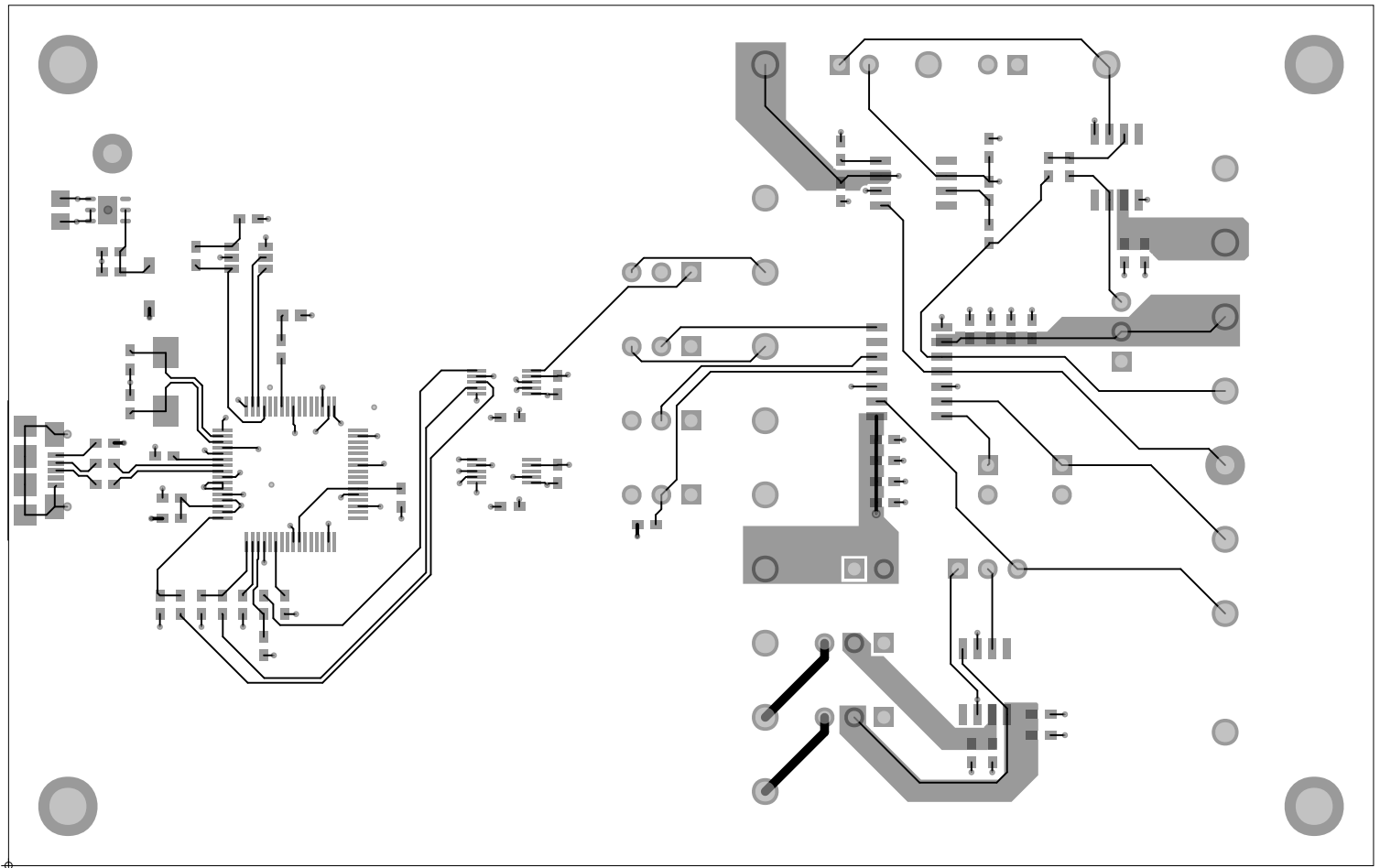
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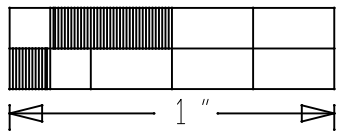
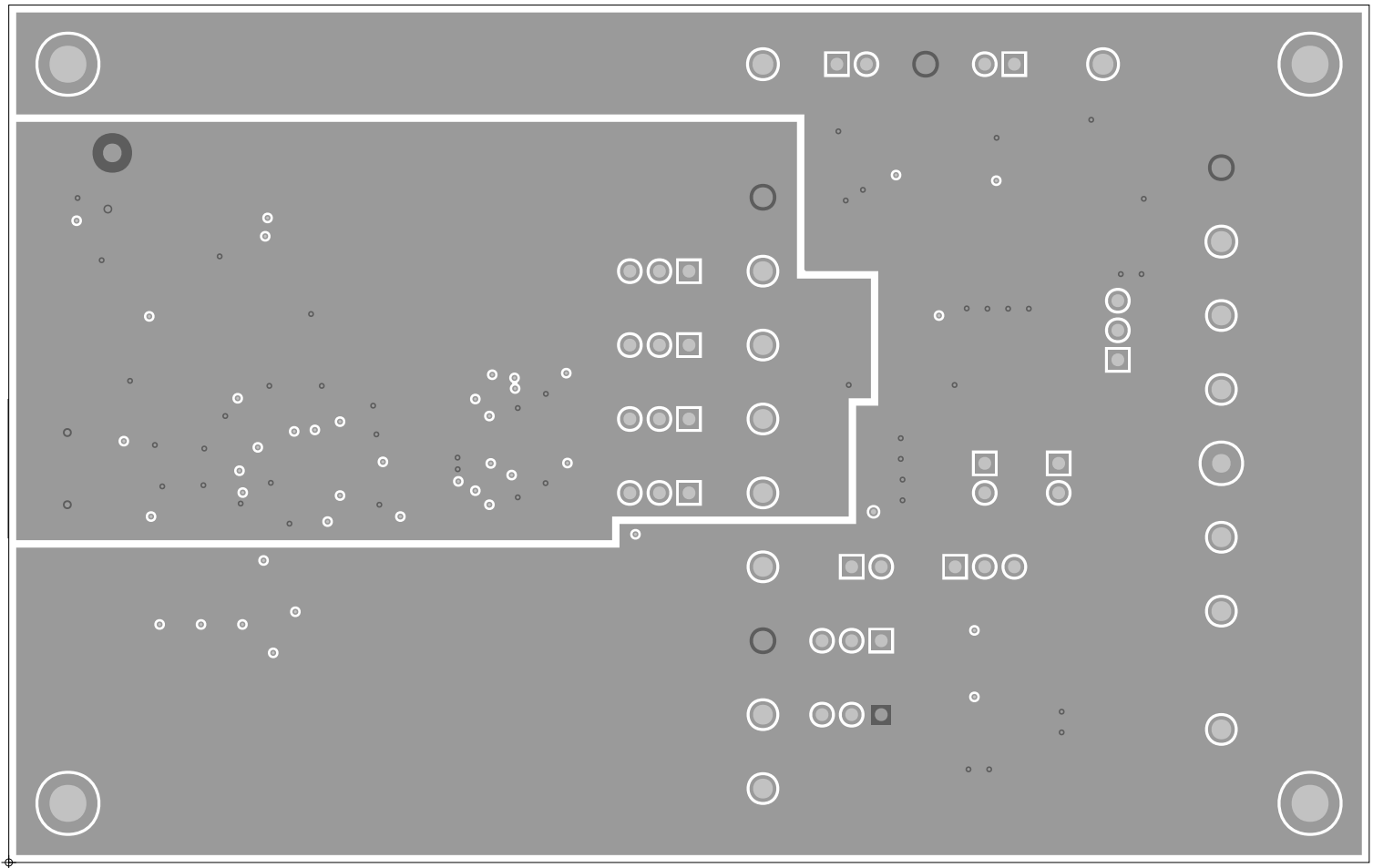
TOP PASTE



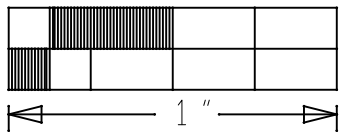
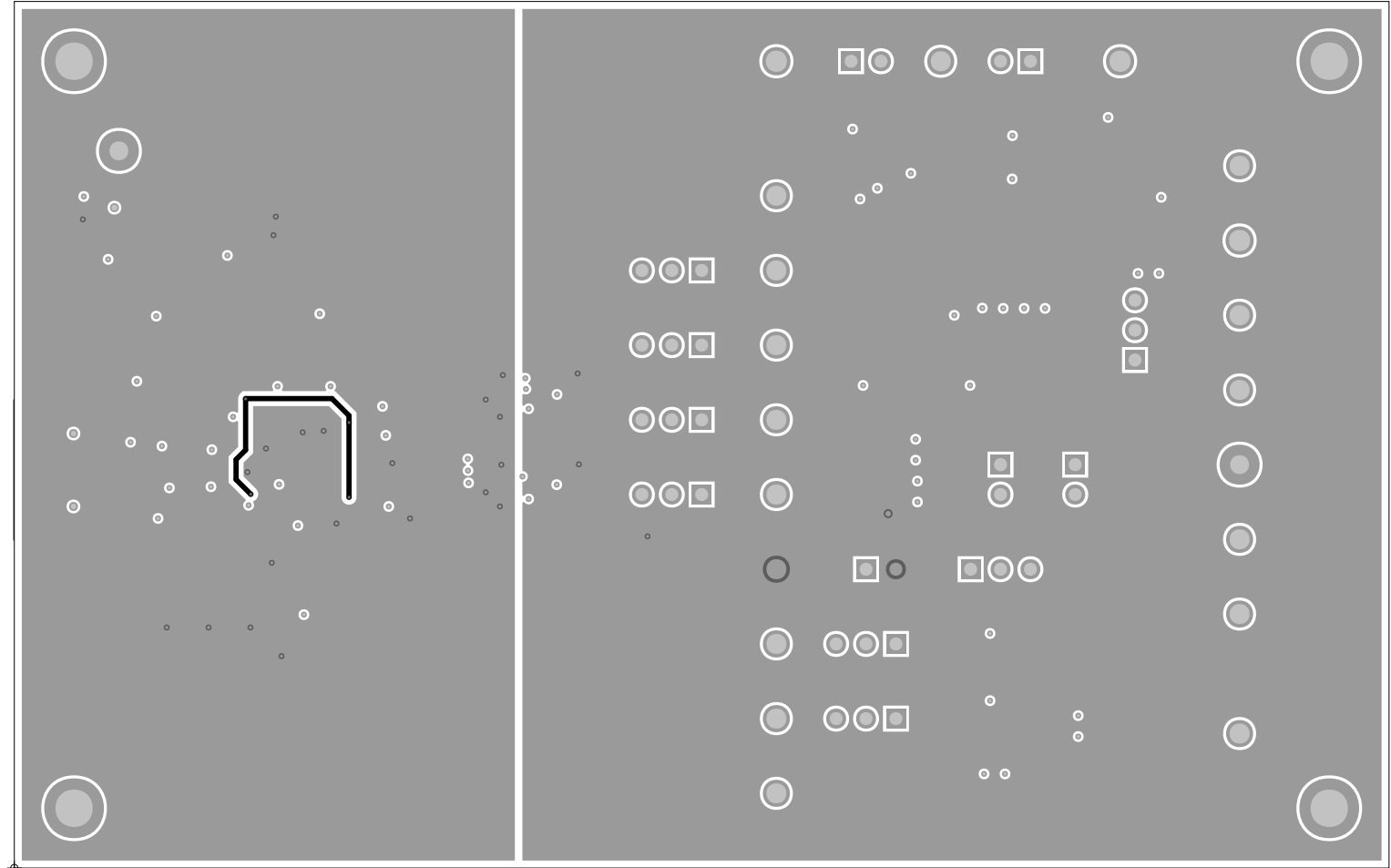
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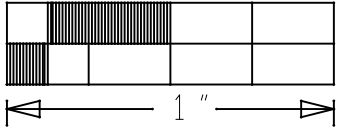
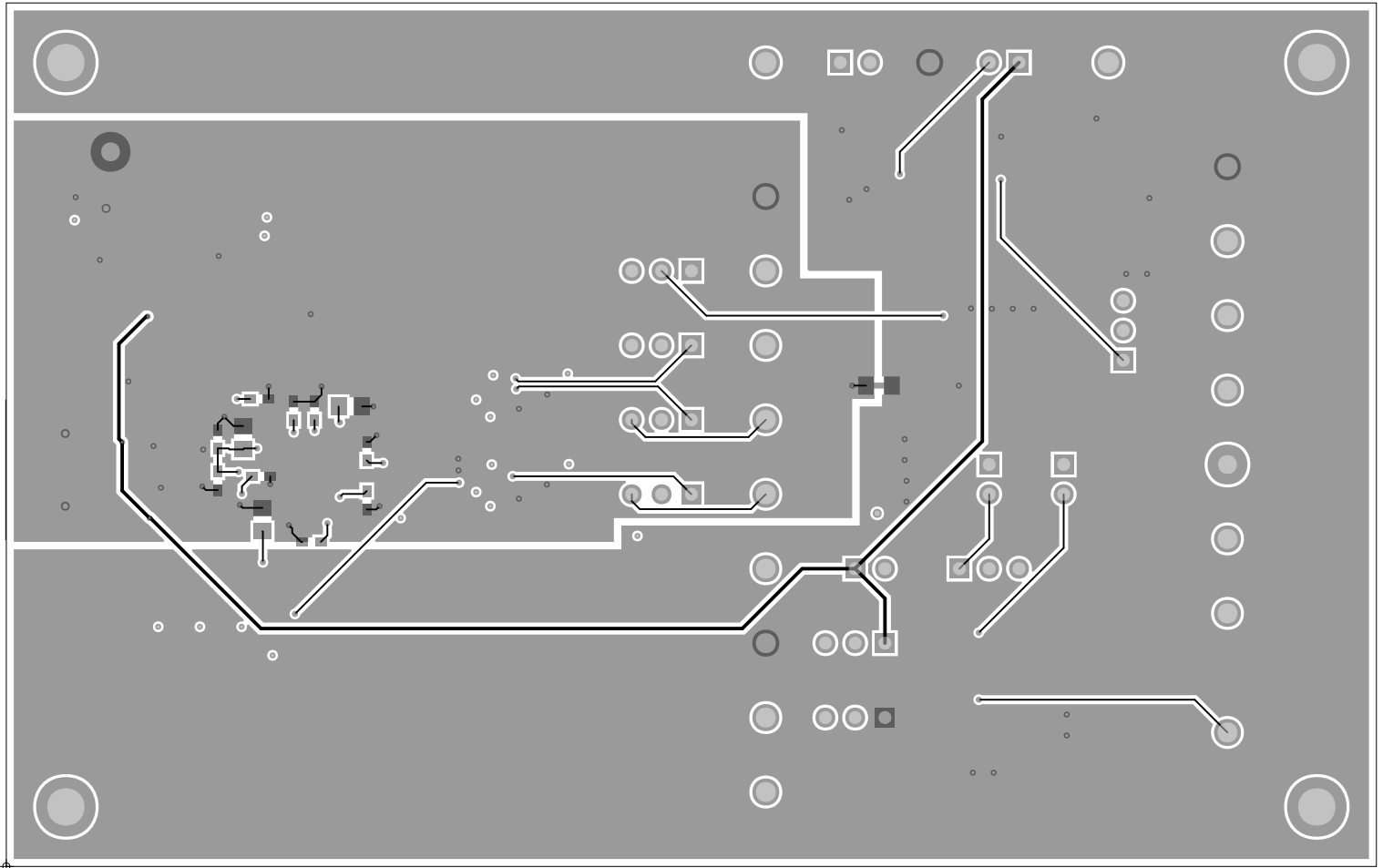
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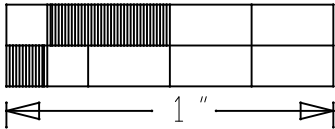
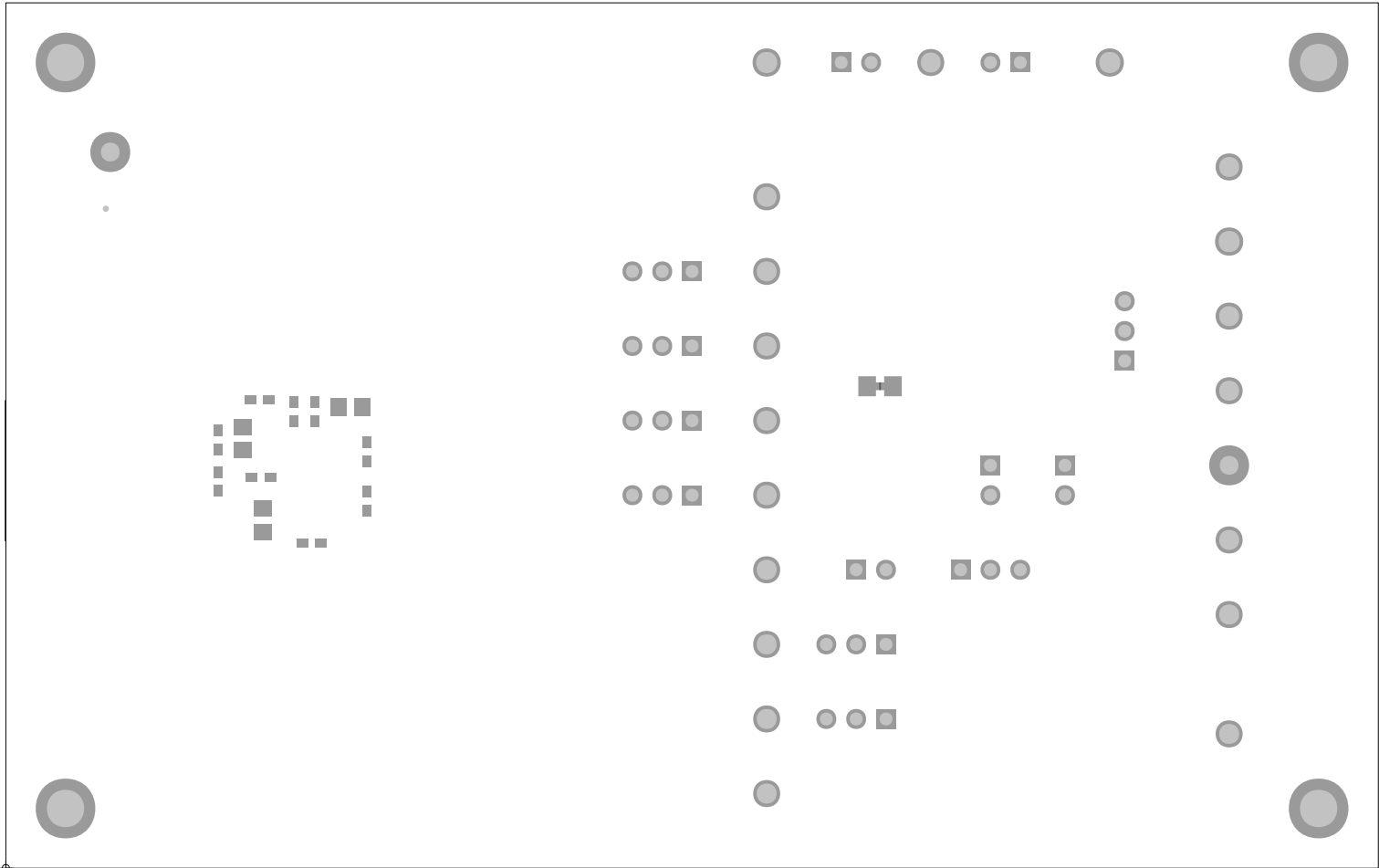
LAYER 2



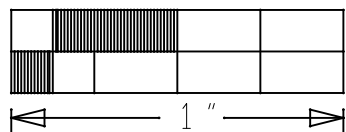
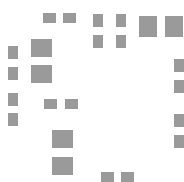
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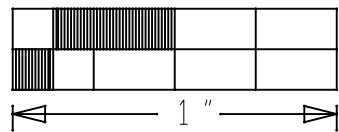
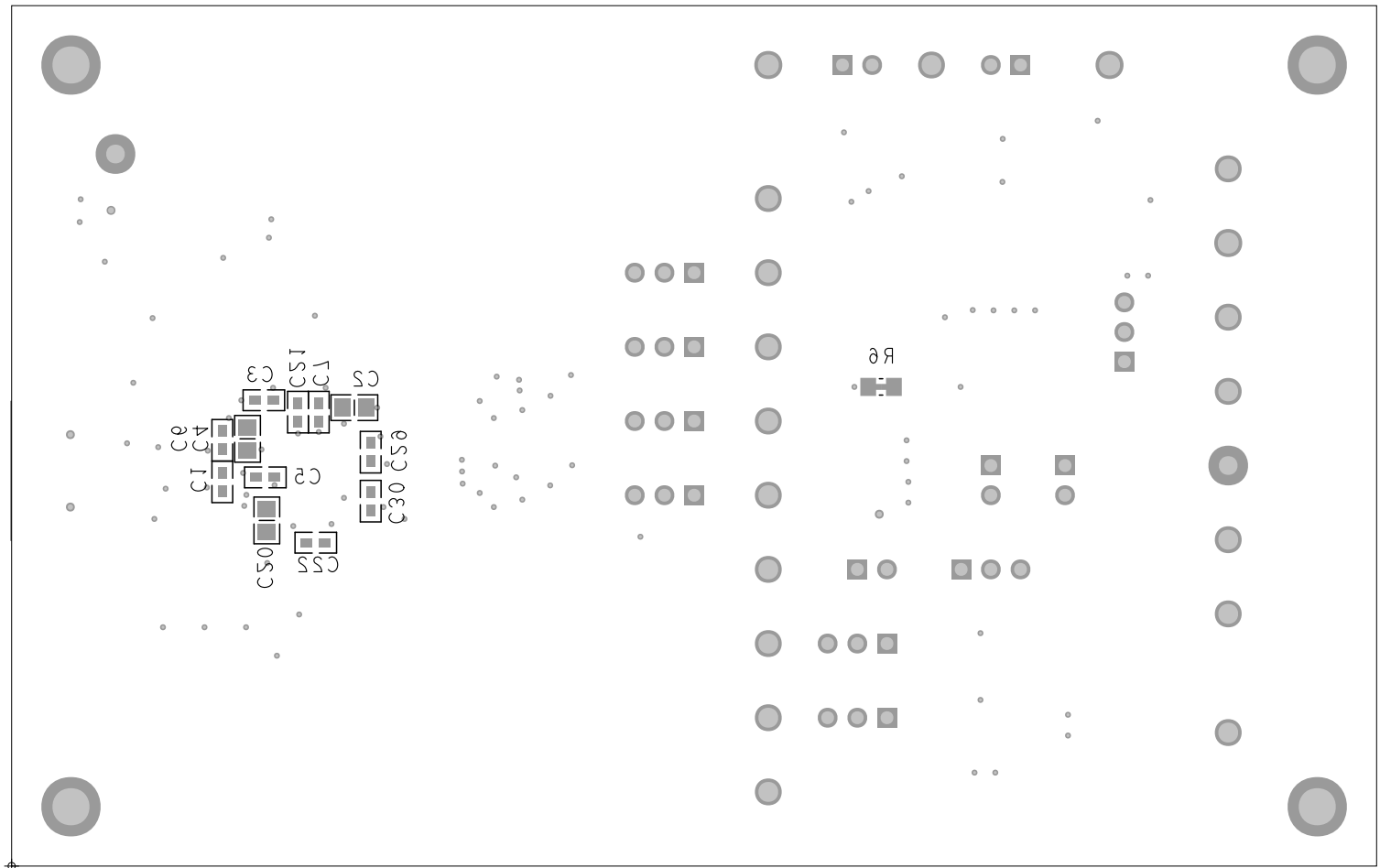
BOTTOM



BOTTOM MASK



BOTTOM PASTE



BOTTOM SILKSCREEN

