



PRODUCT DATA SHEET



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Datasheet

es Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

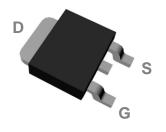
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

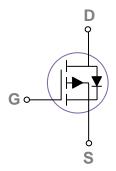
BVDSS	RDSON	ID
-60V	28m Ω	-35A

Features

- -60V, -35A, $RDS(ON) = 28m\Omega@VGS = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

TO252 Pin Configuration





Applications

- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	- 60	V
V _G s	Gate-Source Voltage	±20	V
I_	Drain Current – Continuous (Tc=25°C)	-35	А
D	Drain Current – Continuous (Tc=100°C)	-22.1	А
Ірм	Drain Current – Pulsed ¹	-140	А
EAS	Single Pulse Avalanche Energy ²	105	mJ
AS	Single Pulse Avalanche Current ²	-46	А
3	Power Dissipation (T _C =25°C)	72.6	W
P_{D}	Power Dissipation – Derate above 25°C	0.58	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
ReJC	Thermal Resistance Junction to Case		1.72	°C/W
R ₀ JA	Thermal Resistance Junction to Ambient		62	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D = - 250μA	- 60			V
	Drain Source Leekage Current	V _{DS} = - 60V , V _{GS} =0V , T _J =25°C			-1	μΑ
I _{DSS}	Drain-Source Leakage Current	V _{DS} = - 48V , V _{GS} =0V , T _J =125°C			-10	μΑ
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-8A	22	28	mΩ	
NDS(ON)		V _{GS} = - 4.5V , I _D = - 6A		26	35	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-1.0	- 1.6	- 2.5	V
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-3A		18		S

Dynamic and switching Characteristics

	•				
Q_g	Total Gate Charge ^{3, 4}	V _{DS} =-30V , V _{GS} =-10V , I _D =-5A	 43.8	88	
Qg	Total Gate Charge		 20	40	nC
Q_{gs}	Gate-Source Charge ^{3, 4}	V_{DS} =-30V , V_{GS} =-4.5V , I_{D} =-5A	 4.6	9	iiC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 8.3	17	
T _{d(on)}	Turn-On Delay Time ^{3, 4}		 25	50	
Tr	Rise Time ^{3, 4}	V_{DD} =-30V , V_{GS} =-10V , R_G =6 Ω	 13.8	28	ns
T _{d(off)}	Turn-Off Delay Time ^{3, 4}	I _D = - 1A	 148	290	115
Tf	Fall Time ^{3, 4}		 51	100	
C_{iss}	Input Capacitance		 2595	3900	
Coss	Output Capacitance	V _{DS} = - 25V , V _{GS} =0V , F=1MHz	 162	240	pF
C_{rss}	Reverse Transfer Capacitance		 115	170	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current	V- V- OV Force Current			-35	Α
I _{SM}	Pulsed Source Current	V _G =V _D =0V , Force Current			-70	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =−1A , T _J =25°C			-1	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. V_{DD} =-25V, V_{GS} =-10V,L=0.1mH, I_{AS} =-46A.,Starting T_J=25°C
- 3. The data tested by pulsed , pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 4. Essentially independent of operating temperature.



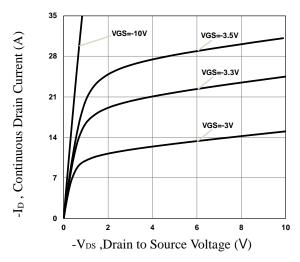


Fig.1 Typical Output Characteristics

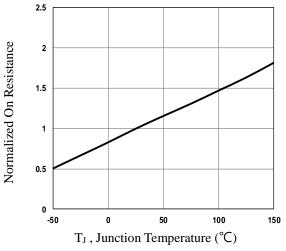


Fig.3 Normalized RDSON vs. T_J

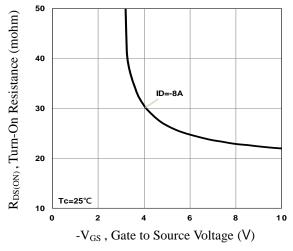
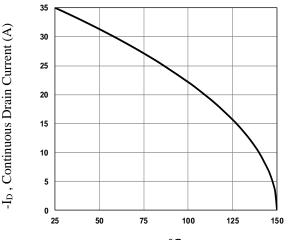


Fig.5 Turn-On Resistance vs. V_{GS}



 T_C , Case Temperature (°C)

Fig.2 Continuous Drain Current vs. Tc

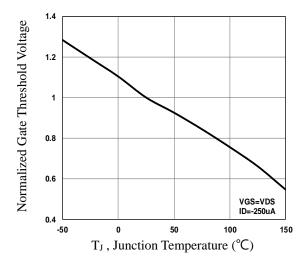


Fig.4 Normalized V_{th} vs. T_J

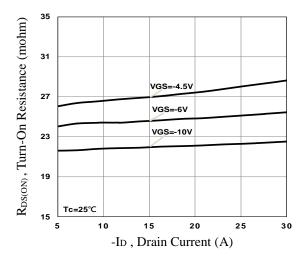


Fig.6 Turn-On Resistance vs. ID



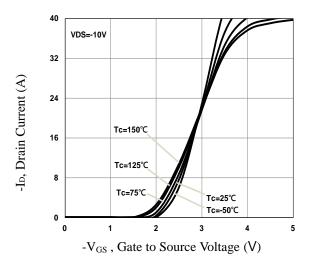


Fig.7 Transfer Characteristics

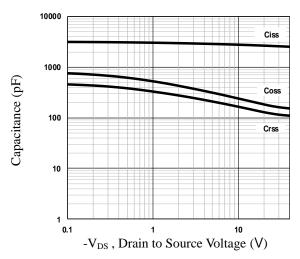


Fig.9 Capacitance Characteristics

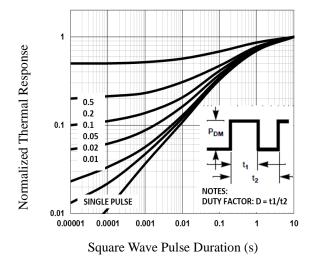


Fig.11 Normalized Transient Impedance

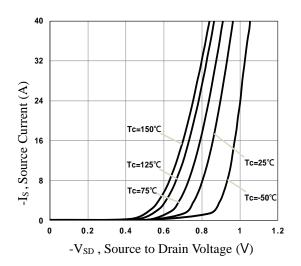


Fig.8 Source Current vs. V_{SD}

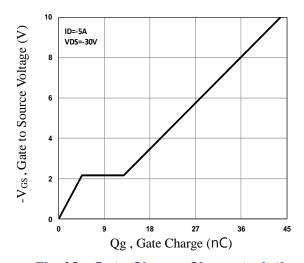


Fig.10 Gate Charge Characteristics

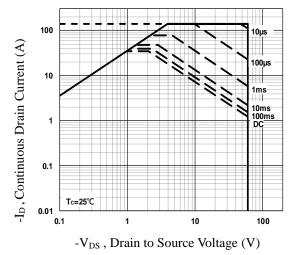
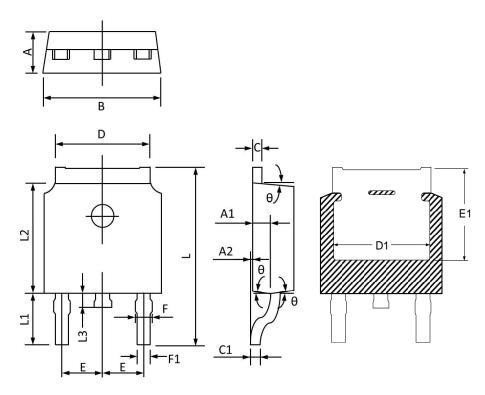


Fig.12 Maximum Safe Operation Area



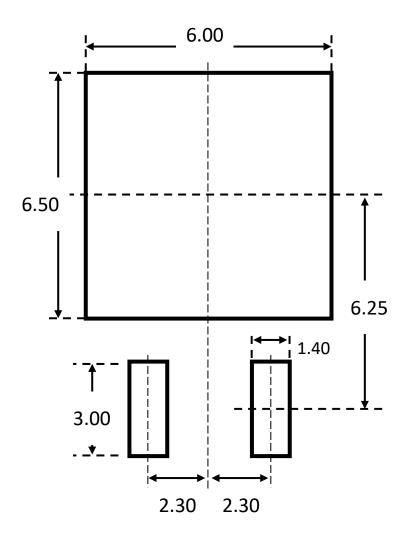
TO252 PACKAGE INFORMATION



Symbol	Dimensions I	n Millimeters	Dimension	s In Inches
Symbol	MAX	MIN	MAX	MIN
Α	2.450	2.150	0.096	0.085
A1	1.200	0.900	0.047	0.035
A2	0.250	0.000	0.010	0.000
В	6.800	6.300	0.268	0.248
С	0.600	0.350	0.024	0.014
C1	0.600	0.380	0.024	0.015
D	5.500	5.100	0.217	0.201
D1	5.400	4.950	0.212	0.195
E	2.400	2.000	0.094	0.079
E1	5.650	4.950	0.222	0.194
F	1.150	0.600	0.045	0.024
F1	0.900	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.100	2.400	0.122	0.094
L2	6.300	5.300	0.248	0.209
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°



TO252 RECOMMENDED LAND PATTERN



unit: mm



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