

Description

The SQ7415CENW-T1_GE3 uses advanced trench technology to provide excellent R DS(ON), low gate charge and operation with gate voltages as Jow as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

 $V_{DS} = -60V I_{D} = -20A$

 $R_{DS(ON)}$ < 65 m Ω @ V_{GS} =4.5V

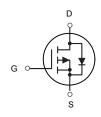
Application

Battery protection

Load switch

Uninterruptible power supply

DFN3X3-8L (PowerPAK1212-8)



P-Channel MOSFET

Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
SQ7415CENW-T1_GE3	DFN3X3-8L (PowerPAK1212-8)	HXY MOSFET	5000

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-60	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	-20	А
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	-12	А
IDM	Pulsed Drain Current ²	-30	А
P _D @T _C =25°C	Total Power Dissipation ⁴	25	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JA	Thermal Resistance Junction-ambient ¹	62	°C/W
R₀JC	Thermal Resistance Junction-Case ¹	5	°C/W



Electrical Characteristics (T_J=25°C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250uA	-60			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =-1mA		-0.023		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V , I _D =-10A		55	65	mΩ
		V _{GS} =-4.5V , I _D =-6A		83	90	
$V_{GS(th)}$	Gate Threshold Voltage	V -V 1 - 250vA	-1.2		-2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=-250uA$		4		mV/°C
1	Di., C	V _{DS} =-24V , V _{GS} =0V , T _J =25°C			-1	uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-24V , V _{GS} =0V , T _J =55°C			-5	
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =-5V , I _D =-15A		12		S
Qg	Total Gate Charge (-4.5V)			6.1		
Q_{gs}	Gate-Source Charge	V _{DS} =-15V , V _{GS} =-4.5V , I _D =-15A		3.1		nC
Q_gd	Gate-Drain Charge			1.8		
T _{d(on)}	Turn-On Delay Time	V_{DD} =-15V , V_{GS} =-10V , R_{G} =3.3 Ω , I_{D} =-15A		2.6		
T _r	Rise Time			8.6		
$T_{d(off)}$	Turn-Off Delay Time			33.6		ns
T _f	Fall Time			6		
C _{iss}	Input Capacitance	V _{DS} =-15V , V _{GS} =0V , f=1MHz		585		
Coss	Output Capacitance			100		pF
C _{rss}	Reverse Transfer Capacitance			85		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,5}	\\ -\\ -0\\			-20	Α
I _{SM}	Pulsed Source Current ^{2,5}	V _G =V _D =0V , Force Current			-30	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V
t _{rr}	Reverse Recovery Time	IF=-15A , dI/dt=100A/µs ,		6.1		nS
Qrr	Reverse Recovery Charge	T _J =25°C		1.4		nC

Note

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\,\leq\,300\text{us}$, duty cycle $\,\leq\,2\%$
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =-25V, V_{GS} =-10V, L=0.1mH, I_{AS} =-19A
- 4.The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

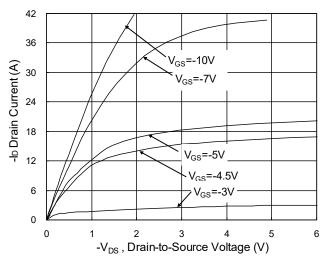


Fig.1 Typical Output Characteristics

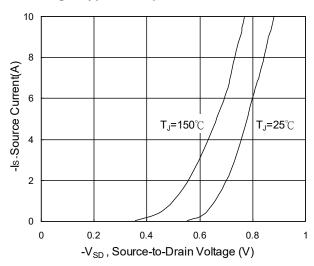


Fig.3 Forward Characteristics Of Reverse

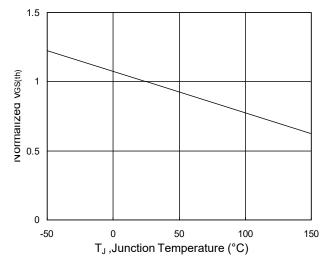


Fig.5 Normalized V_{GS(th)} vs. T_J

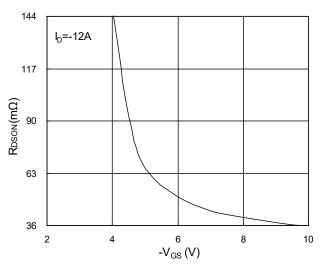


Fig.2 On-Resistance v.s Gate-Source

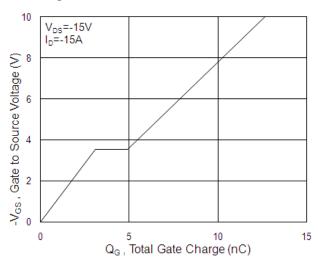


Fig.4 Gate Charge Characteristics

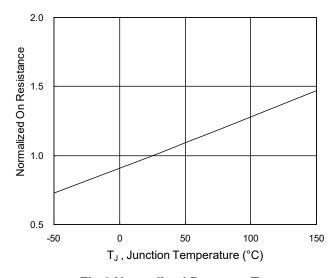
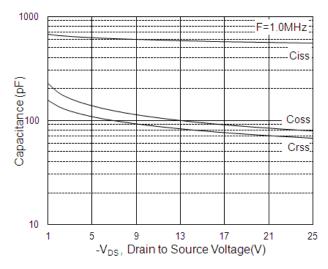


Fig.6 Normalized R_{DSON} vs. T_J



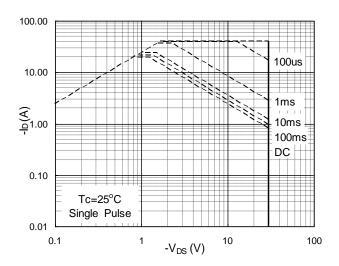


Fig.7 Capacitance

Fig.8 Safe Operating Area

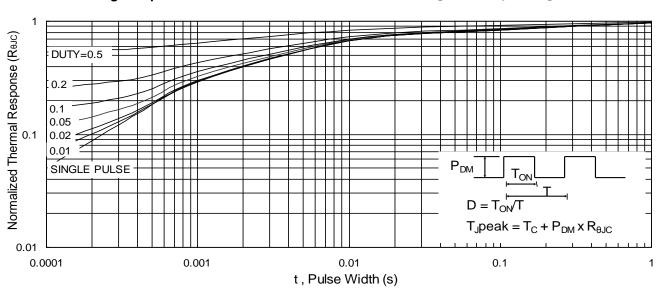


Fig.9 Normalized Maximum Transient Thermal Impedance

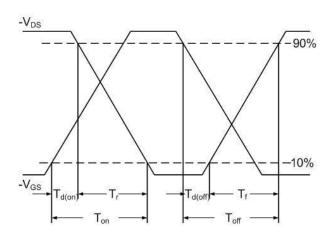


Fig.10 Switching Time Waveform

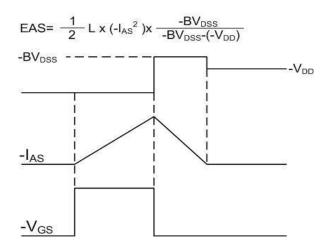
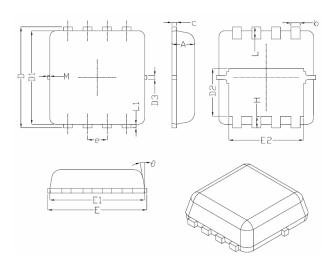


Fig.11 Unclamped Inductive Switching Waveform

DFN3X3-8L(PowerPAK1212-8) Package Information



Complete I	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
M	*	*	0.15	
θ		10 [°]	12 [°]	



Attention

- Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.
- HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.
- Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all HUA XUAN YANG ELECTRONICS products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production.

 HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.