

Features

- For surface mounted applications
- · Low profile package
- Glass Passivated Chip Junction
- Easy to pick and place

INI

SMC (DO-214AB)



Mechanical Data

• Case: SMC

• Terminals: Solderable per MIL-STD-750, Method 2026

Package Marking and Ordering Information

Product ID	Pack	Marking	Qty(PCS)		
S10AC-S10MC	SMC(DO-214AB)	S10XX	3000		

Absolute Maximum Ratings and Characteristics

Ratings at 25 °C ambient temperature unless otherwise specified.

Single phase half-wave 60 Hz, resistive or inductive load, for capacitive load current derate by 20 %.

Parameter	Symbols	S10AC	S10BC	S10DC	S10GC	S10JC	S10KC	S10MC	Units
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V _{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V _{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current	I _{F(AV)}	10							А
Peak Forward Surge Current 8.3 ms Single Half Sine Wave Superimposed on Rated Load	I _{FSM}	200						А	
Maximum Instantaneous Forward Voltage at 10A	V _F	1.0							V
	I _R	5 100							μA
Typical Junction Capacitance (1)	C _j	100							pF
Typical Thermal Resistance (2)	R _{θJA}	10						°C/W	
Operating and Storage Temperature Range	T_{j},T_{stg}	-55 ~ + 150						°C	

⁽¹⁾ Measured at 1 MHz and applied reverse voltage of 4 V D.C

^(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Typical Characteristics

Fig.1 Forward Current Derating Curve

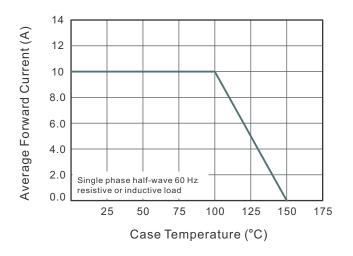


Fig.3 Typical Forward Characteristic

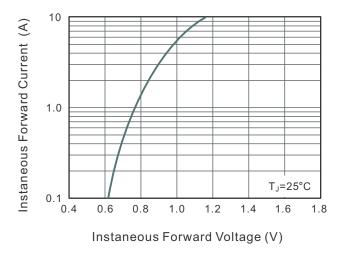


Fig.6 Maximum Non-Repetitive Peak Forward Surage Current

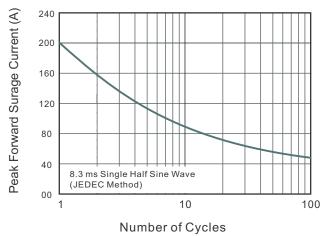


Fig.2 Typical Reverse Characteristics

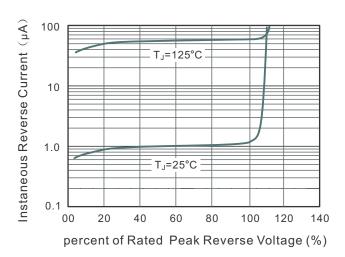
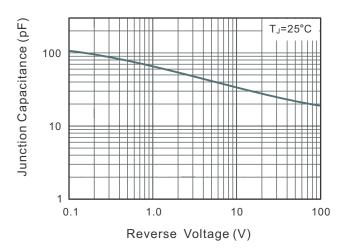
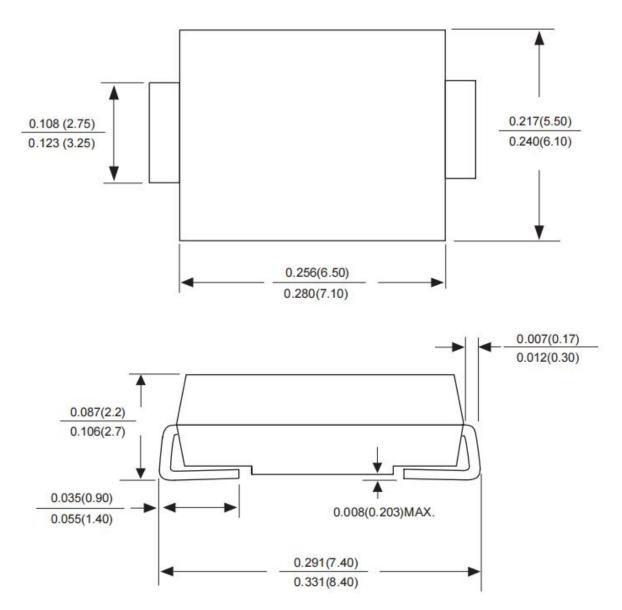


Fig.4 Typical Junction Capacitance



Package Outline Dimensions SMC(DO-214AB)



Dimensions in inches and (millimeters)

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