

N-channel Enhancement Mode Power MOSFET

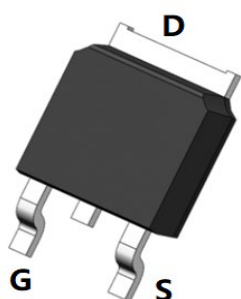
Features

- $V_{DS} = 80\text{ V}$, $I_D = 95\text{ A}$
 $R_{DS(ON)} < 6\text{ m}\Omega$ @ $V_{GS} = 10\text{ V}$
 $R_{DS(ON)} < 7\text{ m}\Omega$ @ $V_{GS} = 4.5\text{ V}$

General Features

- Advanced Trench Technology
- Provide Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead Free and Green Available

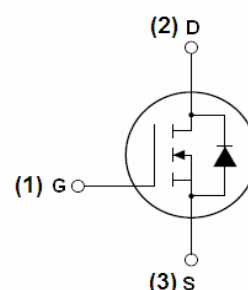
100% UIS TESTED!
100% ΔV_{ds} TESTED!



TO-252-2L Top View



Pin Assignment



Schematic Diagram

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	95	A
Drain Current-Continuous($T_c = 100^\circ\text{C}$)	$I_D(100^\circ\text{C})$	67	A
Pulsed Drain Current	I_{DM}	320	A
Maximum Power Dissipation	P_D	170	W
Derating factor		1.13	W/ $^\circ\text{C}$
Single pulse avalanche energy ^(Note 5)	E_{AS}	529	mJ
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ\text{C}$

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{\theta JC}$	0.88	$^\circ\text{C/W}$
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter		Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V I _D =250μA	80	-	-	V
Zero Gate Voltage Drain Current		I _{DSS}	V _{DS} =82V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current		I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)							
Gate Threshold Voltage		V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2		4	V
Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	6		mΩ
Forward Transconductance		g _{FS}	V _{DS} =5V, I _D =20A	-	50	-	S
Dynamic Characteristics (Note4)							
Input Capacitance		C _{iss}	V _{DS} =25V, V _{GS} =0V, F=1.0MHz	-	5633	-	PF
Output Capacitance		C _{oss}		-	268	-	PF
Reverse Transfer Capacitance		C _{rss}		-	226	-	PF
Switching Charact							
Turn-on Delay Time		t _{d(on)}	VDD=40V, RL=15Ω RG=2.5Ω, VGS=10V	-	18	-	nS
Turn-on Rise Time		t _r		-	12	-	nS
Turn-Off Delay Time		t _{d(off)}		-	56	-	nS
Turn-Off Fall Time		t _f		-	15	-	nS
Total Gate Charge		Q _g	V _{DS} =40V, I _D =50A, V _{GS} =10V	-	109.3	-	nC
Gate-Source Charge		Q _{gs}		-	35.1	-	nC
Gate-Drain Charge		Q _{gd}		-	25.8	-	nC
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)		V _{SD}	V _{GS} =0V, I _S =95A	-	-	1.2	V
Diode Forward Current (Note 2)		I _S		-	-	95	A
Reverse Recovery Time		t _{rr}	Tj=25℃, I _F =100A di/dt=100A/μs (Note3)	-		37	nS
Reverse Recovery Charge		Q _{rr}		-		58	nC

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition: Tj=25°C, V_{DD}=40V, V_{GS}=10V, L=0.5mH, Rg=25Ω

Typical Electrical and Thermal Characteristics (Curves

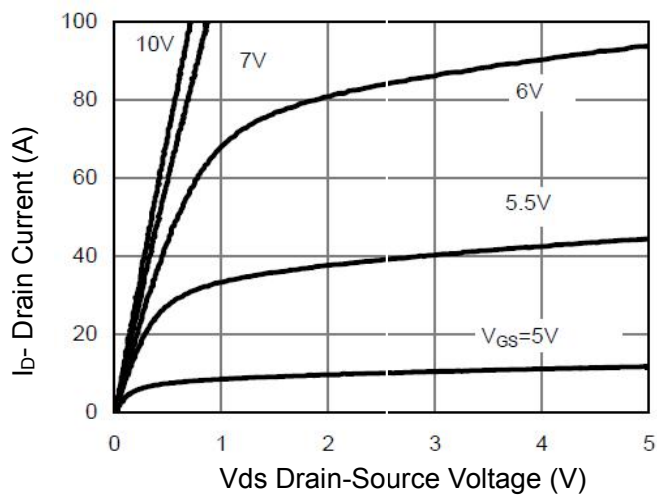


Figure 1 Output Characteristics

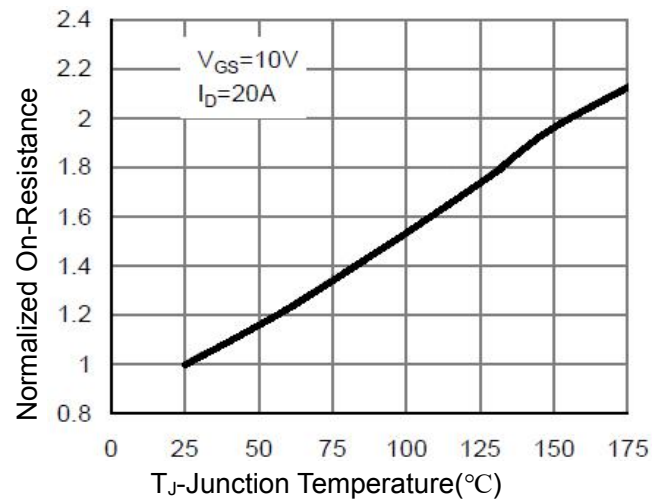


Figure 4 $R_{DS(on)}$ -Junction Temperature

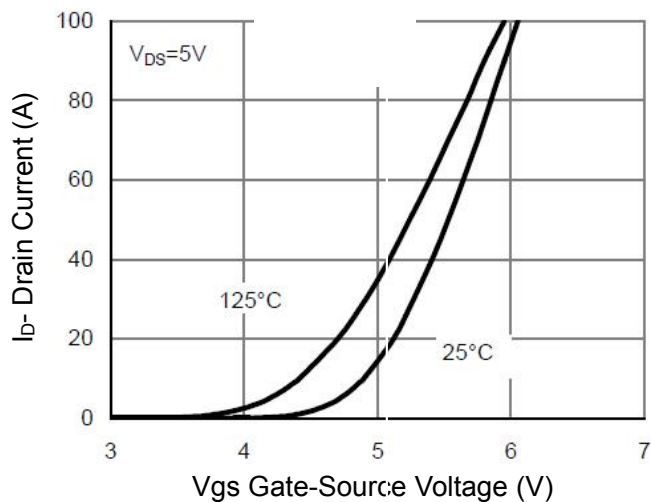


Figure 2 Transfer Characteristics

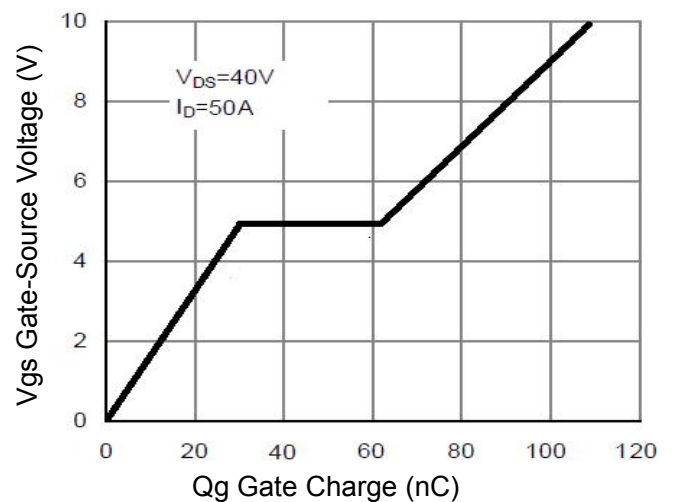


Figure 5 Gate Charge

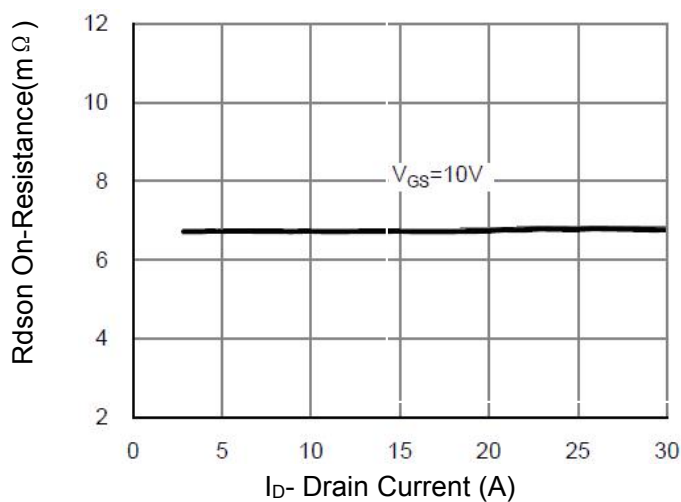


Figure 3 $R_{DS(on)}$ - Drain Current

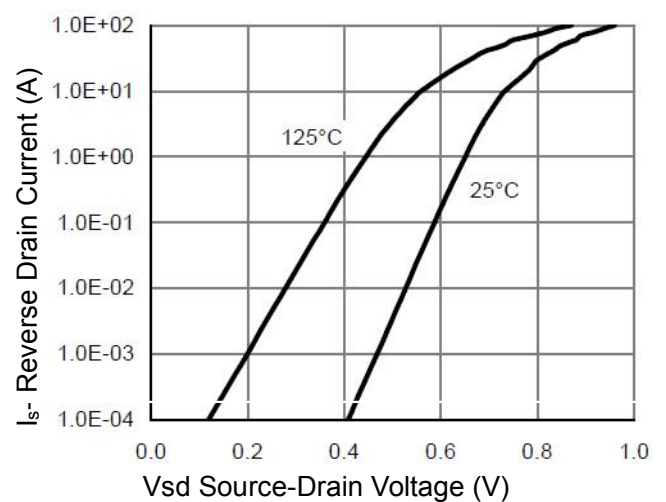


Figure 6 Source- Drain Diode Forward

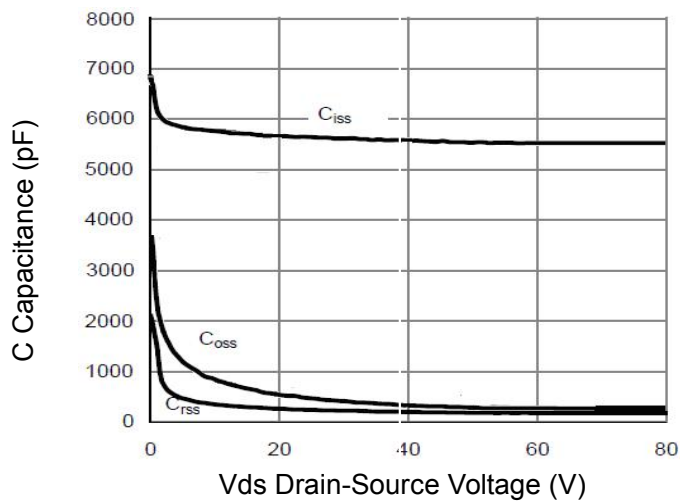


Figure 7 Capacitance vs Vds

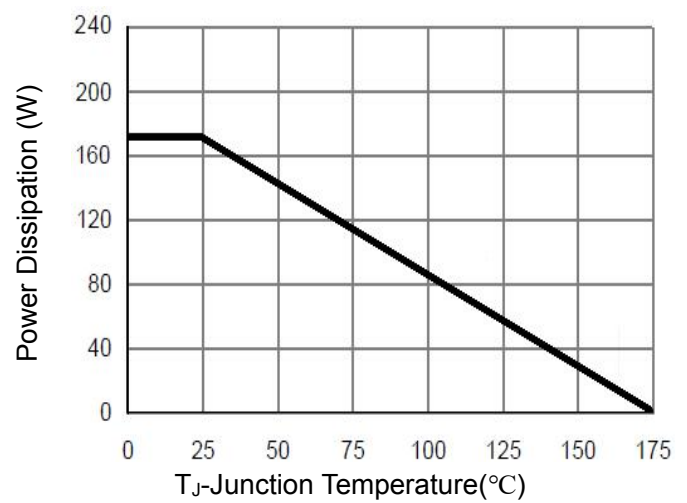


Figure 9 Power De-rating

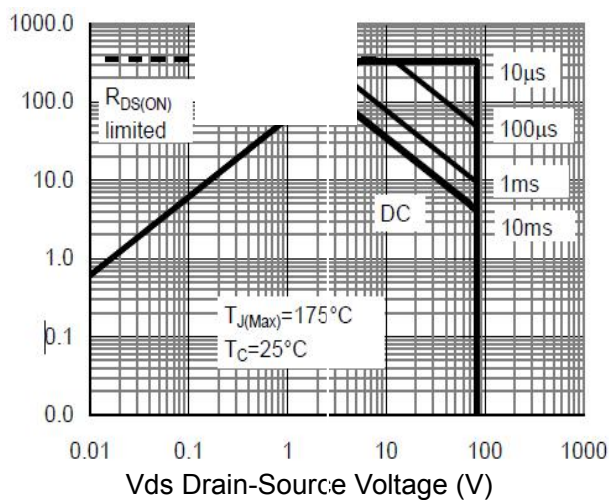


Figure 8 Safe Operation Area

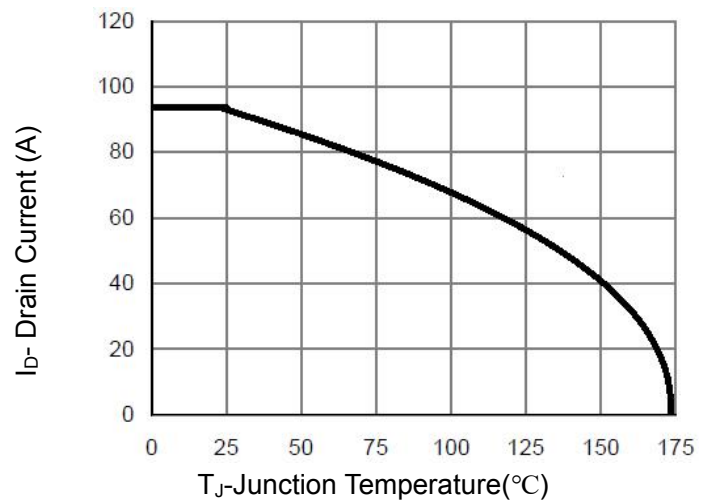


Figure 10 Id Current De-rating

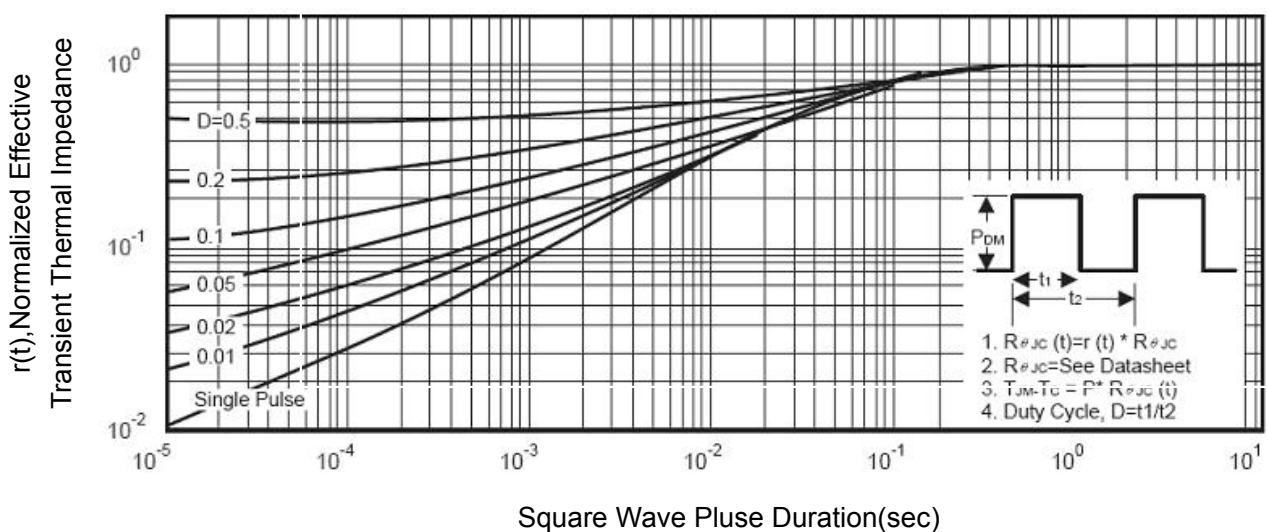


Figure 11 Normalized Maximum Transient Thermal Impedance