LM3490

LM3490 100 mA, SOT-23, Quasi Low-Dropout Linear Voltage Regulator with Logic-Controlled ON/ OFF



Literature Number: SNVS012D



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General Description

The LM3490 is an integrated linear voltage regulator. It features operation from an input as high as 30V and a guaranteed maximum dropout of 1.2V at the full 100 mA load. Standard packaging for the LM3490 is the 5-lead SOT-23 package. A logic-controlled ON/OFF feature makes the LM3490 ideal for powering subsystems ON and OFF as needed.

The 5, 12, and 15V members of the LM3490 series are intended as tiny alternatives to industry standard LM78LXX series and similar devices. The 1.2V quasi low dropout of LM3490 series devices makes them a nice fit in many applications where the 2 to 2.5V dropout of LM78LXX series devices precludes their (LM78LXX series devices) use.

The LM3490 series features a 3.3V member. The SOT packaging and quasi low dropout features of the LM3490 series converge in this device to provide a very nice, very tiny 3.3V, 100 mA bias supply that regulates directly off the system 5V ±5% power supply.

Key Specifications

- 30V maximum input for operation
- 1.2V guaranteed maximum dropout over full load and temperature ranges
- 100 mA guaranteed load current
- ±5% guaranteed output voltage tolerance over full load and temperature ranges
- -40 to +125°C junction temperature range for operation

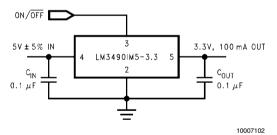
Features

- 3.3, 5, 12, and 15V versions available
- Logic-controlled ON/OFF
- Packaged in the tiny 5-lead SOT-23 package

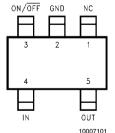
Applications

- Tiny alternative to LM78LXX series and similar devices
- Tiny 5V±5% to 3.3V, 100 mA converter
- Post regulator for switching DC/DC converter
- Bias supply for analog circuits

Typical Application Circuit



Connection Diagram



Top View
SOT-23 Package
5-Lead, Molded-Plastic Small-Outline Transistor (SOT) Package
Package Code MF05A(Note 1)

Ordering Information

Output Voltage (V)	Order Number (Note 2)	Package Marking (Note 3)	Comments
3.3	LM3490IM5-3.3	L78B	1000 Units on Tape and Reel
3.3	LM3490IM5X-3.3	L78B	3000 Units on Tape and Reel
5.0	LM3490IM5-5.0	L79B	1000 Units on Tape and Reel
5.0	LM3490IM5X-5.0	L79B	3000 Units on Tape and Reel
12	LM3490IM5-12	L80B	1000 Units on Tape and Reel
12	LM3490IM5X-12	L80B	3000 Units on Tape and Reel
15	LM3490IM5-15	L81B	1000 Units on Tape and Reel
	LM3490IM5X-15	L81B	3000 Units on Tape and Reel

Absolute Maximum Ratings (Note 4)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Soldering Time, Temp. (Note 6)

 Wave
 4sec., 260°C

 Infrared
 10sec., 240°C

 Vapor Phase
 75sec., 219°C

ESD (Note 7)

ON/OFF 1.0kV

All Other Pins 2.0kV

Operating Ratings (Note 4)

Electrical Characteristics

LM3490-3.3, LM3490-5.0

 $V_{IN} = V_{NOM} + 1.5V$ unless otherwise noted. Typicals and limits appearing in normal type apply for $T_A = T_J = 25^{\circ}$ C. Limits appearing in boldface type apply over the entire junction temperature range for operation, -40 to +125°C. (Notes 9, 10, 11)

Nominal Output Voltage (V _{NOM})		3.3V		5.0V		Units	
Symbol	Parameter	Conditions	Typical	Limit	Typical	Limit	Units
V _{OUT}	Output Voltage	1 mA ≤ I _{OUT} ≤ 100 mA	3.30		5.00		V
001		001		3.17		4.80	V(min)
				3.14		4.75	V(min)
				3.43		5.20	V(max)
				3.46		5.25	V(max)
ΔV_{OUT}	Line Regulation	$V_{NOM} + 1.5V \le V_{IN} \le 30V$	7		9		mV
00.		I _{OUT} = 1 mA		25		25	mV(max)
ΔV _{OUT}	Load Regulation	10 mA ≤ I _{OUT} ≤ 100 mA	15		15		mV
		001		40		40	mV(max)
I _{GND}	Ground Pin Current	$V_{NOM} + 1.5V \le V_{IN} \le 30V$, No Load					
		V _{ON/OFF} = 5V	2		2		mA
		VON/OFF - SV	_	4	_	4	mA(max)
		$V_{ON/\overline{OFF}} = 0V$	0.1	-	0.1	-	μΑ
		- ON/OFF		5		5	μA(max)
V _{IN} -	Dropout Voltage	I _{OUT} = 10 mA	0.7		0.7		V
V _{OUT}		001		0.9		0.9	V(max)
				1.0		1.0	V(max)
		I _{OUT} = 100 mA	0.9		0.9		l v
				1.1		1.1	V(max)
				1.2		1.2	V(max)
e _n	Output Noise Voltage	V _{IN} = 10V, Bandwidth: 10 Hz to 100 kHz	100		150		μV _{rms}
V _{IL}	Maximum Low Level Input Voltage at ON/ OFF			0.2		0.2	V(max)
V _{IH}	Minimum High Level Input Voltage at ON/ OFF			2.0		2.0	V(min)
I _{IL}		$V_{ON/\overline{OFF}} = 0V$		-1		-1	μA(max)
I _{IH}		$V_{ON/\overline{OFF}} = 5V$	1		1		μA
				20		20	μA(max)

3

LM3490-12, LM3490-15

 $V_{IN} = V_{NOM} + 1.5V$ unless otherwise noted. Typicals and limits appearing in normal type apply for $T_A = T_J = 25$ °C. Limits appearing in boldface type apply over the entire junction temperature range for operation, -40 to +125°C. (Notes 9, 10, 11)

Nominal Output Voltage (V _{NOM})			12V		15V		Units
Symbol	Parameter	Conditions	Typical	Limit	Typical	Limit	Units
V _{OUT}	Output Voltage	1 mA ≤ I _{OUT} ≤ 100 mA	12.00		15.00		V
001		001		11.52		14.40	V(min)
				11.40		14.25	V(min)
				12.48		15.60	V(max)
				12.60		15.75	V(max)
ΔV_{OUT}	Line Regulation	$V_{NOM} + 1.5V \le V_{IN} \le 30V$	14		16		mV
00.		I _{OUT} = 1 mA		40		40	mV(max)
ΔV_{OUT}	Load Regulation	10 mA ≤ I _{OUT} ≤ 100 mA	36		45		mV
001				60		75	mV(max)
I _{GND}	Ground Pin Current	$V_{NOM} + 1.5V \le V_{IN} \le 30V$					
GIND		No Load					
		$V_{ON/\overline{OFF}} = 5V$	2		2		mA
		ON/OTT		4		4	mA(max)
		$V_{ON/\overline{OFF}} = 0V$	0.1		0.1		μA
		ON OTT		5		5	μA(max)
V _{IN} -	Dropout Voltage	I _{OUT} = 10 mA	0.7		0.7		V
V_{OUT}				0.9		0.9	V(max)
				1.0		1.0	V(max)
		I _{OUT} = 100 mA	0.9		0.9		V
				1.1		1.1	V(max)
				1.2		1.2	V(max)
e _n	Output Noise	$V_{IN} = 10V,$	360		450		μV_{rms}
	Voltage	Bandwidth: 10 Hz to 100 kHz					
V _{IL}	Maximum Low			0.2		0.2	V(max)
	Level Input Voltage						
	at ON/OFF						
V _{IH}	Minimum High			2.0		2.0	V(min)
	Level Input Voltage						
	at ON/OFF						
I _{IL}		$V_{ON/\overline{OFF}} = 0V$		-1		-1	μA(max)
I _{IH}		$V_{ON/\overline{OFF}} = 5V$	1		1		μΑ
				20		20	μA(max)

Note 1: The package code MA05B is internal to National Semiconductor Corporation and indicates a specific version of the SOT-23 package and associated mechanical drawings.

Note 2: The suffix "I" indicates the junction temperature range for operation is the industrial temperature range, -40 to +125°C. The suffix "M5" indicates the die is packaged in the 5-lead SOT-23 package. The suffix "X" indicates the devices will be supplied in blocks of 3k units as opposed to blocks of 250 units.

Note 3: Because the entire part number does not fit on the SOT-23 package, the SOT-23 package is marked with this code instead of the part number.

Note 4: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Ratings are conditions under which operation of the device is guaranteed. Operating Ratings do not imply guaranteed performance limits. For guaranteed performance limits and associated test conditions, see the Electrical Characteristics tables.

Note 5: The Absolute Maximum power dissipation depends on the ambient temperature and can be calculated using $P = (T_J - T_A)/\theta_{JA}$ where T_J is the junction temperature, T_A is the ambient temperature, and T_A is the junction-to-ambient thermal resistance. The 400 mW rating results from substituting the Absolute Maximum junction temperature, 150°C, for T_J , 50°C for T_A , and 250°C/W for θ_{JA} . More power can be safely dissipated at lower ambient temperatures. Less power can be safely dissipated at higher ambient temperatures. The Absolute Maximum power dissipation can be increased by 4 mW for each °C below 50°C ambient. It must be derated by 4 mW for each °C above 50°C ambient. A θ_{JA} of 250°C/W represents the worst-case condition of no heat sinking of the 5-lead plastic SOT-23 package. Heat sinking enables the safe dissipation of more power. The LM3490 actively limits its junction temperature to about 150°C.

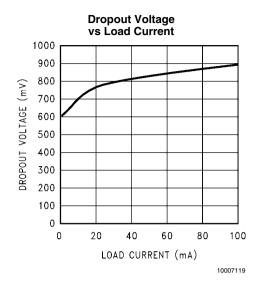
Note 6: Times shown are dwell times. Temperatures shown are dwell temperatures. For detailed information on soldering plastic small-outline packages, refer to the *Packaging Databook* available from National Semiconductor Corporation.

Note 7: For testing purposes, ESD was applied using the human-body model, a 100 pF capacitor discharged through a 1.5 kΩ resistor.

Note 8: As with the Absolute Maximum power dissipation, the maximum power dissipation for operation depends on the ambient temperature. The 300 mW rating appearing under Operating Ratings results from substituting the maximum junction temperature for operation, 125° C, for T_J , 50° C for T_A , and 250° C/W for θ_{JA} in $P = (T_J - T_A)/\theta_{JA}$. More power can be dissipated at lower ambient temperatures. Less power can be dissipated at higher ambient temperatures. The maximum power dissipation for operation appearing under Operating Ratings can be increased by 4 mW for each °C below 50° C ambient. It must be derated by 4 mW for

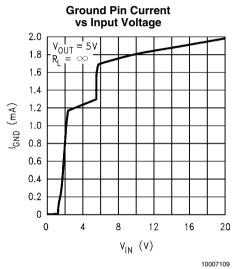
each °C above 50°C ar the dissipation of more	mbient. A θ _{JA} of 250°C/W represents the worst-case condition of no heat sinking of the 5-lead plastic SOT-23 package. Heat sinking enables power during operation.
Note 9: A typical is the Note 10: All limits are	e center of characterization data taken with $T_A = T_J = 25$ °C. Typicals are not guaranteed. guaranteed. All electrical characteristics having room-temperature limits are tested during production with $T_A = T_J = 25$ °C. All hot and cold
	by correlating the electrical characteristics to process and temperature variations and applying statistical process control. Except dropout are with respect to the voltage at the GND pin.

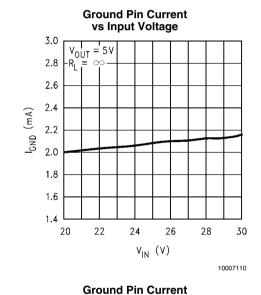
Typical Performance Characteristics Unless indicated otherwise, $V_{IN} = V_{NOM} + 1.5V$, $C_{IN} = 0.1~\mu F$, $C_{OUT} = 0.1~\mu F$, and $T_{A} = 25^{\circ}C$.

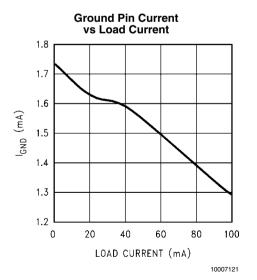


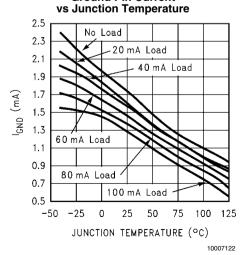
vs Junction Temperature 1100 100 mA Load 1000 80 mA Load 900 DROPOUT VOLTAGE (mV) 800 700 600 No Load 500 400 20 mA Load 300 40 mA Load 200 60 mA Load 100 0 -50 -25 50 75 100 125 150 JUNCTION TEMPERATURE (°C) 10007120

Dropout Voltage

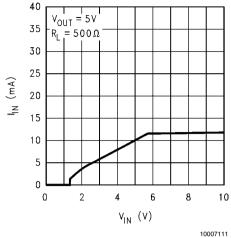




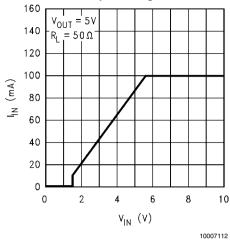




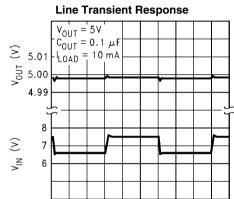
Input Current vs Input Voltage 40



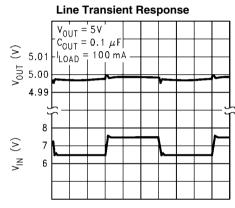
Input Current vs Input Voltage



Line Transient Response



Line Transient Response



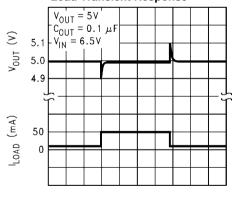
 $200 \mu s/Div$

10007103

 $200 \, \mu s/\text{Div}$

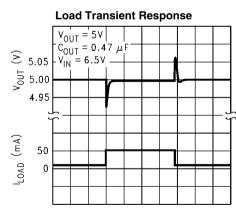
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Load Transient Response



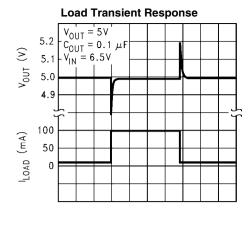
 $50 \mu s/Div$

Load Transient Response

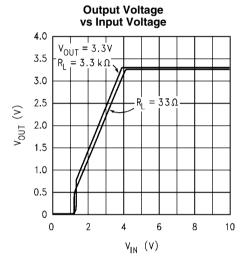


 $50~\mu \mathrm{s/Div}$

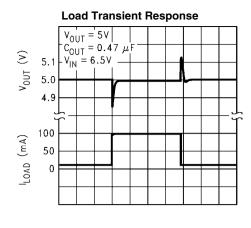
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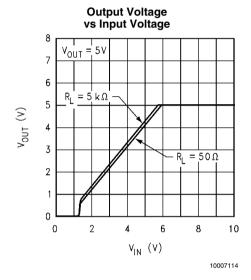
50 μs/Div



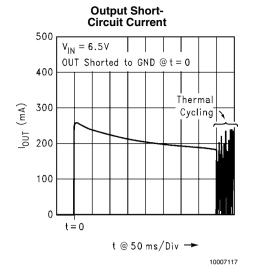
Output Voltage vs Input Voltage V_{OUT} = 12½ = 12 kΩ ·R_I Ι 120Ω V_{IN} (V)

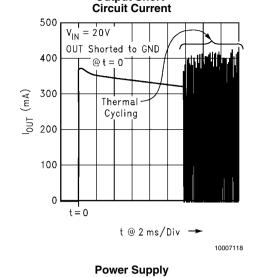


50 μs/Div

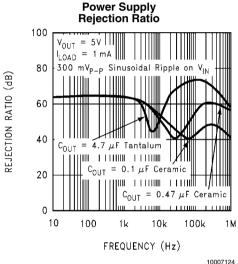


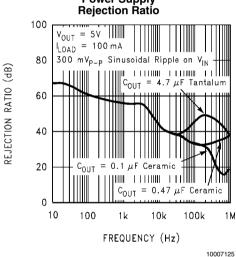
Output Voltage vs Input Voltage V_{OUT} = 15 V_{OUT} (V) = 150Ω $V_{\mathsf{IN}}^{}$ (V)

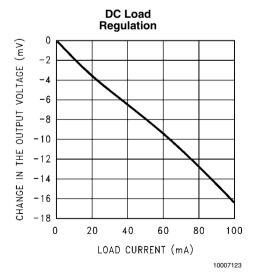




Output Short-







Applications Information

ON/OFF Pin

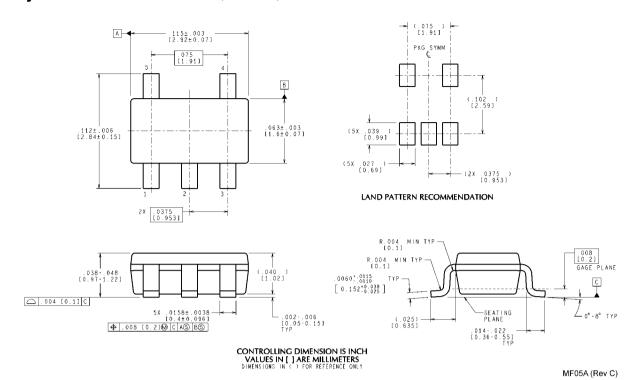
The LM3490 features a logic controlled ON/OFF pin that will allow the output voltage to be disabled, or enabled, as needed. The defined operating voltage range for this pin is 0.0V to 5.0V. The ON/OFF pin can not be left floating, as the output status cannot be guaranteed. Additionally, the ON/OFF pin should not be biased below ground potential as unpredictable device behavior may occur.

Pulling the ON/ $\overline{\text{OFF}}$ pin voltage to a value between the V $_{\text{IH}}$ threshold and 5.0V will enable the output voltage. Pulling the

ON/OFF pin voltage to a value between the V_{IL} threshold and Ground potential will disable the output voltage. Although the ON/OFF threshold is typically 725mV, and has no hysteresis, the ON/OFF signal must rise and fall, cleanly and promptly, from voltage levels that are below the V_{IL} threshold and above the V_{IH} threshold.

The ON/OFF pin has no internal pull-up or pull-down to establish a default condition and, as a result, this pin must be terminated, either actively or passively, to an appropriate voltage level.

Physical Dimensions inches (millimeters) unless otherwise noted



SOT-23 Package 5-Lead Small-Outline Package (M5) For Ordering, Refer to Ordering Information Table NS Package Number MF05A

Notes

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