

WP7104ALUP/2YD-0L

T-1 (3mm) LED Lamp with Housing

DESCRIPTION

• The Yellow source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Yellow Light Emitting Diode

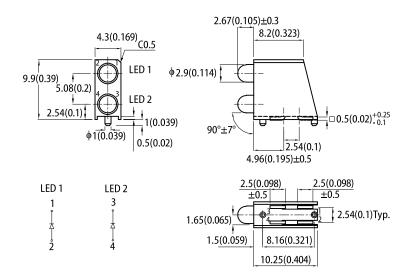
FEATURES

- Black case enhances contrast ratio
- · High reliability life measured in years
- Moisture sensitivity level: 3
- Housing material: PPA
- . Housing UL rating: 94V-0
- · High temperature resistant housing
- · High glass transition temperature epoxy
- Halogen-free
- · RoHS compliant

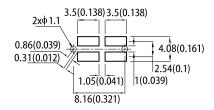
APPLICATIONS

- · Status indicator
- Illuminator
- Signage applications
- · Decorative and entertainment lighting
- Commercial and residential architectural lighting

PACKAGE DIMENSIONS



RECOMMENDED SOLDERING PATTERN



- 1. All dimensions are in millimeters (inches).
 2. Tolerance is ±0.25(0.01") unless otherwise noted.
- The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SELECTION GUIDE

Part Number	Emitting Color (Material)	Lens Type	Iv (mcd) @ 10mA [2]		Viewing Angle [1]
			Min.	Тур.	201/2
WP7104ALUP/2YD-0L	Yellow (GaAsP/GaP)	Yellow Diffused	8	15	50°

Notes.

1. 01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.

2. Luminous intensity / luminous flux: +/-15%.

3. Luminous intensity value is traceable to CIE127-2007 standards.



ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Emitting Color	Value		11:-:4
Parameter		Emitting Color	Тур.	Max.	Unit
Wavelength at Peak Emission I _F = 10mA	λ_{peak}	Yellow	590	-	nm
Dominant Wavelength I _F = 10mA	λ _{dom} ^[1]	Yellow	588	-	nm
Spectral Bandwidth at 50% Φ REL MAX I _F = 10mA	Δλ	Yellow	35	-	nm
Capacitance	С	Yellow	20	-	pF
Forward Voltage I _F = 10mA	V _F ^[2]	Yellow	1.95	2.4	V
Reverse Current (V _R = 5V)	I _R	Yellow	-	10	μΑ
Temperature Coefficient of λ_{peak} I _F = 10mA, -10°C \leq T \leq 85°C	$TC_{\lambda peak}$	Yellow	0.12	-	nm/°C
Temperature Coefficient of λ_{dom} I _F = 10mA, -10°C \leq T \leq 85°C	TC_{\lambdadom}	Yellow	0.07	-	nm/°C
Temperature Coefficient of V_F I _F = 10mA, -10°C \leq T \leq 85°C	TC _V	Yellow	-2.0	-	mV/°C

ABSOLUTE MAXIMUM RATINGS at $T_A=25$ °C

Parameter	Symbol	Value	Unit
Power Dissipation	P _D	75	mW
Reverse Voltage	V _R	5	V
Junction Temperature	T _j	110	°C
Operating Temperature	T _{op}	-40 to +85	°C
Storage Temperature	T _{stg}	-40 to +85	°C
DC Forward Current	I _F	30	mA
Peak Forward Current	I _{FM} ^[1]	140	mA
Electrostatic Discharge Threshold (HBM)	-	8000	V
Thermal Resistance (Junction / Ambient)	R _{th JA} ^[2]	690	°C/W
Thermal Resistance (Junction / Solder point)	R _{th JS} ^[2]	450	°C/W

Notes:
1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. R_{R_1, L_2} Results from mounting on PC board FR4 (pad size \geq 16 mm² per pad).
3. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

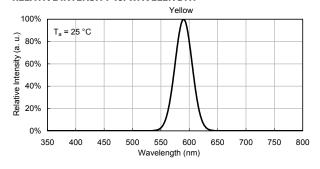


The dominant wavelength (λd) above is the setup value of the sorting machine. (Tolerance λd:±1nm.)
 Forward voltage: ±0.1V.
 Wavelength value is traceable to CIE127-2007 standards.
 Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.

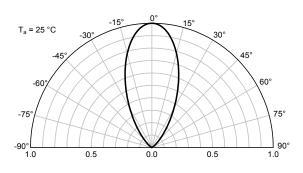


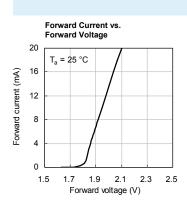
TECHNICAL DATA

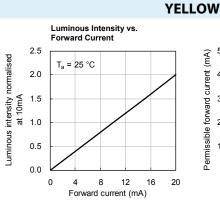
RELATIVE INTENSITY vs. WAVELENGTH

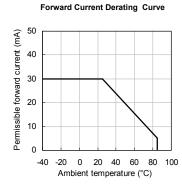


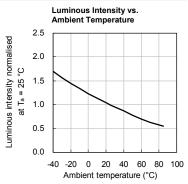
SPATIAL DISTRIBUTION



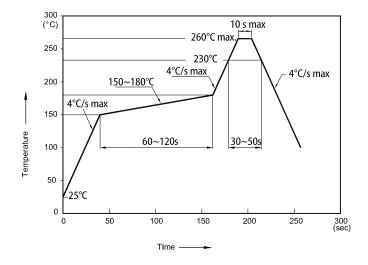






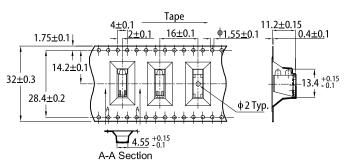


REFLOW SOLDERING PROFILE for LEAD-FREE SMD PROCESS

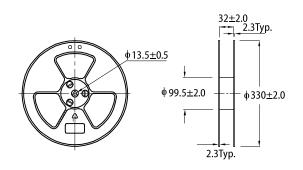


- 1.0 We recommend the reflow temperature 245°C(+/-5°C). The maximum soldering temperature should be limited to 260°C.
- Don't cause stress to the LEDs while it is exposed to high temperatureNo more than once.
- A. Reflow soldering is recommended. Other soldering methods are not recommended as they might cause damage to the product.

TAPE SPECIFICATIONS (units:mm)

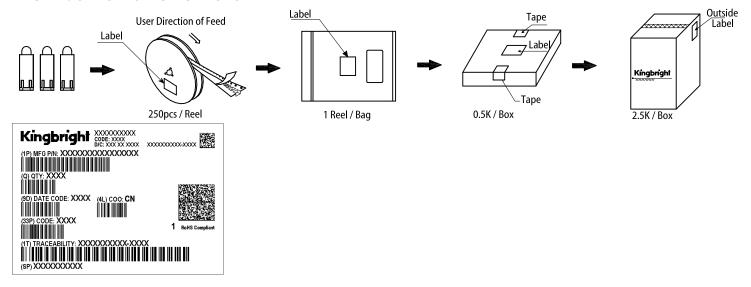


REEL DIMENSION (units: mm)





PACKING & LABEL SPECIFICATIONS



PRECAUTIONS

- 1. A moisture barrier bag (MBB) containing LEDs shall be kept in an environment with temperature below 40°C and humidity below 90% RH.
 - A MBB shall be kept sealed until the LEDs contained in that bag are to be used immediately.
 - Storage in an environment with temperature 5~30°C and humidity below 60% RH.
- 2. After a MBB has been opened, all LEDs contained in that bag shall complete soldering process within according to the conditions listed on the Kingbright MBB.
- 3. If the 10% spot of a humidity indicator card (HIC) indicates wet, LEDs shall be baked according to the conditions listed on the Kingbright MBB.
- 4. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.



- 5. The tip of the soldering iron should never touch the lens epoxy.
- 6. After soldering, allow at least three minutes for the component to cool down to room temperature before further operations.
- 7. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.

PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer to the latest datasheet for the updated specifications.
- When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.

 The information in this document applies to typical usage in consumer electronics applications. If customer's application has special reliability requirements or have life-threatening
- liabilities, such as automotive or medical usage, please consult with Kingbright representative for further assistance.
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- All design applications should refer to Kingbright application notes available at https://doi.org/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1003/nat/10.1

