

## **Discription**

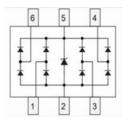
The 82400102 is a 5-channel ultra low capacitance rail clamp ESD protection diodes array. Each channel consists of a pair of ESD diodes that steer positive or negative ESD current to either the positive or negative rail. A zener diode is integrated in to the array between the positive and negative supply rails. In the typical applications, the negative rail pin (assigned as GND) is connected with system ground. The Positive ESD current is steered to the ground through an ESD diode and Zener diode and the positive ESD voltage is clamped to the zener voltage.

# Features

- ★ 5 channels of ESD protection
- ★ Provides ESD protection to IEC61000-4-2 level 4
  - ±27kV air discharge
  - ±15kV contact discharge
- ★ Channel I/O to GND capacitance: 0.4pF(Max)
- ★ Channel I/O to I/O capacitance: 0.8pF(Max)
- ★ Low clampingvoltage
- ★ Low operating voltage
- ★ Improved zener structure
- ★ Optimized package for easyhigh speed data lines PCB layout
- \* RoHS compliant.



SOT-23-6L



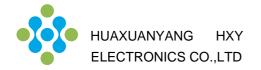
Circuit Diagram

#### **Ordering Information**

Product ID	Pack	Qty(PCS)
82400102	SOT-23-6L	3000

#### Absolute Ratings(Tamb = 25°C)

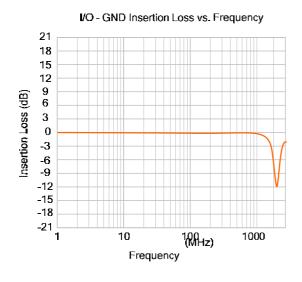
Characteristics	Symbol	Ratings	Unit
Peak Pulse Power(8/20µs)	$P_PP$	55	W
Peak Pulse Current(8/20µs)	I <sub>PP</sub>	4	А
ESD per IEC 61000-4-2(Air)	$V_{ESD1}$	±2 <b>0</b> kV	kV
ESD per IEC 61000-4-2(Contact)	V <sub>ESD2</sub>	±15kV	kV
Operating Temperature Range	Topr	-55 ~ +125	°C
Storage Temperature Range	Tstg	-55 ~ +150	°C

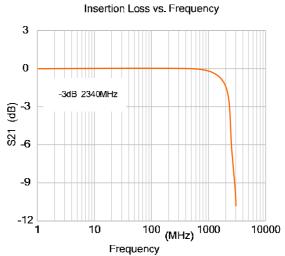


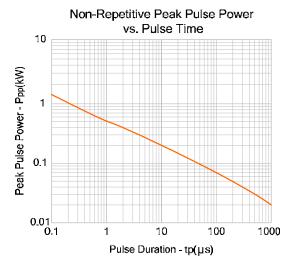
#### Electrical Characteristics (Tamb=25°C)

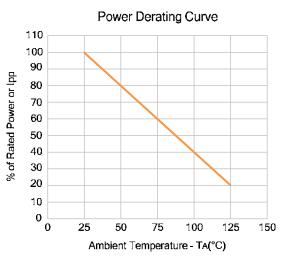
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working		Any I/O pin to GND			_	V
Voltage	$V_{RWM}$				5	V
Reverse Breakdown	$V_{BR}$	I t=1mA;	6			V
Voltage		Any I/O pin to GND				
Reverse Leakage	I <sub>R</sub>	V <sub>RWM</sub> =5V, T=25°C;			1	
Current		Any I/O pin to GND			Į.	μA
Positive Clamping Voltage	V <sub>C1</sub>	$I_{PP}=4A$ , $t_{P}=8/20 \mu s$ ;		8.5	12.0	V
		Positive pulse;				
		Any I/O pin to GND				
Negative Clamping Voltage	V <sub>C2</sub>	I <sub>PP</sub> =4A, t <sub>P</sub> =8/20µs;		1.8		V
		Negative pulse;				
		Any I/O pin to GND				
Junction Capacitance	C <sub>J1</sub>	V <sub>R</sub> =0V, f=1MHz;		0.3	0.4	pF
Between Channel		Between I/O pins		0.3		
Junction Capacitance	C <sub>J2</sub>	V <sub>R</sub> =0V, f=1MHz;		0.6	0.8	pF
Between I/O And GND		Any I/O pin to GND				

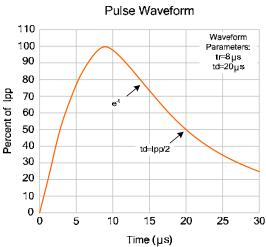
# **Typical Characteristics**

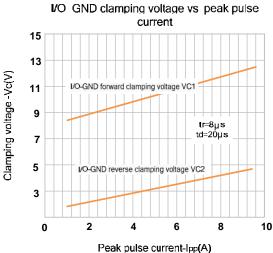


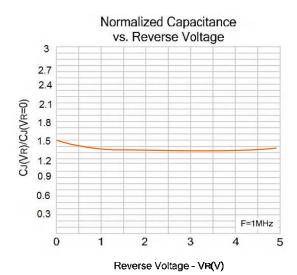




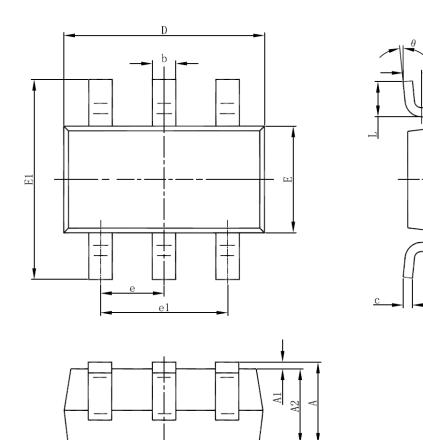


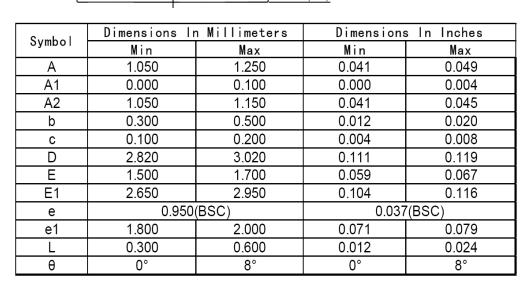






### **SOT-23-6L Package Information**







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