JLHF200V65R62E7DN

L62 PACK module with Trench/Fieldstop IGBT and Emitter Controlled diode and NTC

RoHS

Features

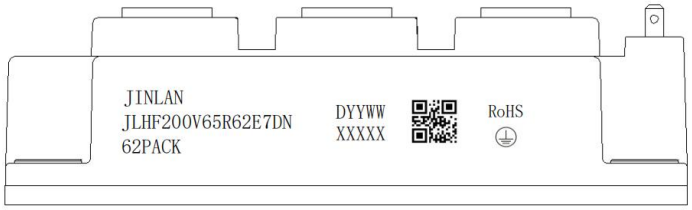
- Low $V_{CE(sat)}$ Trench IGBT technology
- $V_{CE(sat)}$ with positive temperature coefficient
- Maximum junction temperature 175℃
- Low inductance case
- Al_2O_3 substrate with low thermal resistance

L62 Pack

MARKING DIAGRAM

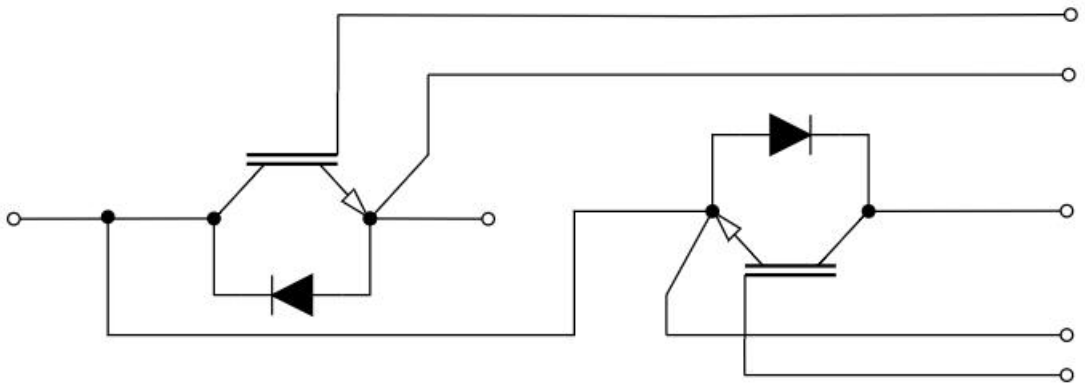
Typical Applications

- Switched mode power supplies
- Servo Drives
- UPS Systems
- Welding inverters



JINLAN	= Company Name
JLHF200V65R62E7DN	= Specific Device Code
YYWW	= Year and Work Week Code
XXXXX	= Serial Number
QR code	= Custom Assembly Information

Description



Package Insulation coordination

Parameter	Symbol	Note or test condition	Values	Unit
Isolation test voltage	V_{ISOL}	RMS,f = 50 Hz,t = 60 s	2.5	kV
Internal isolation		basic insulation(class 1, IEC 61140)	Al_2O_3	
Creepage distance	d_{creep}	terminal to heatsink	29.0	mm
Creepage distance	d_{creep}	terminal to terminal	23.0	mm
Clearance	d_{clear}	terminal to heatsink	23.0	mm
Clearance	d_{clear}	terminal to terminal	11.0	mm
Comparative tracking index (electrical)	CTI		>400	
RTI Elec.	RTI	housing	140	°C

Package Characteristic values

Parameter	Symbol	Note or test condition		Values			Unit
				Min.	Typ.	Max.	
Stray Inductance	L_{CE}			--	20	--	nH
Module Lead Resistance, Terminal to Chip	$R_{CC'+EE'}$	T_C=25°C, per switch		--	0.7	--	mΩ
Storage temperature	T_{stg}			-40	--	125	°C
Mounting torque for module mounting	M	-Mounting according to valid application note	M6, Screw	3.00	--	6.00	Nm
Terminal connection torque	M	-Mounting according to valid application note	M6, Screw	2.5	--	5.0	Nm
Weight	G			--	340	--	g

IGBT

Absolute Maximum Ratings (T_c = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{CES}	Collector-Emitter Voltage	650	V
V_{GES}	Gate-Emitter Voltage	±30	V
I_{CDC}	Continuous Collector Current @ T_C = 100 C	200	A
I_{CM}	Pulsed Collector Current，t_p limited by $T_{vj\,max}$	600	A
P_{tot}	Total power dissipation,T_C = 25 °C, $T_{vj\,max}$ = 175 °C	608	W

Characteristics （T_c=25℃ unless otherwise noted）

Symbol	Parameter	Test Condition		Min	Typ	Max	Unit
$V_{CE(sat)}$	Collector−Emitter Saturation Voltage	I_C=200 A, V_{GE} = 15 V	T_{vj} = 25 °C	--	1.45	2.10	V
			T_{vj} = 125 °C	--	1.60	--	
			T_{vj} = 175 °C	--	1.65	--	
$V_{GE(TH)}$	Gate−Emitter Threshold Voltage	I_C=5 mA,V_{CE}=V_{GE}		4.00	4.75	5.50	V
I_{CES}	Collector−Emitter Cutoff Current	V_{GE} = 0 V, V_{CE} = 1200 V		--	--	500	μA
I_{GES}	Gate-Emitter Leakage Current	V_{GE} = ±30 V, V_{CE} = 0 V, T_{vj} = 25 °C		--	--	100	nA
R_{Gint}	Internal Gate Resistance	f=1 MHz		--	0.25	--	Ω
C_{ies}	Input Capacitance	V_{CE}=25 V,V_{GE}=0 V, f=1 MHz		--	11.80	--	nF
C_{oes}	Output Capacitance			--	0.40	--	nF
C_{res}	Reverse Transfer			--	0.08	--	nF
Q_G	Gate Charge	V_{CC}=480V, V_{GE}=15V, I_C=200 A		--	0.416	--	μC
$t_{d(on)}$	Turn−On Delay Time	V_{CC}=400V,I_C=200 A V_{GE}=0/15V, R_g=5.7Ω, Inductive Load T_{vj} = 25 °C		--	172	--	ns
t_r	Rise Time			--	64	--	
$t_{d(off)}$	Turn−off Delay Time			--	544	--	
t_f	Fall Time			--	80	--	
E_{on}	Turn−On Switching Loss per Pulse			--	4.36	--	mJ
E_{off}	Turn Off Switching Loss per Pulse			--	1.36	--	
$t_{d(on)}$	Turn−On Delay Time	V_{CC}=400V,I_C=200 A, V_{GE}=0/15V, R_g=5.7Ω, Inductive Load T_{vj} = 125 °C		--	TBD	--	ns
t_r	Rise Time			--	TBD	--	
$t_{d(off)}$	Turn−off Delay Time			--	TBD	--	
t_f	Fall Time			--	TBD	--	
E_{on}	Turn−on Switching Loss per Pulse			--	TBD	--	mJ
E_{off}	Turn Off Switching Loss per Pulse			--	TBD	--	
$t_{d(on)}$	Turn−On Delay Time	V_{CC}=400V,I_C=200 A, V_{GE}=0/15V, R_g=5.7Ω, Inductive Load T_{vj} = 175 °C		--	TBD	--	ns
tr	Rise Time			--	TBD	--	
$t_{d(off)}$	Turn−off Delay Time			--	TBD	--	
t_f	Fall Time			--	TBD	--	
E_{on}	Turn−on Switching Loss per Pulse			--	6.72	--	mJ
E_{off}	Turn Off Switching Loss per Pulse			--	3.32	--	
R_{thJC}	Thermal resistance	Junction-to-Case (per IGBT)		--	0.22	-	K/W
$T_{vj\,op}$	Temperature under switching conditions			-40		175[1)]	℃

[1)] $T_{vj\,op}$ > 150℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

Diode

Absolute Maximum Ratings (Tc = 25°C unless otherwise noted)

Symbol	Description	Value	Unit
V_{RRM}	Repetitive Peak Reverse Voltage	650	V
I_F	Diode Continuous Forward Current	200	A
I_{FM}	Diode Maximum Forward Current t_p=1ms	600	A

Characteristics （Tc=25℃ unless otherwise noted）

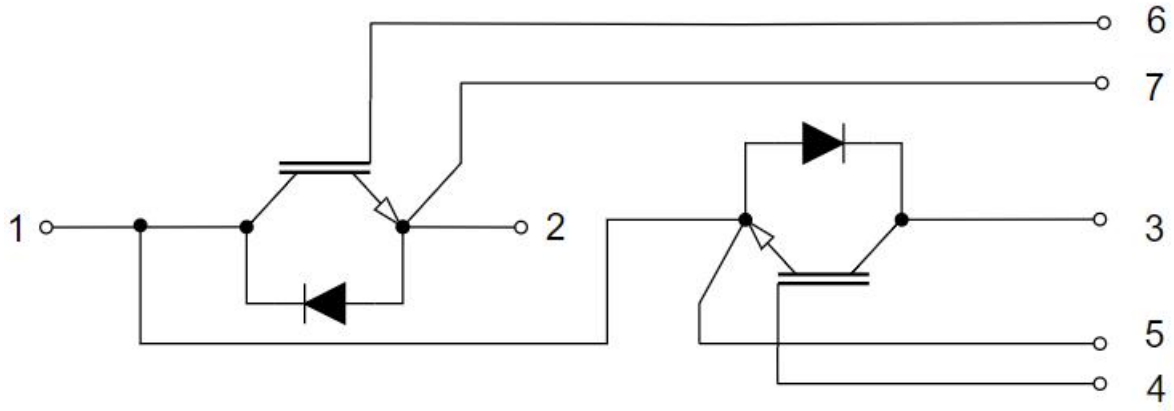
Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
V_F	Diode Forward Voltage	I_F = 200 A, T_{vj} = 25 °C	--	1.70	2.40	V
		I_F =200 A, T_{vj} = 125 °C	--	1.60	--	
		I_F =200 A, T_{vj} = 175 °C	--	1.55	--	
T_{rr}	Reverse Recovery Time	I_F=200 A,R_G=5.7 Ω T_{vj} = 25 °C	--	360	--	ns
I_{RM}	Peak Reverse Recovery Current		--	104	--	A
Q_{rr}	Recovered Charge		--	4.92	--	μC
E_{rec}	Reverse Recovery Energy		--	0.64	--	mJ
T_{rr}	Reverse Recovery Time	I_F=200 A,R_G=5.7 Ω T_{vj} = 125 °C	--	TBD	--	ns
I_{RM}	Peak Reverse Recovery Current		--	TBD	--	A
Q_{rr}	Recovered Charge		--	TBD	--	μC
E_{rec}	Reverse Recovery Energy		--	TBD	--	mJ
T_{rr}	Reverse Recovery Time	I_F=200 A,R_G=5.7 Ω T_{vj} = 175 °C	--	TBD	--	ns
I_{RM}	Peak Reverse Recovery Current		--	TBD	--	A
Q_{rr}	Recovered Charge		--	TBD	--	μC
E_{rec}	Reverse Recovery Energy		--	TBD	--	mJ
R_{thJC}	Thermal resistance	Junction-to-Case (per diode)	--	0.23	--	K/W
$T_{vj\ op}$	Temperature under switching conditions		-40		175[2)]	℃

[2)] $T_{vj\ op}$ > 150℃ is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

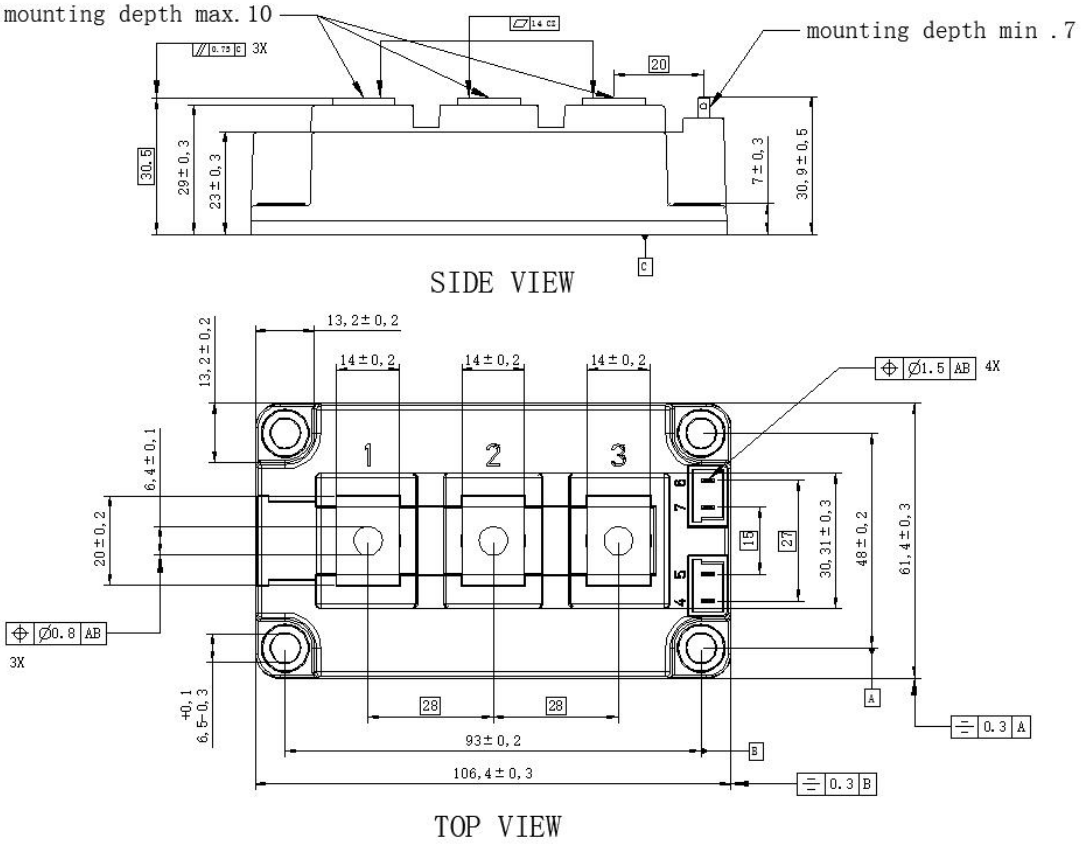
NTC Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
R_{25}	Rated Resistance		--	5.0	--	kΩ
ΔR/R	Deviation of R_{100}	T_C=100 °C,R_{100}=493.3 Ω	-5	--	5	%
P_{25}	Power Dissipation		--	--	20.0	mW
$B_{25/50}$	B-value	$R_2=R_{25}\exp[B_{25/50}(1/T_2 - 1/(298.15K))]$	--	3375	--	K
$B_{25/80}$	B-value	$R_2=R_{25}\exp[B_{25/80}(1/T_2 - 1/(298.15K))]$	--	3411	--	K
$B_{25/100}$	B-value	$R_2=R_{25}\exp[B_{25/100}(1/T_2 - 1/(298.15K))]$	--	3433	--	K

CIRCUIT DIAGRAM



PACKAGE DIMENSION



REVISION HISTORY

Document version	Date of release	Description of changes
Rev.00	2024-10-18	Preview

ATTENTION

■ Any and all Jinlan power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Jinlan Power Semiconductor representative nearest you before using any Jinlan power products described or contained herein in such applications.

■ Jinlan Power Semiconductor assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Jinlan power modules described or contained herein.

■ Specifications of any and all Jinlan power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ Jinlan Power Semiconductor (Wuxi).co.,LTD. strives to supply high-quality high-reliability products. However,any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

■ In the event that any or all Jinlan power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations,such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

■ No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Jinlan Power Semiconductor (Wuxi).co.,LTD.

■ Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Jinlan Power Semiconductor believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

■ Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Jinlan power product that you intend to use.

■ This catalog provides information as of Oct.2024. specifications and information herein are subject to change without notice.