

RU3040M2-VB Datasheet **N-Channel 30-V (D-S) MOSFET**

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)
30	0.0045 at V _{GS} = 10 V	40	26.5 nC
	0.0060 at V _{GS} = 4.5 V	33.3	

FEATURES

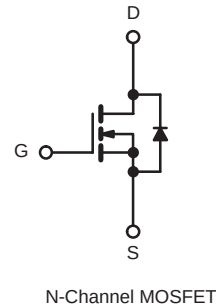
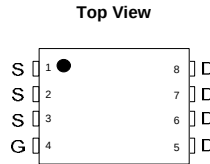
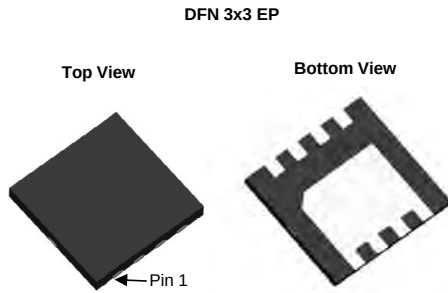
- Halogen-free
- Trench Power MOSFET
- 100 % R_g and UIS Tested



RoHS
COMPLIANT

APPLICATIONS

- DC/DC Conversion
- Low-Side Switch
- Notebook PC
- Gaming



ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	± 20	V
Continuous Drain Current (T _J = 150 °C)	I _D	40	A
		32.6	
		31.5 ^{b, c}	
		27.1 ^{b, c}	
Pulsed Drain Current	I _{DM}	70	A
Continuous Source-Drain Diode Current	I _S	5.4	A
		2.7 ^{b, c}	
Single Pulse Avalanche Current	I _{AS}	40	A
Avalanche Energy	E _{AS}	80	mJ
Maximum Power Dissipation	P _D	6.0	W
		3.3	
		3.0 ^{b, c}	
		1.9 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R _{thJA}	33	42	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	16	21	

Notes:

- Based on T_C = 25 °C.
- Surface Mounted on 1" x 1" FR4 board.
- t = 10 s.
- Maximum under Steady State conditions is 85 °C/W.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 1 mA	30			V		
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		27		mV/°C		
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 5.6				
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.5		3.0	V		
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA		
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10			
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.0045		Ω		
		V _{GS} = 4.5 V, I _D = 10 A		0.0060				
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A		75		S		
Dynamic ^b								
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		2545		pF		
Output Capacitance	C _{oss}			450				
Reverse Transfer Capacitance	C _{rss}			140				
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 10 A		62		nC		
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 10 A		26.5				
Gate-Drain Charge	Q _{gd}			8.5				
Gate Resistance	R _g			7.3				
Turn-On Delay Time	t _{d(on)}	f = 1 MHz	0.2	1.1	2.2	Ω		
Rise Time	t _r		V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 4.5 V, R _g = 1 Ω		35		60	ns
Turn-Off Delay Time	t _{d(off)}				16		30	
Fall Time	t _f				48		85	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.5 Ω I _D ≅ 10 A, V _{GEN} = 10 V, R _g = 1 Ω			16	30		
Rise Time	t _r			18	35			
Turn-Off Delay Time	t _{d(off)}			8	16			
Fall Time	t _f			41	75			
				8	18			
Drain-Source Body Diode Characteristics								
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			5.4	A		
Pulse Diode Forward Current ^a	I _{SM}				70			
Body Diode Voltage	V _{SD}	I _S = 3 A		0.72	1.1	V		
Body Diode Reverse Recovery Time	t _{rr}	I _F = 10 A, dI/dt = 100 A/μs, T _J = 25 °C		33	65	ns		
Body Diode Reverse Recovery Charge	Q _{rr}			27	54	nC		
Reverse Recovery Fall Time	t _a			17		ns		
Reverse Recovery Rise Time	t _b			16				

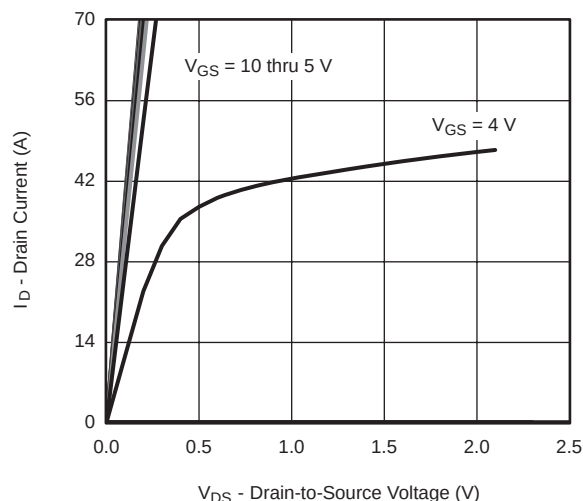
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

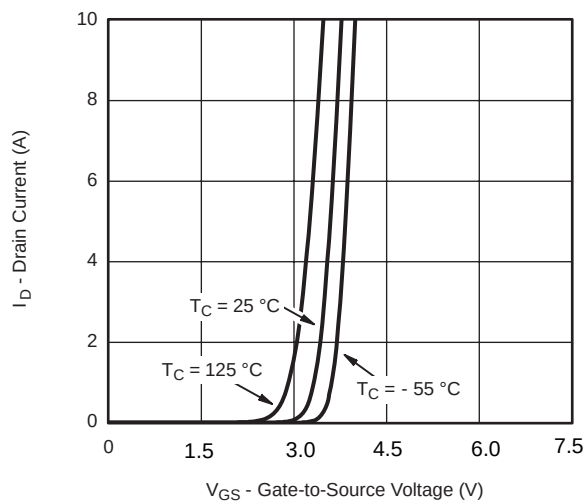
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

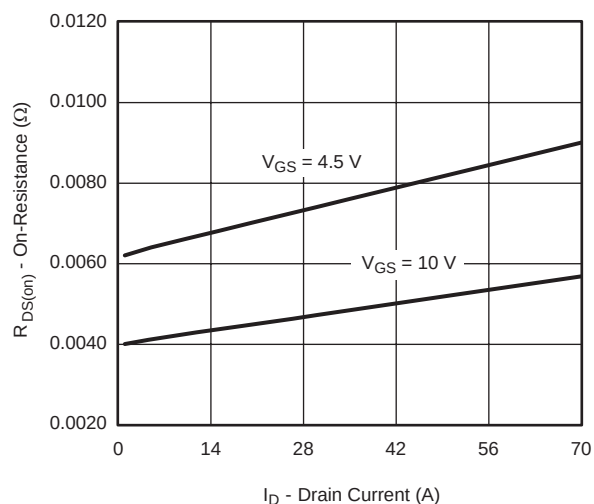
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



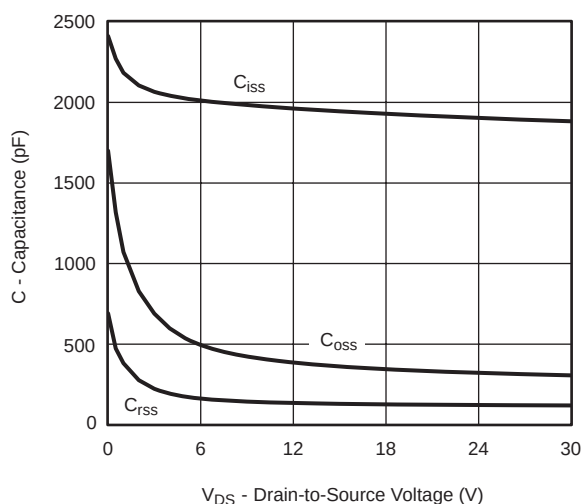
Output Characteristics



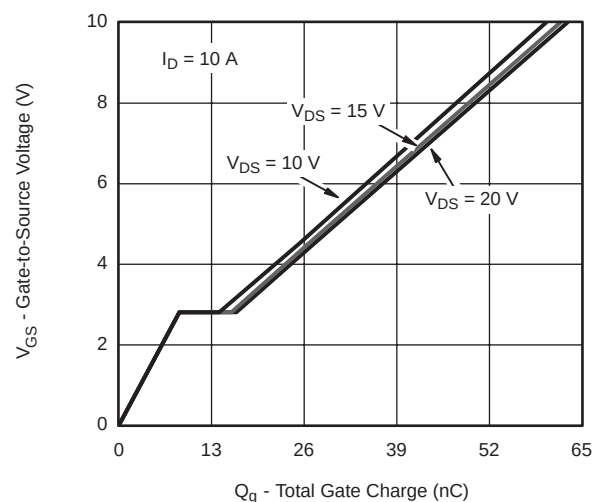
Transfer Characteristics



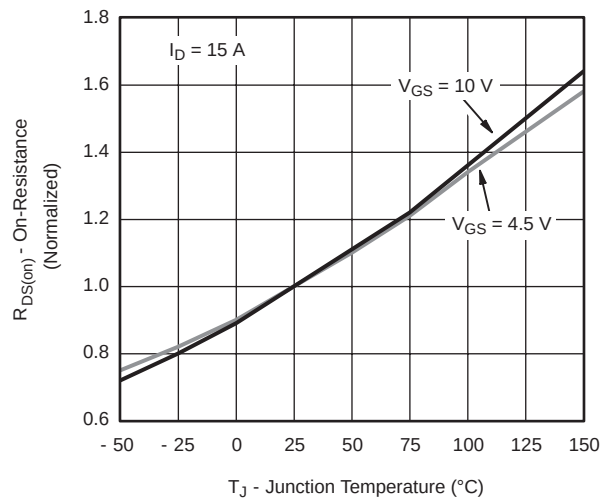
On-Resistance vs. Drain Current and Gate Voltage



Capacitance

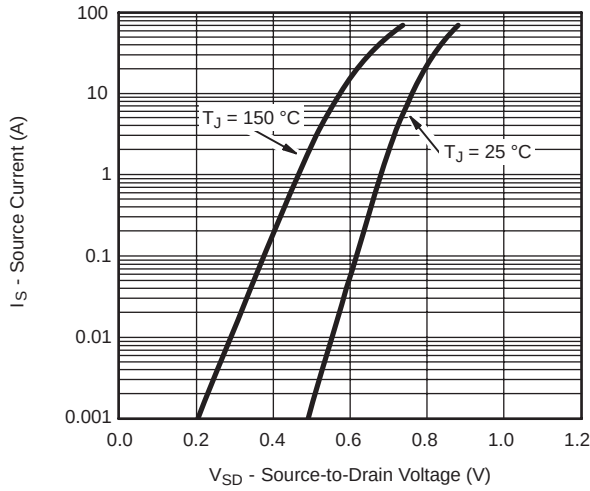


Gate Charge

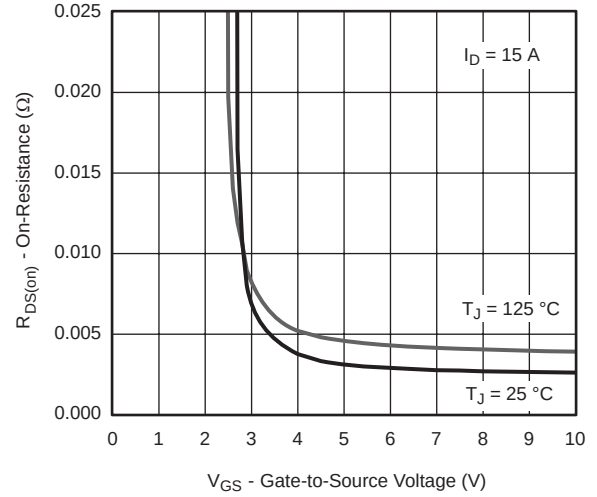


On-Resistance vs. Junction Temperature

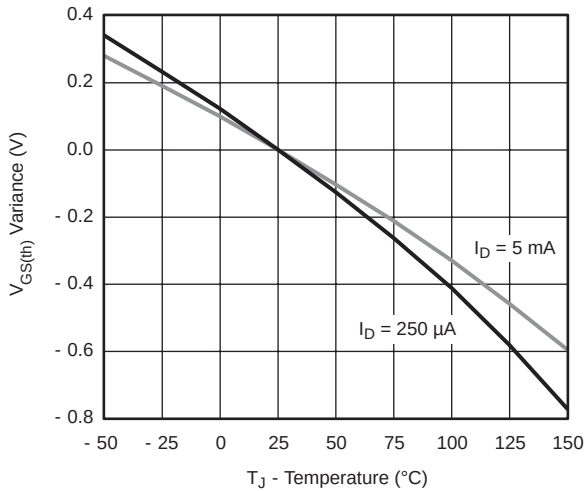
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



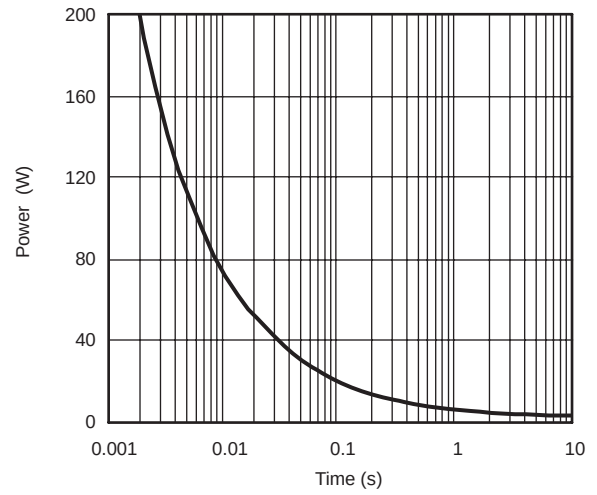
Source-Drain Diode Forward Voltage



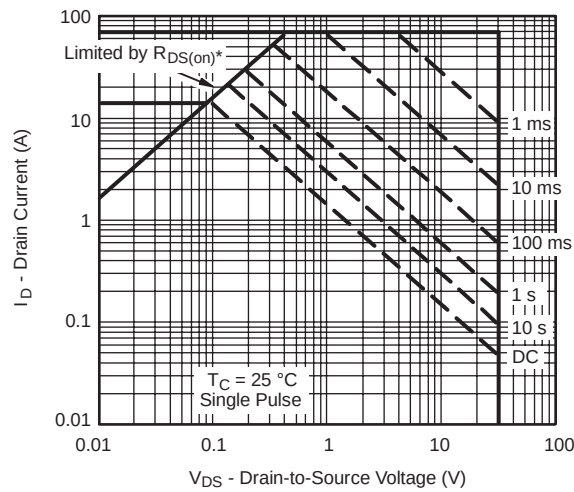
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage



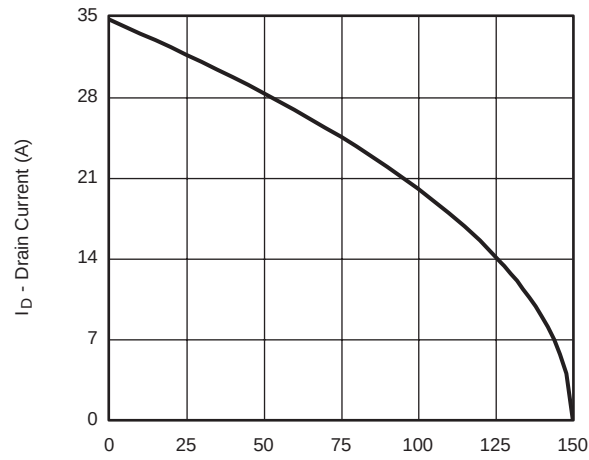
Single Pulse Power



* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified

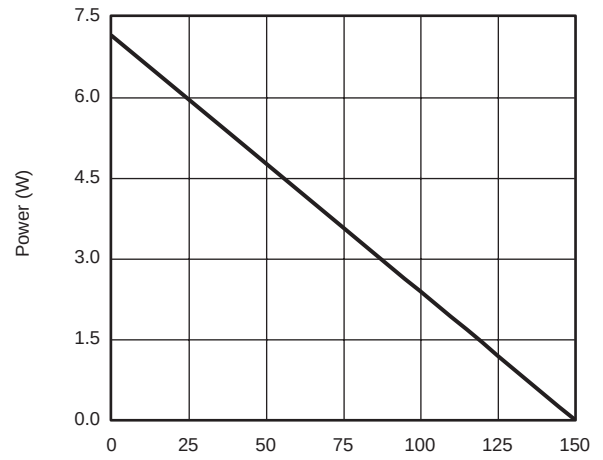
Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



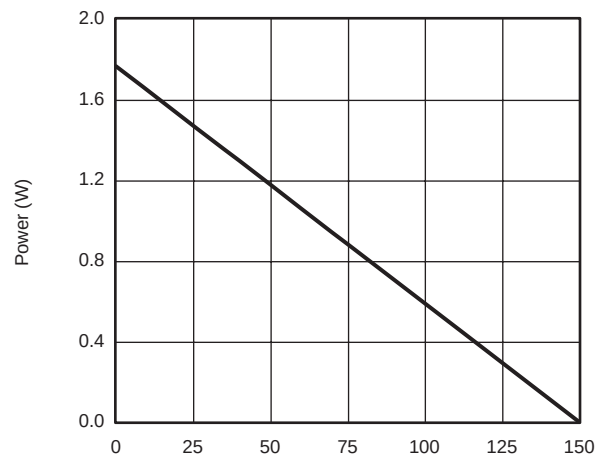
T_C - Case Temperature (°C)

Current Derating*



T_C - Case Temperature (°C)

Power Derating, Junction-to-Foot

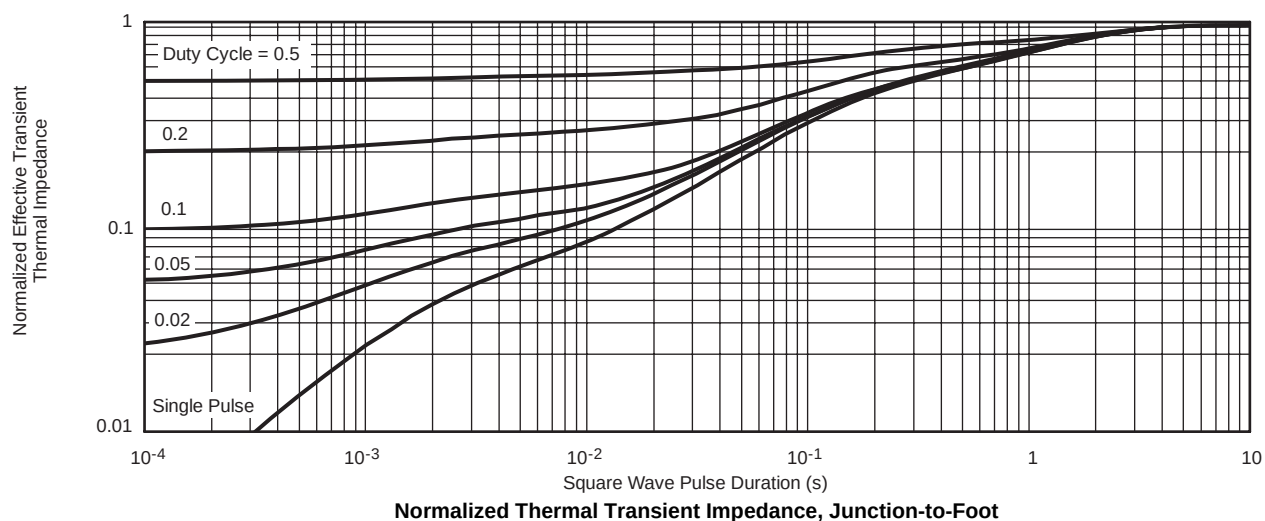
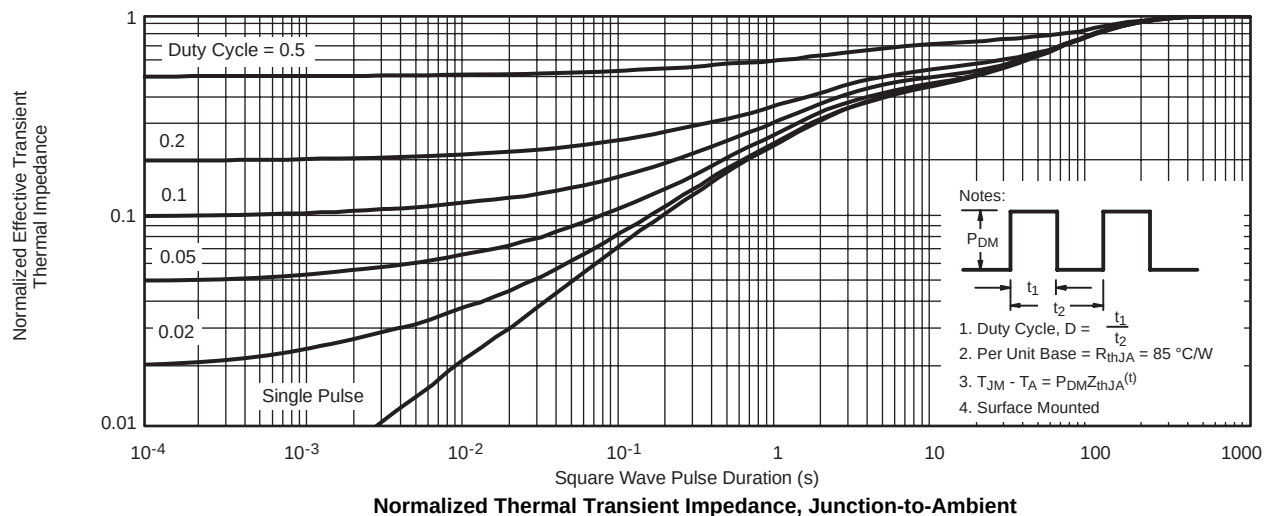


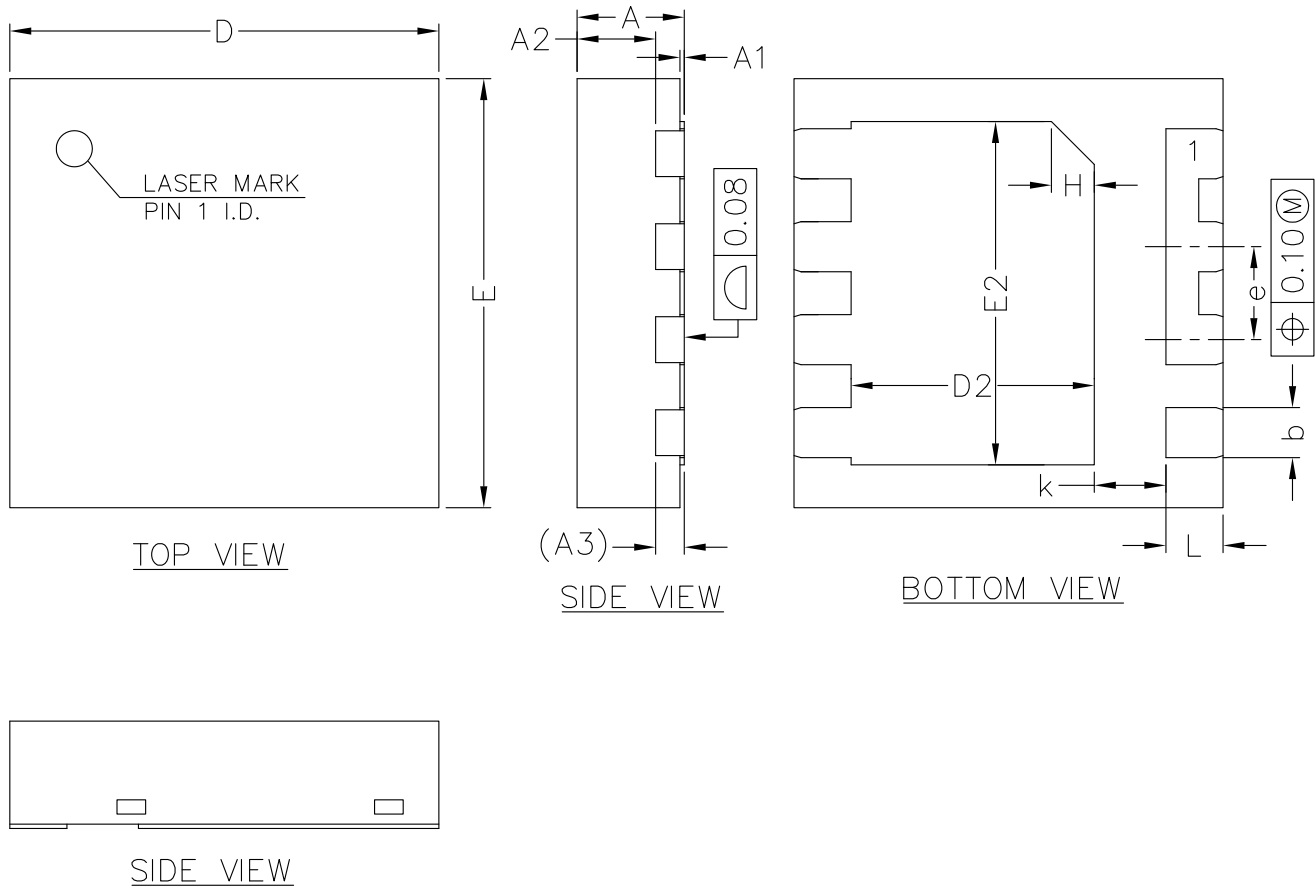
T_A - Ambient Temperature (°C)

Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
A2	0.50	0.55	0.60
A3	0.20REF		
b	0.30	0.35	0.40
D	2.90	3.00	3.10
E	2.90	3.00	3.10
D2	1.60	1.70	1.80
E2	2.30	2.40	2.50
e	0.55	0.65	0.75
K	0.40	0.50	0.60
L	0.35	0.40	0.45

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