APPROVAL SHEET

SF25Q&R_A, SF20Q&R _A, SF10Q&R_A, SF12Q&R_A, SF08Q&R_A, SF06Q&R_A, SF04Q&R_A

±1.0%, ±0.5%, ±0.25%, ±0.1%, ±0.05%

TC50, TC25

High Precision Power Thin Film chip resistors

Size: 2512,2010,1210,1206,0805,0603,0402

(Anti-Sulfuration)





*Contents in this sheet are subject to change without prior notice.

FEATURE

- 1. SMD metal film resistor
- 2. High reliability and stability of 0.5% and below per customer request
- 3. High performance of TCR: 50 25 ppm/K and below per customer request
- 4. Low current noise
- 5. Sulfuration resistant Oil 105'C +3.5% Sulfur powder x 500hrs
- 6. RoHS compliant and Lead free products

APPLICATION

- Medical equipment
- · Measuring instrument
- Communication device
- Computer
- Printer

DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive layer that is applied to the top surface of the substrate. The composition of the resistive layer is adjusted to give the approximate resistance required and the value is trimmed to nominated value within tolerance which controlled by laser trimming of this resistive layer. The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For environmental soldering issue, the outer layer of these end terminations is a Lead-free solder.

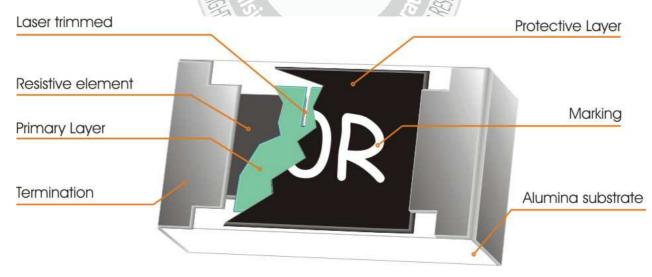


Fig 1. Construction of Chip-R SFxxQ&R_A



QUICK REFERENCE DATA

Item		General Specification					
Series No.	SF25Q&R _A	SF20Q&R _A	SF10Q&R _A	SF12Q&R _A	SF08Q&R _A	SF06Q&R _A	SF04Q&R _A
Size Code	2512	2010	1210	1206	0805	0603	0402
Size Code	(6432)	(5025)	(3225)	(3216)	(2012)	(1608)	(1005)
Resistance Tolerance	±1.0%, ±0.5%, ±0.25%, ±0.1%, ±0.05%						
Resistance Range	1Ω ~ 3ΜΩ	4.7Ω ~ 3MΩ	4.7Ω ~ 2.5MΩ	1Ω ~ 2.5MΩ	4.7Ω ~ 2MΩ	4.7Ω ~ 1MΩ	10Ω ~255KΩ
TCR			±:	50 & 25 ppm/°	С		
Max. Dissipation at T _{amb} =70°C	1W	3/4W	2/5W	1/4W	1/8W	1/10W	1/10W
Max. Operating Voltage	200V	200V	200V	200V	150V	75V	50V
Max. Overload Voltage	400V	400V	400V	400V	300V	150V	100V
Operating Temperature Range	- 55~ +155°C						

Note:

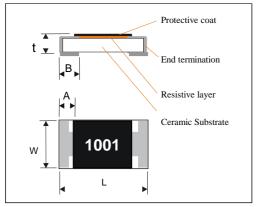
- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

 $RCWV = \sqrt{Rated Power \times Resistance Value}$ or Max. RCWV listed above, whichever is lower.

3. Tolerance: ±0.01% is upon requested

DIMENSIONS: (unit:mm)

Туре	SF25	SF20	SF10	SF12	SF08	SF06	SF04
L	6.35 ± 0.10	5.00 ± 0.10	3.10 ± 0.10	3.05 ± 0.15	2.00 ± 0.10	1.55 ± 0.10	1.00 ± 0.10
W	3.20 ± 0.15	2.50 ± 0.15	2.60 ± 0.15	1.55 ± 0.15	1.25 ± 0.10	0.80 ± 0.10	0.50 ± 0.05
Α	0.60 ± 0.20	0.60 ± 0.20	0.50 ± 0.20	0.40 ± 0.20	0.25 ± 0.20	0.30 ± 0.15	0.30 ± 0.15
В	0.90 ± 0.25	0.60 ± 0.25	0.50 ± 0.20	0.40 ± 0.20	0.40 ± 0.20	0.30 ± 0.15	0.30 ± 0.15
t	0.55 ± 0.10	0.55 ± 0.10	0.55 ± 0.10	0.55 ± 0.15	0.50 ± 0.15	0.45 ± 0.15	0.35 ± 0.05



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MARKING

• 3-digits marking for 0603 size

SFxxR_A has same marking rule as WRxx ±1%.

3-digits marking (±1%:0603)

Nominal	resistan	ce							Descr	iption					
1.E-24 s	eries			As <i>0603</i>	0603 WR06X ±5%.										
2.E-96 s	eries			The 1st two digit codes are referring to the CODE o value:				CODE or	the tab	he table, the 3rd code is the index of resistance				stance	
				Y=10 ⁻² ,	X=10 ⁻¹ ,	A=10 ⁰ ,	B=10 ¹ ,	C=10 ² ,	D=10 ³ ,	E=10 ⁴ ,	F=10 ⁵				
						EX:	17.8 Ω=	25X,17	′8Ω=25A	,1K78	=25B				
							17K8=2	25C · 17	78K=25D	, 1M78	=25E				
3. Rema	ark			There is	no marki	ng for th	e items a	re not u	nder E-24	and E-	96 series				
CODE	R_value	CODE	R_value	CODE	R_Value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value	CODE	R_value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
08	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45 PASSIV	287	57 M ALLI	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

4-digits marking for 2512, 2010, 1210, 1206, 0805 size

For E24+E96, each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value. For values below $97\Omega6$ the R is used as a digit. For values of 100Ω or greater, the first 3 digits are significant, the fourth digit indicates the number of multiple to follow.

Example

RESISTANCE	10Ω	12Ω	100Ω	6800Ω	47000Ω
4-digits marking	10R0	12R0	1000	6801	4702

No marking code for 0402 size

FUNCTIONAL DESCRIPTION

Product characterization

Standard values of nominal resistance are taken from the E192 & E24 series for resistors with a tolerance of $\pm 0.5\%$, $\pm 0.25\%$, $\pm 0.1\%$. The values of the E24/E192 series are in accordance with "IEC publication 60063"

Derating

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

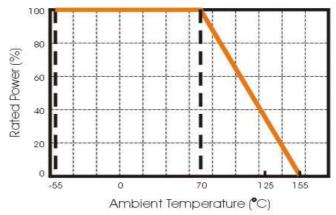


Fig. 2 Maximum dissipation in percentage of rated power As a function of the ambient temperature

MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.



SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds within lead-free solder bath. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3. and Table 1.

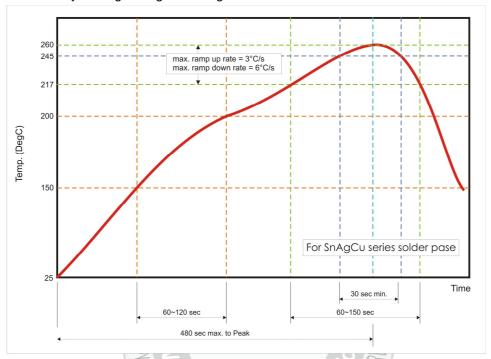


Fig. 3 Infrared soldering profile for Chip Resistors

Table 1. Infrared soldering condition for Chip Resistors

Temperature Condition	Exposure Time
Average ramp-up rate (217°C to 260°C)	Less than 3°C/second
Between 150 and 200°C	Between 60-120 seconds
> 217°C	Between 60-150 seconds
Peak Temperature	260°C +0/-5°C
Time within 245°C	Min. 30 seconds
Ramp-down rate (Peak to 217°C)	Less than 6°C/second
Time from 25°C to Peak	No greater than 480 seconds



CATALOGUES NUMBERS

The resistors have a catalogue number starting with .

SF04	Q/R	xxxx	D	Т	L	Α
Size code	Type code	Resistance code	Tolerance	Packaging	Termination	Special code
	Q: TCR=50ppm R: TCR=25ppm	E192+E24:	F : ±1.0%	code	code	A = Antisulfur
SF20: 2010	(High power)	3 significant digits followed by no. of zeros	D:±0.5%	T:Taped & Reeled		Under Oil 105'C+3.5%
SF10: 1210		102Ω =1020	C: ±0.25%		(lead free)	Sulfur powder 500hrs
SF12: 1206		$37.4K\Omega = 3742$	B:±0.1%			3001115
SF08: 0805		37.4832 = 3742 $220\Omega = 2200$	A:±0.05%			
SF06: 0603		22002 =2200				
SF04: 0402						

^{*} Anti-Sulfur test conditions: Oil 105'C, 3.5% Sulfur powder, 500hrs, acceptance criteria: +/-1.0%

1. Reeled tape packaging: 8mm width paper taping.

5,000pcs/reel for SF10, SF12, SF08, SF06

10,000pcs/reel for SF04

2. Reeled tape packaging: 12mm width paper taping

4,000pcs/reel for SF25, SF20





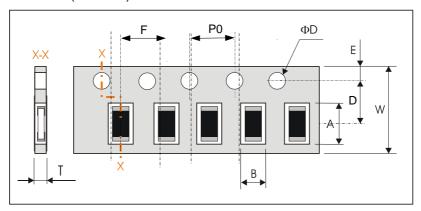
TEST AND REQUIREMENTS(JIS C 5201-1: 1998)

TEST	PROCEDURE	REQUIREMENT
IESI	PROCEDURE	Resistor
DC resistance Clause 4.5	DC resistance values measured at the test voltages specified below :	Within the specified tolerance
Temperature Coefficient of Resistance(T.C.R) Clause 4.8	Natural resistance change per change in degree centigrade. $\frac{R_2-R_1}{R_1(t_2-t_1)}\!\!\times\!10^6 \; \text{(ppm/°C)}$ $\text{R}_1: \text{Resistance at reference temperature}$ $\text{R}_2: \text{Resistance at test temperature}$ $\text{t}_1: 20\text{C}+5\text{C}-1\text{C}$	Refer to "QUICK REFERENCE DATA"
Short time overload (S.T.O.L) Permanent resistance change after a 5second application of a voltage 2.5 times RCWV or the maximum overload voltage specified in the above list, whichever is less.		Δ R/R max. ±(0.2%+0.05 Ω)
Resistance to soldering heat(R.S.H) Clause 4.18	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 260°C±5°C	no visible damage Δ R/R max. \pm (0.25%+0.05 Ω)
Solderability Clause 4.17	Un-mounted chips completely immersed for 2±0.5 second in a SAC solder bath at 235°C ±5°C	good tinning (>95% covered) no visible damage
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20℃+5℃/-1℃, 30 minutes at +125°C±3°C, 2~3 minutes at 20℃+5℃/-1℃, total 5 continuous cycles	no visible damage $\Delta R/R \text{ max. } \pm (0.5\% + 0.05\Omega)$
Load life (endurance) Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	Δ R/R max. \pm (0.5%+0.05 Ω)
Humidity Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	Δ R/R max. $\pm (0.5\% + 0.05\Omega)$
Bending strength Clause 4.33	Resistors mounted on a 90mm glass epoxy resin PCB(FR4); bending : 3 mm, once for 10 seconds.	Δ R/R max. ±(0.1%+0.05 Ω)
Adhesion Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or removal of the terminations.
Insulation Resistance Clause 4.6	Apply the maximum overload voltage (DC) for 1minute	R≥10GΩ
Dielectric Withstand Voltage Clause 4.7	Apply the maximum overload voltage (AC) for 1 minute	No breakdown or flashover
Sulfuration Test	Soaked in industrial oil with 3.5% sulfur concentration 105°C \pm 3°C, 500 hours	ΔR/R max. ±(1%+0.1Ω)



PACKAGING

Paper Tape specifications (unit :mm)

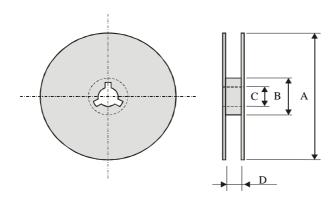


Series No.	А	В	W	D	E
SF25	6.90±0.20	3.60±0.20	12.00±0.30	5.50±0.10	1.75±0.10
SF20	5.50±0.20	2.80±0.20	12.00±0.30	5.50±0.10	1.75±0.10
SF12	3.60±0.20	2.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SF10	3.60±0.20	3.00±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SF08	2.40±0.20	1.65±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SF06	1.90±0.20	1.10±0.20	8.00±0.30	3.50±0.20	1.75±0.10
SF04	1.20±0.10	0.7±0.10	8.00±0.20	3.50±0.05	1.75±0.10

	8 4	PASSIVE SYSTEM		ED.
Series No.	F	P0	ΦD	Т
SF12	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1} _{-0.0}	Max. 1.0
SF25	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1}	Max 1.2
SF20	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1}	Max 1.2
SF10	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1} _{-0.0}	Max. 1.0
SF08	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1} _{-0.0}	Max. 1.0
SF06	4.00±0.10	4.00±0.10	Ф1.50 ^{+0.1} _{-0.0}	0.65±0.05
SF04	2.00±0.10	4.00±0.10	Ф1.50 ^{+0.1} _{-0.0}	0.40±0.05



Reel dimensions



SF25, SF20

Symbol	Α	В	С	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	14.0±0.5

SF12, SF10, SF08, SF06, SF04

Symbol	А	В	С	D
(unit : mm)	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5

SF02

		TAC DIDICH VELTV	THE COLUMN	
Symbol	Α	В	С	D
(unit : mm)	Ф180+0.0	$\Phi60^{+0.0}_{-1.5}$	13.0±0.2	$\Phi 9^{+1.0}_{-0.0}$

Taping quantity

- Chip resistors 4,000 pcs per reel (SF25, SF20)
- Chip resistors 5,000 pcs per reel (SF10, SF12, SF08, SF06)
- Chip resistors 10,000 pcs per reel (SF04)