

TM20G03GD

N+P-Channel Enhancement Mode Mosfet

General Description

- Low $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

Applications

- Load switch
- PWM

General Features

N Channel

$V_{DS} = 30V$ $I_D = 23A$

$R_{DS(ON)} = 15m\Omega$ (typ.) @ $V_{GS} = 10V$

P Channel

$V_{DS} = -30V$ $I_D = -17A$

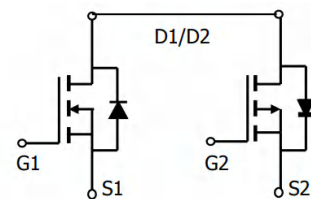
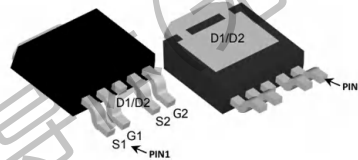
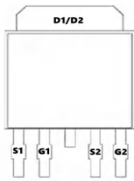
$R_{DS(ON)} = 33m\Omega$ (typ.) @ $V_{GS} = -10V$

100% UIS Tested

100% R_g Tested



GD:TO-252-4L



Marking: 20G03 OR 3012

Absolute Maximum Ratings ($T_A = 25^\circ C$ Unless Otherwise Noted)

Symbol	Parameter	Rating		Units
		N-Channel	P-Channel	
V_{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	± 20	± 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	23	-17	A
$I_D @ T_A = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	15	-12	A
I_{DM}	Pulsed Drain Current	55	-43	A
EAS	Single Pulse Avalanche Energy ²	34.6	28	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ⁴	16.8	10.8	W
T_{STG}	Storage Temperature Range	-55 to 175	-55 to 175	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 175	-55 to 175	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	32	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	6	$^\circ C/W$

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N-Electrical Characteristics (T_J=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} = 0V,	-	-	1.0	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.2	1.6	2.0	V
R _{DS(on)}	Static Drain-Source on-Resistance note3	V _{GS} =10V, I _D =5A	-	15	20	mΩ
		V _{GS} =4.5V, I _D =3A	-	21	29	
Dynamic Characteristics						
C _{iSS}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1.0MHz	-	490	-	pF
C _{oSS}	Output Capacitance		-	79	-	pF
C _{rSS}	Reverse Transfer Capacitance		-	61	-	pF
Q _g	Total Gate Charge	V _{DS} =15V, I _D =5.8A, V _{GS} =10V	-	10	-	nC
Q _{gs}	Gate-Source Charge		-	1.7	-	nC
Q _{gd}	Gate-Drain(“Miller”) Charge		-	2.5	-	nC
Switching Characteristics						
t _{d(on)}	Turn-on Delay Time	V _{DS} =15V, I _D =3A, V _{GS} =10V, R _{REN} =3Ω	-	6	-	ns
t _r	Turn-on Rise Time		-	15	-	ns
t _{d(off)}	Turn-off Delay Time		-	17	-	ns
t _f	Turn-off Fall Time		-	17	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain to Source Diode Forward Current		-	-	23	A
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	30	A
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =9A	-	-	1.2	V
t _{rr}	Body Diode Reverse Recovery Time	I _F =5A, dI/dt=100A/μs	-	7	-	ns
Q _{rr}	Body Diode Reverse Recovery Charge		-	2	-	nC

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition : T_J=25°C, V_{DD}=15V, V_G=10V, L=0.5mH, R_g=25Ω, I_{AS}=6A

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤0.5%

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P-Electrical Characteristics (T_J=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V	-	-	-1	μA
Gate-Source Leakage	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Gate-Source Threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1	-1.5	-2	V
Drain-Source on-State Resistance ³	R _{DS(on)}	V _{GS} = -10V, I _D = -4.1A	-	33	41	mΩ
		V _{GS} = -4.5V, I _D = -3A	-	42	59	
Dynamic Characteristics ⁴						
Input Capacitance	C _{iss}	V _{GS} = 0V , V _{DS} = -15V, f = 1.0MHz	-	530	-	pF
Output Capacitance	C _{oss}		-	70	-	
Reverse Transfer Capacitance	C _{rss}		-	56	-	
Switching Characteristics ⁴						
Total Gate Charge	Q _g	V _{GS} = -10V, V _{DS} = -15V, I _D = -4.1A	-	6.8	-	nC
Gate-Source Charge	Q _{gs}		-	1.0	-	
Gate-Drain Charge	Q _{gd}		-	1.4	-	
Turn-on Delay Time	t _{d(on)}	V _{GS} = -10V, V _{DS} = -15V , R _L = 15Ω, R _{GEN} = 2.5Ω	-	14	-	ns
Rise Time	t _r		-	61	-	
Turn-off Delay time	t _{d(off)}		-	19	-	
Fall Time	t _f		-	10	-	
Source-Drain Body Diode Characteristics						
Diode Forward Voltage ³	V _{SD}	I _S = -4.1A, V _{GS} = 0V	-	-	-1.2	V
Continuous Source Current	I _S		-	-	-17	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse width≤300μs, duty cycles≤2%.
4. This value is guaranteed by design hence it is not included in the production test.

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N-Channel Typical Characteristics

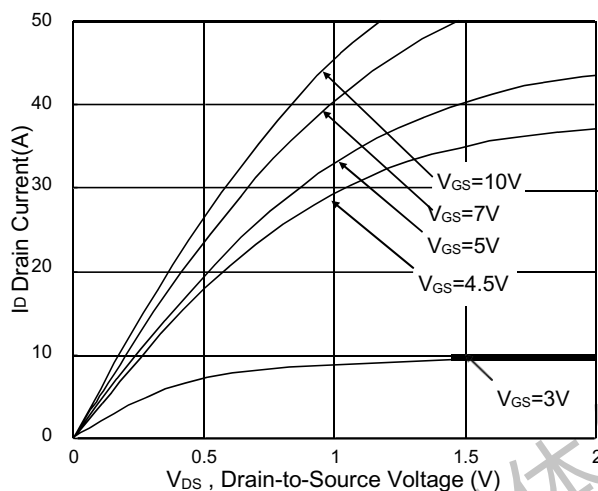


Fig.1 Typical Output Characteristics

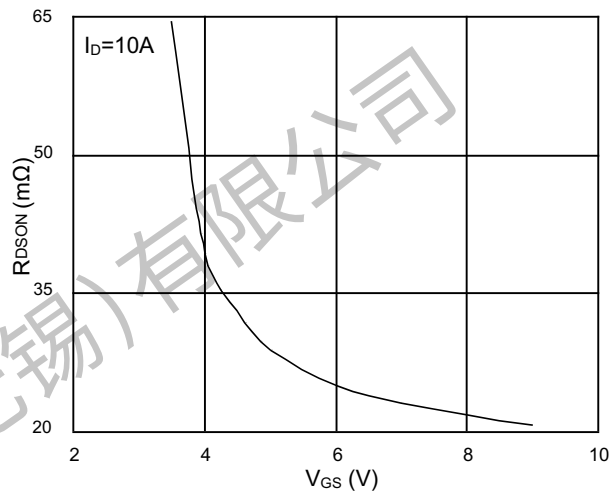


Fig.2 On-Resistance vs. Gate-Source

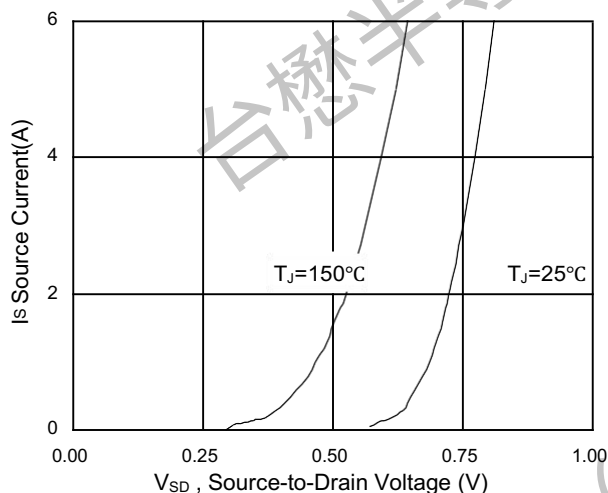


Fig.3 Forward Characteristics Of Reverse

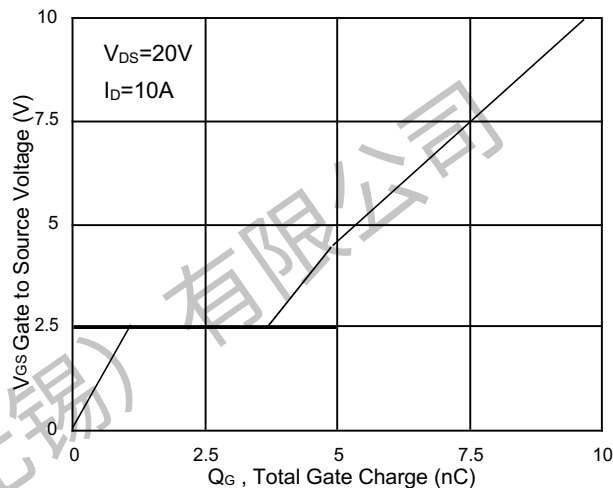


Fig.4 Gate-Charge Characteristics

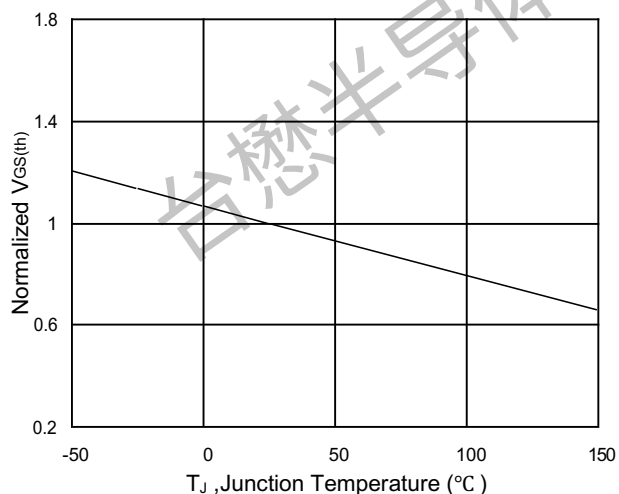


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

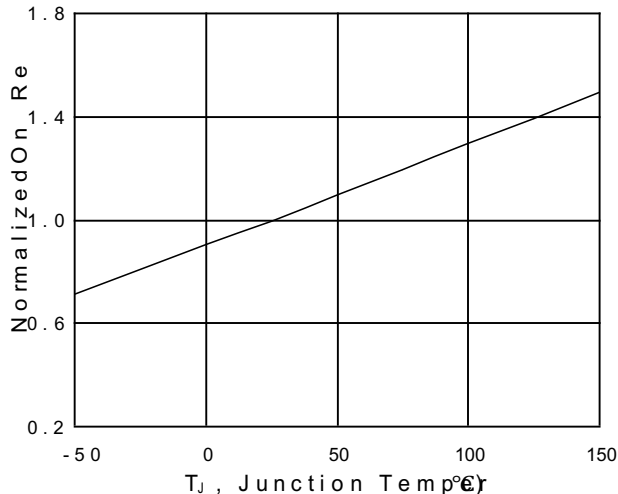


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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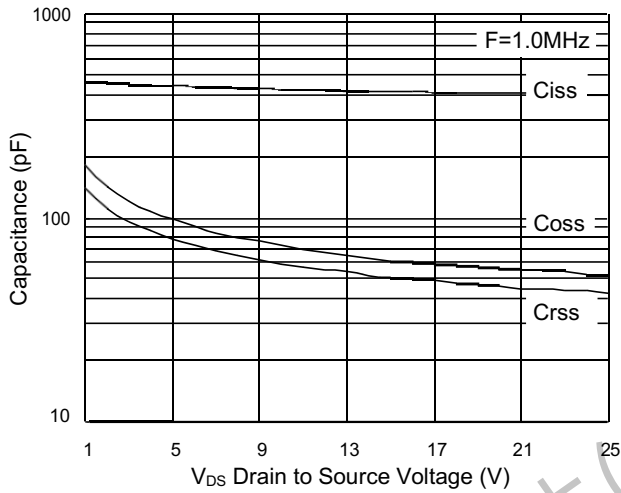


Fig.7 Capacitance

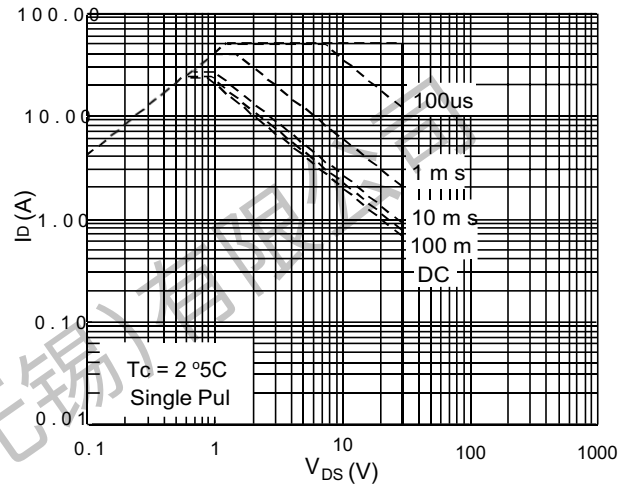


Fig.8 Safe Operating Area

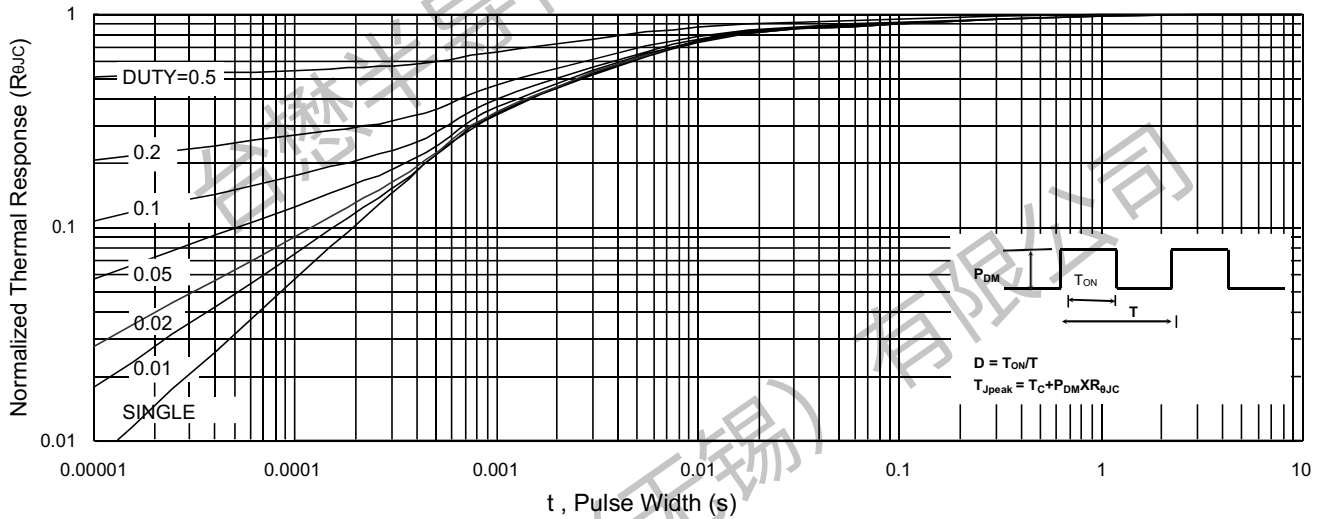
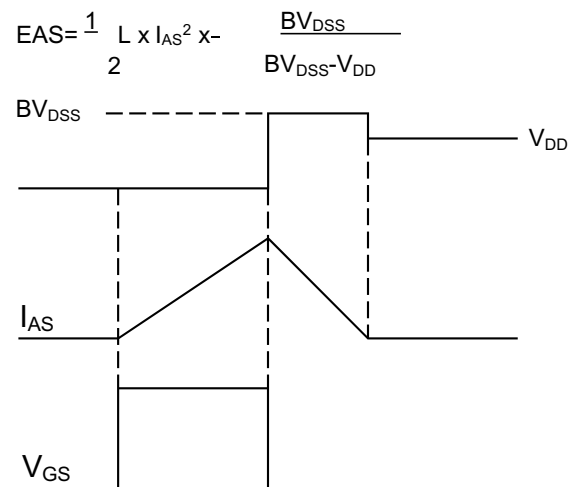
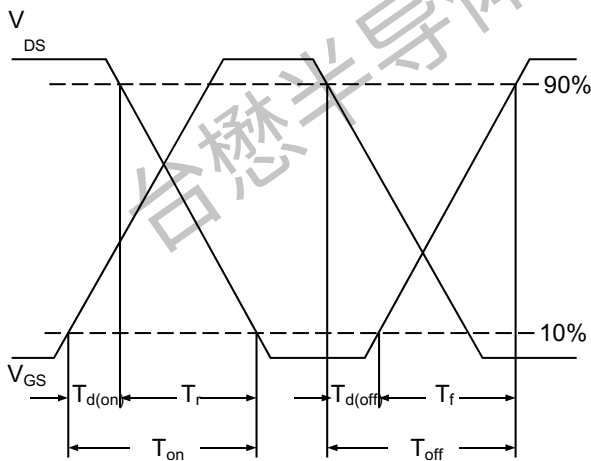


Fig.9 Normalized Maximum Transient Thermal Impedance



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P-Channel Typical Characteristics

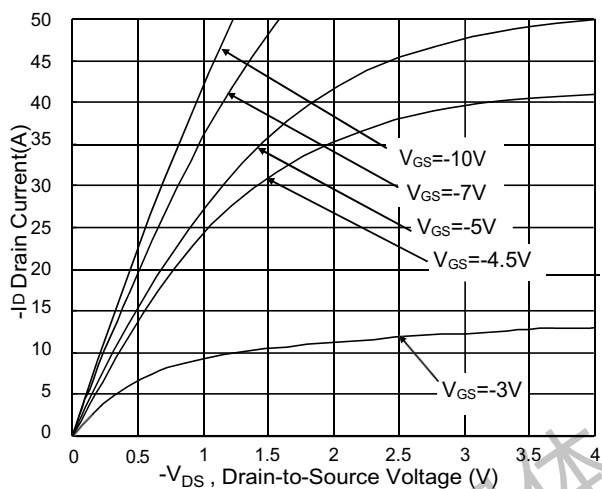


Fig.1 Typical Output Characteristics

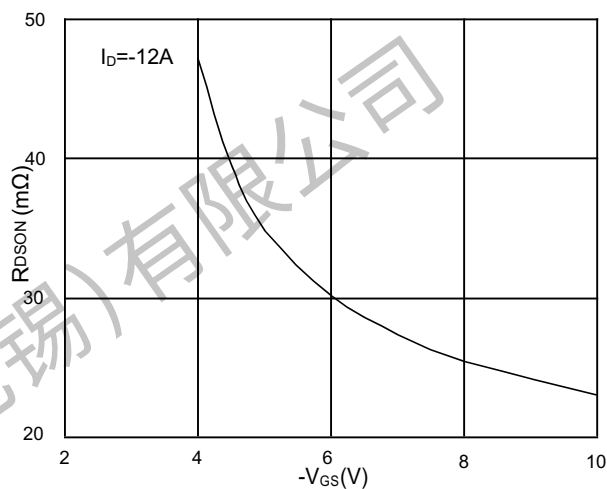


Fig.2 On-Resistance v.s Gate-Source

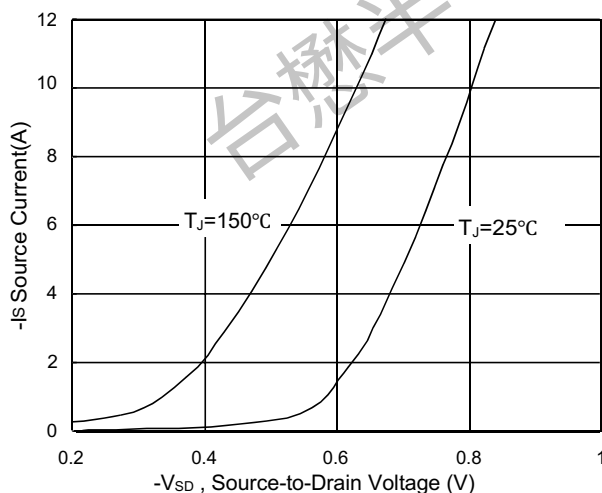


Fig.3 Forward Characteristics Of Reverse

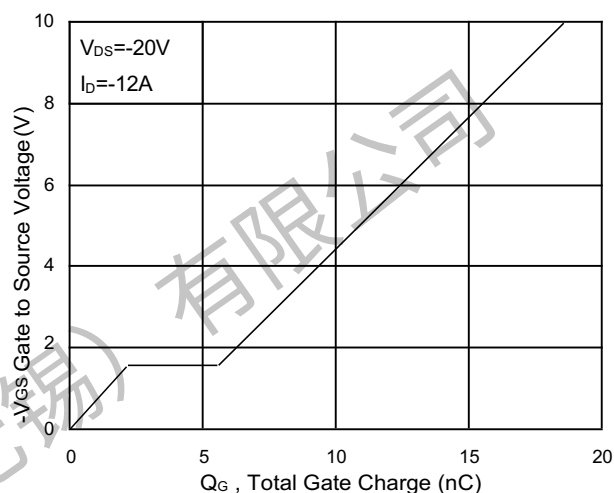


Fig.4 Gate-Charge Characteristics

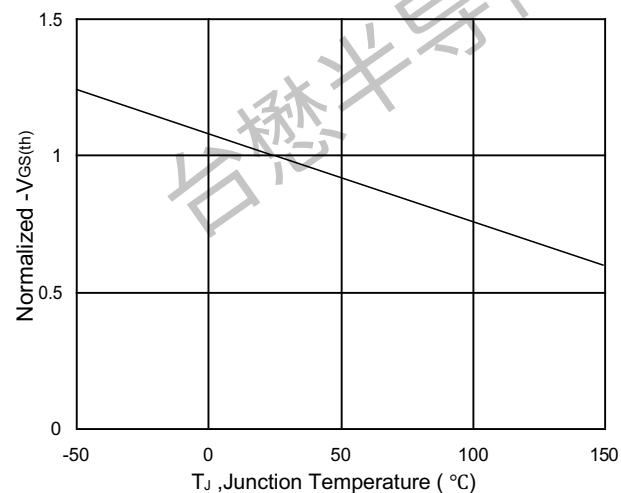


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

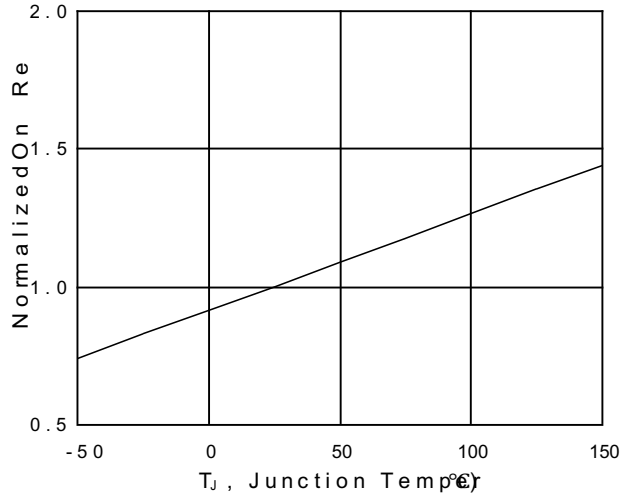
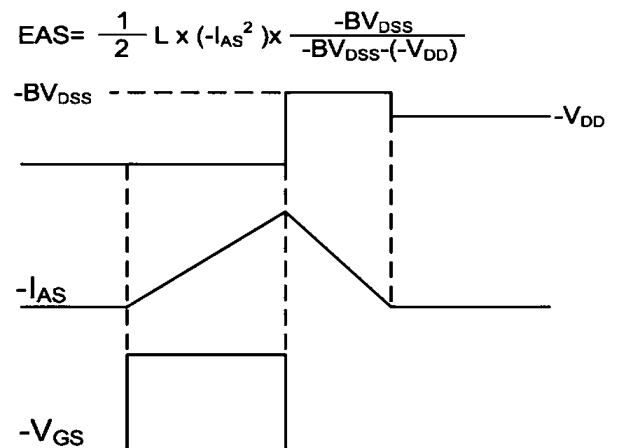
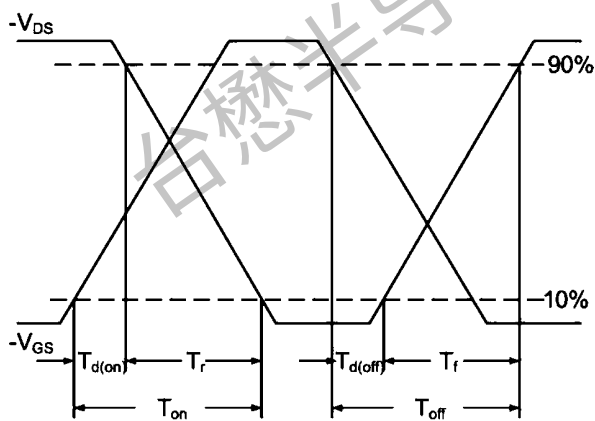
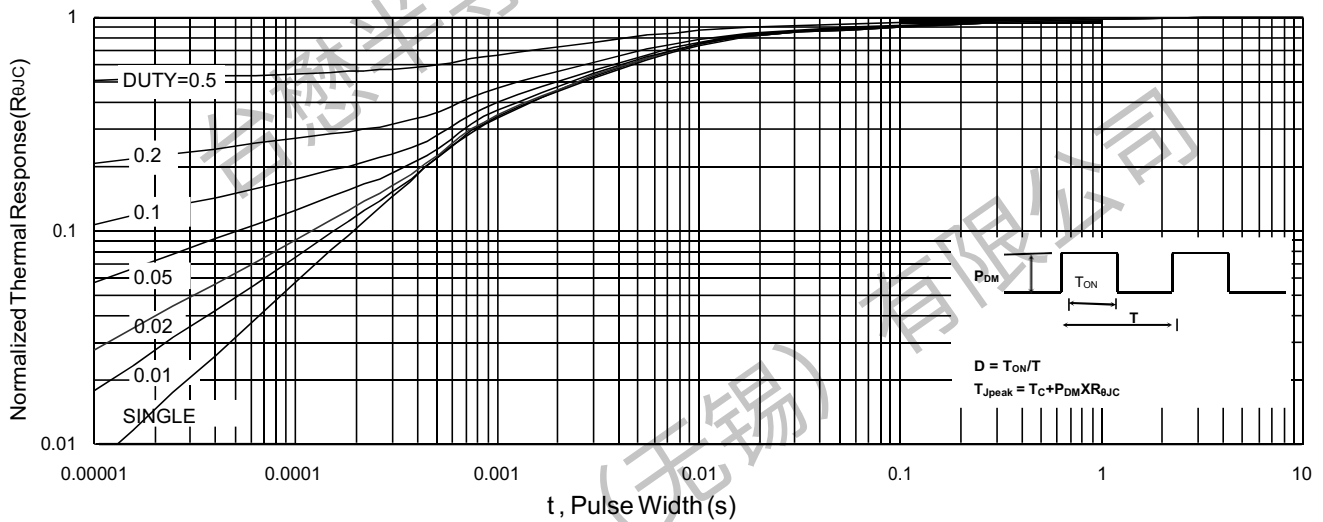
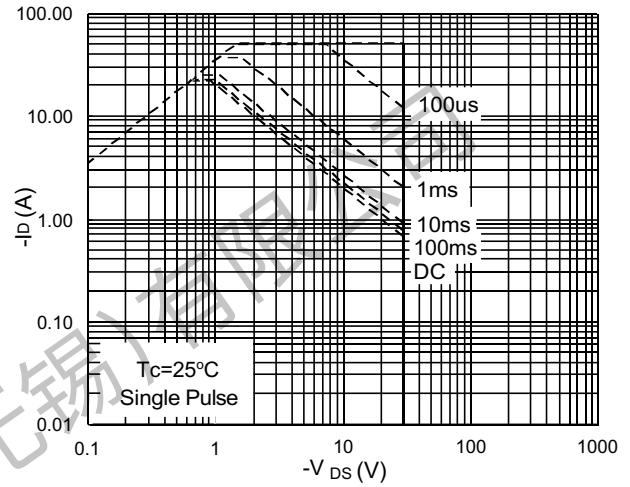
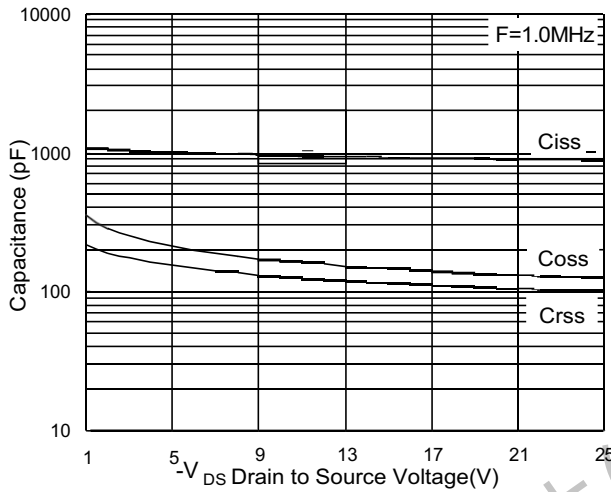


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

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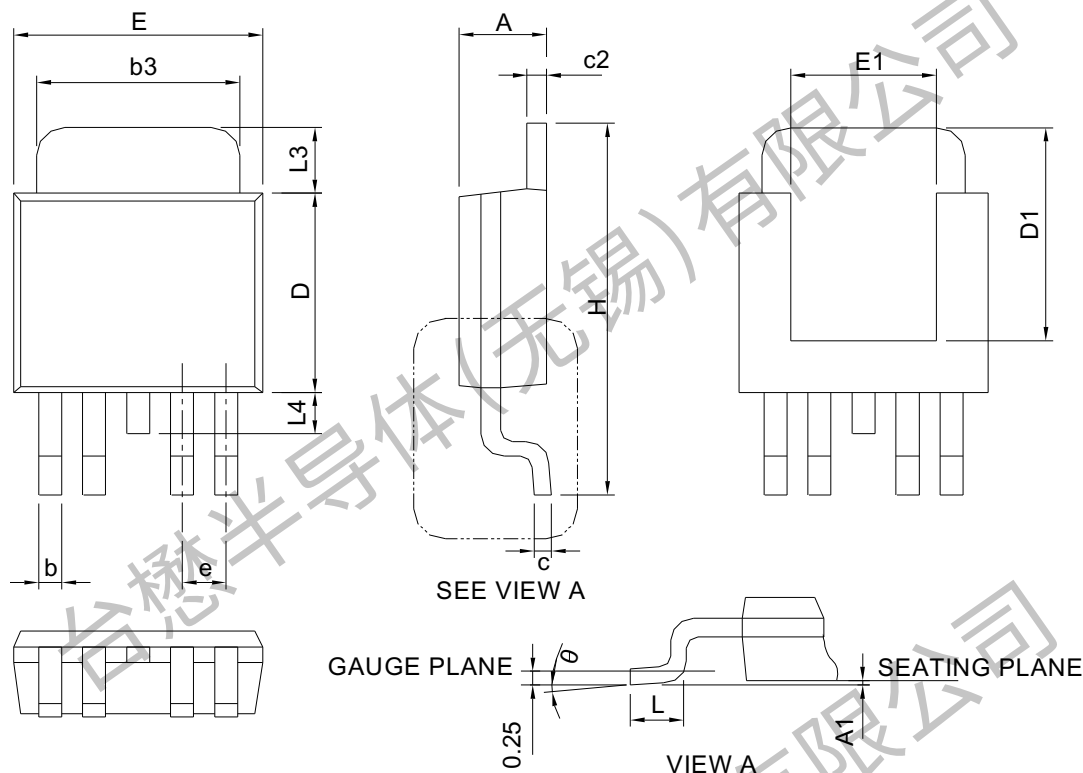
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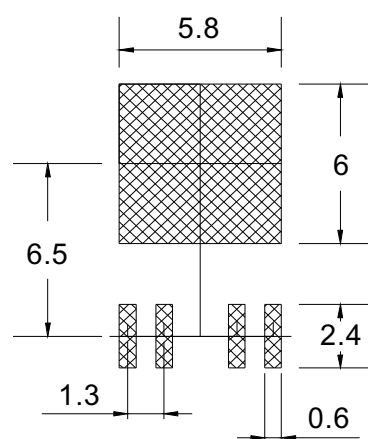
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Package Mechanical Data:TO-252-4L



SYMBOL	TO-252-4			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.2	-	0.008
b	0.50	0.71	0.020	0.028
b3	4.32	5.46	0.170	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	1.30 BSC		0.051 BSC	
H	9.40	10.41	0.370	0.410
L	1.40	1.78	0.055	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

RECOMMENDED LAND PATTERN

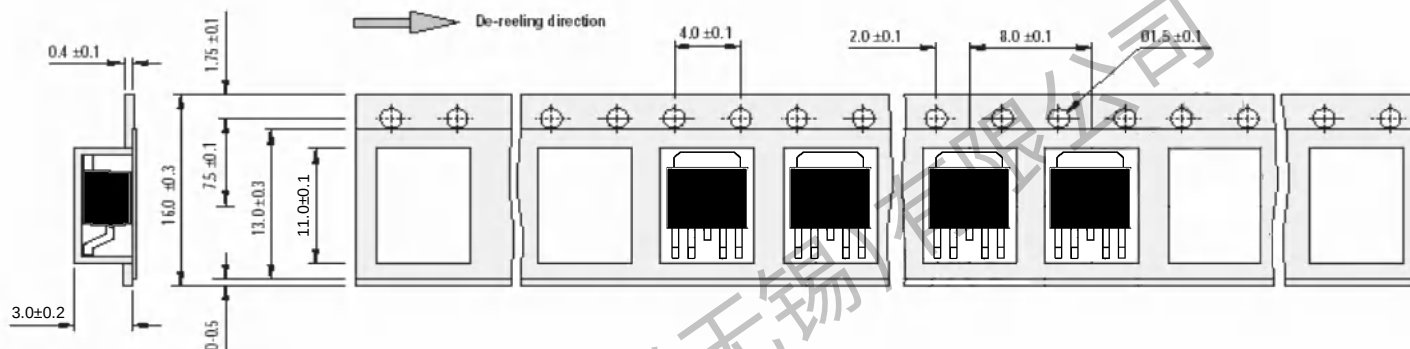


UNIT: mm

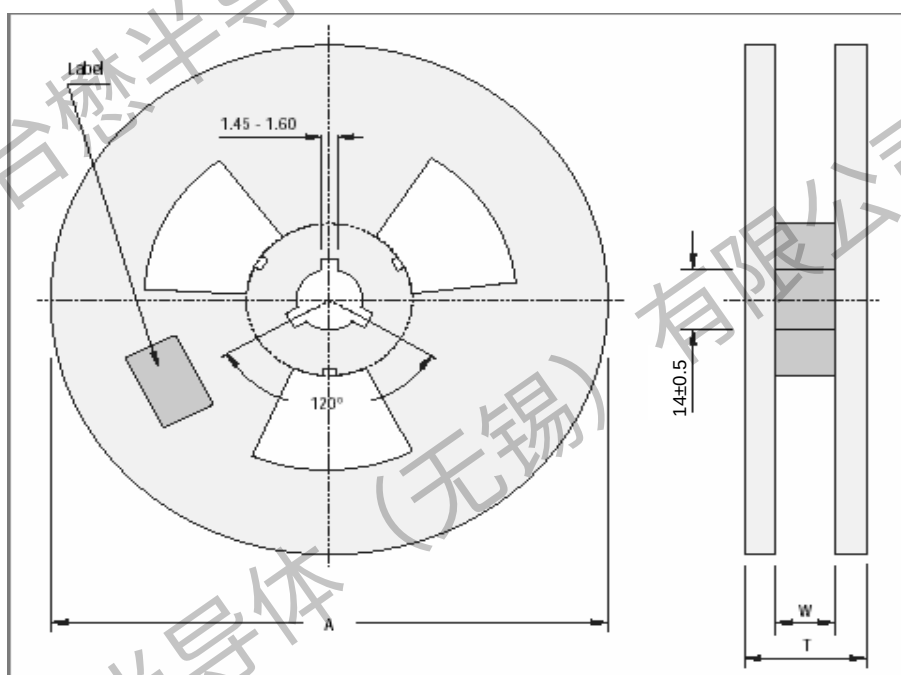
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N+P-Channel Enhancement Mode Mosfet

TO-252-4L Embossed Carrier Tape



TO-252-4L Reel



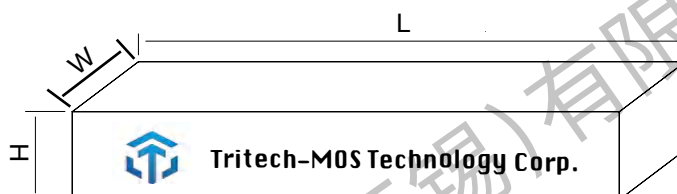
1.TO-252-4L Packaging

Package	Packing Form	Quantity		
		Reel	Inner Box	Outbox
TO-252-4L	Reel	2500	5	1

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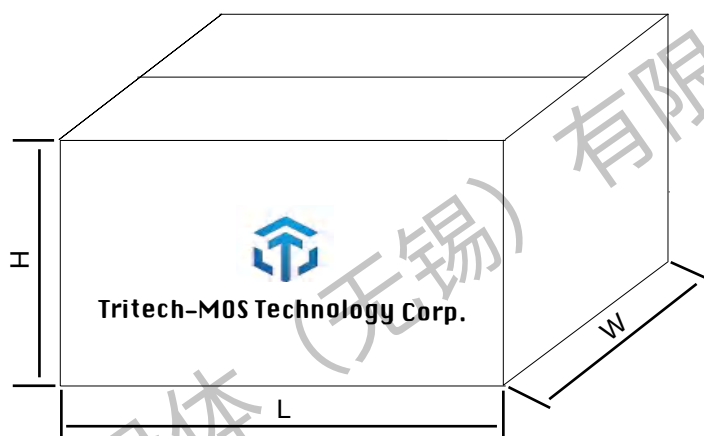
Inner Box



Dimension : 370 (L)×355(W) ×50(H) mm

Quantity : 2500 × 2Ea = 5000pcs

Outer Box



Dimension : 380(L)×380(W) ×275(H) mm

Quantity : 5000 × 5Ea = 25000pcs

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Revision history:

Date	Rev	Description	Page
2023.06.11	23.06	Original	