# **JLHF150W120R34E6DN**

#### L34 module with GEN6 IGBT and emitter controlled diode

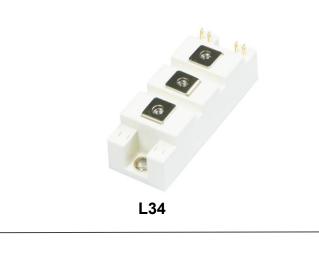
#### **Features**

- · Electrical features
  - V<sub>CES</sub> = 1200 V
  - $IC_{nom} = 150 A / I_{CRM} = 300 A$
  - V<sub>CEsat</sub> with positive temperature coefficient
- Mechanical features
  - Standard housing
  - 2.5 kV AC 1 min insulation
  - High creepage and clearance distances
  - Isolated base plate

#### **Typical Applications**

- High frequency switching application
- · Medical applications
- Motion/servo control
- UPS (Uninterruptible Power Supplies)
- Welding

#### **Description**





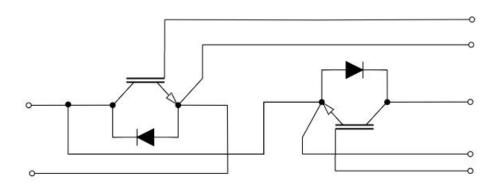
JINLAN = Company Name

JLHF150W120R34E6DN = Specific Device Code

YYWW = Year and Work Week Code

XXXXX = Serial Number

QR code = Custom Assembly Information



# Package Insulation coordination

Symbol	Parameter	Parameter Note or test condition		Parameter Note or test condition		Unit
Visol	Isolation test voltage	RMS,f=50Hz,t=60s	2.5	kV		
d <sub>creep</sub>	Creepage distance	terminal to heatsink	17.0	mm		
d <sub>creep</sub>	Creepage distance	terminal to terminal	20.0	mm		
d <sub>clear</sub>	Clearance	terminal to heatsink	17.0	mm		
d <sub>clear</sub>	Clearance	terminal to terminal	9.5	mm		
	Comparative tracking index					
CTI	(electrical)		≥175			

# Package Characteristic values

Symbol			Values					
Symbol	Description	Note or test condition		Min.	Тур.	Max.	Unit	
L <sub>sCE</sub>	Stray Inductance				30		nH	
Rcc'+EE'	Module Lead Resistance, Terminal to Chip	T <sub>C</sub> =25°C, per switch			0.75		mΩ	
T <sub>stg</sub>	Storage temperature			-40		125	°C	
М	Mounting torque for module mounting	-Mounting according to valid application note	M5, Screw	2.5		5.0	Nm	
М	Terminal connection torque	-Mounting according to valid  M6, Screw application note		3.0		5.0	Nm	
G	Weight				155		g	

# **IGBT**

## **Absolute Maximum Ratings** (Tc = 25°C unless otherwise noted)

Symbol	Description	Note or test condition	Value	Unit
V <sub>CES</sub>	Collector-Emitter Voltage	T <sub>vj</sub> = 25 °C	1200	V
I <sub>CDC</sub>	Continuous Collector	150	А	
I <sub>CRM</sub>	Repetitive peak collector current	Peak Collector Current@ tp=1ms	300	А
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> = 25°C, T <sub>vj max</sub> = 150°C	568	w
V <sub>GES</sub>	Gate-emitter peak voltage		±30	V

## Characteristics (Tc = 25°C unless otherwise noted)

Cumb -1	Davourates	Test Condition			Rating		
Symbol	Parameter			Min	Тур	Max	Unit
.,	Collector-Emitter Saturation Voltage	I <sub>C</sub> =150A	Tj=25°C		2.0	2.8	V
V <sub>CE(sat)</sub>	Concessor Emiliar Catalation Voltage	V <sub>GE</sub> =15V	Tj=150°C		2.4		v
$V_{\text{GE}(\text{TH})}$	Gate-Emitter Threshold Voltage	$I_C=3mA,V_{CE}=V_G$	<sub>E,</sub> T <sub>vj</sub> = 25°C	4.5		6.5	V
Ices	Collector-Emitter Cutoff Current	V <sub>GE</sub> =0V,V <sub>CE</sub> =120	0V,T <sub>vj</sub> = 25°C			1	mA
I <sub>GES</sub>	Gate-Emitter Leakage Current	$V_{GE} = \pm 20V$ , $V_{CE} = 0$	0 V, T <sub>vj</sub> = 25°C			100	nA
R <sub>Gint</sub>	Internal Gate Resistance	T <sub>vj</sub> = 25 °C			0.8		Ω
C <sub>ies</sub>	Input Capacitance	$V_{CE}$ =30V, $V_{GE}$ =0V, f=1MHz, $T_{vj}$ = 25°C			28.2		
Coes	Output Capacitance				0.63		nF
Cres	Reverse Transfer Capacitance				0.62		
Qg	Total Gate Charge	V <sub>CC</sub> =960V, I <sub>C</sub> =150A, V <sub>GE</sub> =15V,T <sub>vj</sub> = 25°C			0.89		
$Q_{ge}$	Gate to Emitter Charge				0.16		uC
Q <sub>gc</sub>	Gate to Collector Charge	, ,			0.42		
t <sub>d(ON)</sub>	Turn-on Delay Time				19		
t <sub>r</sub>	Rise Time				25		,,,
$t_{\text{d(OFF)}}$	Turn-Off Delay Time	$V_{CE}$ =600V, $I_{C}$ =150A, $V_{GE}$ =15/-5V, $R_{g}$ =7.8 $\Omega$ Inductive Load			313		ns
t <sub>f</sub>	Fall Time				15		
Eon	Turn-On Switching Loss				19.2		
E <sub>off</sub>	Turn-Off Switching Loss				6.9		mJ
Ets	Total Switching Loss				25.1		

R <sub>thJC</sub>	Thermal resistance,junction to case	per IGBT		0.2		K/W
T <sub>vj op</sub>	Temperature under switching conditions		-40		150 <sup>1)</sup>	$^{\circ}$

 $<sup>^{1)}</sup>T_{vj \, op} > 150\,^{\circ}\text{C}$  is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

# Diode

## **Absolute Maximum Ratings** (Tc = 25°C unless otherwise noted)

Symbol	Description	Note or test condition	Value	Unit
$V_{RRM}$	Repetitive peak reverse voltage	T <sub>vj</sub> = 25 °C	1200	V
IF	Continuous DC forward current		150	А
I <sub>FRM</sub>	Repetitive peak forward current	t <sub>P</sub> = 1 ms	300	Α

#### Characteristics (Tc = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Rating			Units
Symbol		rest conditions	Min.	Тур.	Max.	Offics
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> =150A		3.3	4.2	V
Тп	Reverse Recovery Time			260		ns
I <sub>RRM</sub>	Diode Peak Reverse Recovery Current	$V_{CE}$ =600V, $I_{F}$ =150A, $V_{GE}$ =15/-5V $R_{g}$ =7.8 $\Omega$		35		А
Q <sub>rr</sub>	Reverse Recovery Charge			2.8		uC
E <sub>rec</sub>	Reverse Recovery Energy			2.3		mJ
R <sub>thJC</sub>	Thermal resistance, junction to case	per Diode		0.45		K/W
T <sub>vj op</sub>	Temperature under switching conditions		-40		150 <sup>2)</sup>	${\mathbb C}$

 $<sup>^{2)}</sup>T_{v_{j}op} > 150^{\circ}C$  is only allowed for operation at overload conditions. For detailed specifications please refer to AN 2018-14.

## **Typical Electrical and Thermal Characteristics**

**Figure 1 Output Characteristics** 

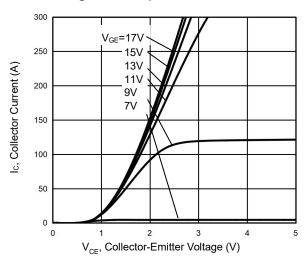
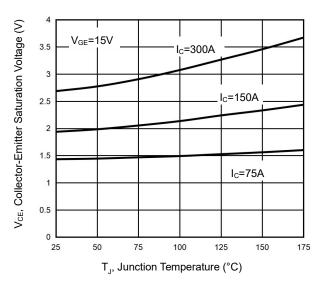
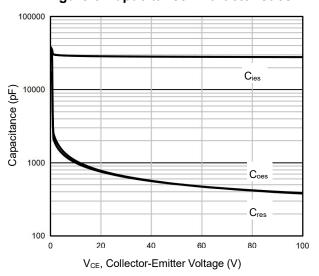


Figure 3  $V_{\text{CE(sat)}}$  vs. Case Temperature



**Figure 5 Capacitance Characteristics** 



**Figure 2 Transfer Characteristics** 

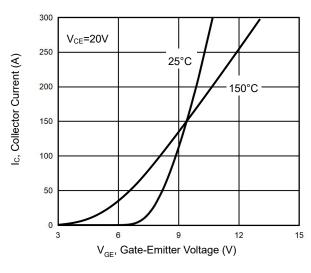


Figure 4 Saturation Voltage vs. V<sub>GE</sub>

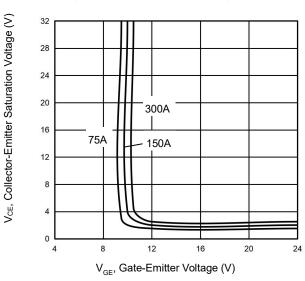
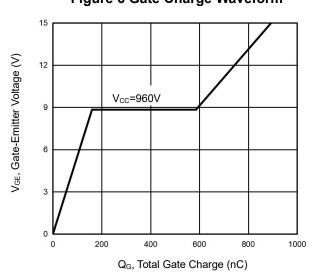


Figure 6 Gate Charge Waveform





## **Typical Electrical and Thermal Characteristics**

**Figure 7 Forward Characteristics** 

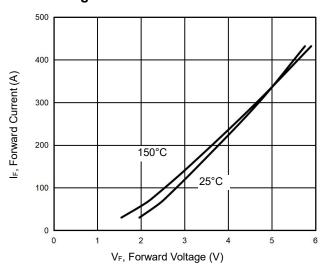


Figure 8 V<sub>F</sub> vs. Temperature

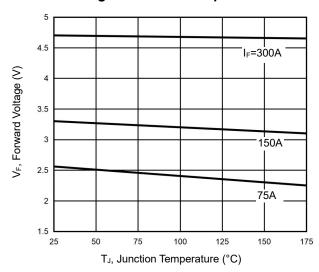


Figure 9 Gate-emitter Threshold Voltage as a Function of Junction Temperature

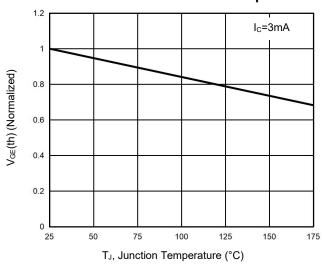
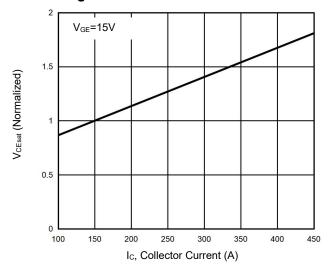
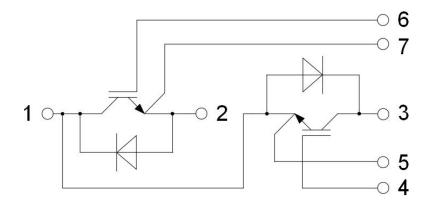


Figure 10 Typical Collector-emitter Saturation Voltage as a function of Collector Current



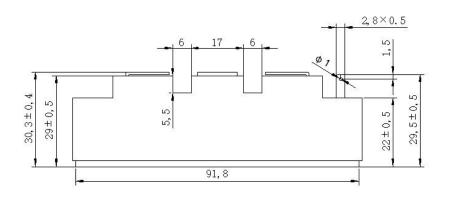


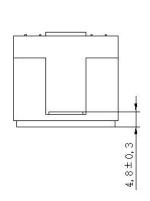
## **Circuit Diagram**

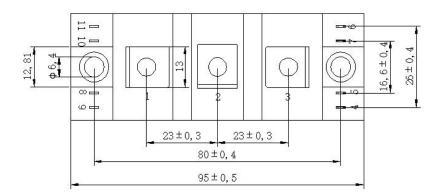


## **Package Dimensions**

#### **Dimensions in Millimeters**









## **REVISION HISTORY**

Document version	Date of release	Description of changes
Rev.00	2024-08-06	Preview

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