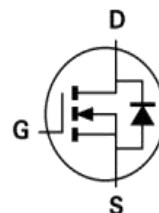


MAIN CHARACTERISTICS

I_D	60A
V_{DSS}	30V
$R_{DS(on)-typ}$ (@ $V_{GS}=10V$)	6.8m Ω



FEATURES

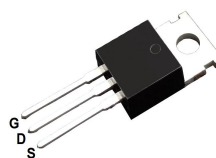
- Adopt advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

APPLICATIONS

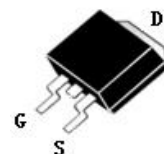
- Battery protection
- Load switch
- Uninterruptible power supply

MECHANICAL DATA

- Case: Molded plastic
- Mounting Position: Any
- Molded Plastic: UL Flammability Classification Rating 94V-0
- Lead free in compliance with EU RoHS 2011/65/EU directive
- Solder bath temperature 275°C maximum, 10s per JESD 22-B106



TO-220C



TO-263C

Product specification classification

Part Number	Package	Mode Name	Pack
LT60N03AP	TO-220C	LT60N03AP	Tape
LT60N03AT	TO-263C	LT60N03AT	Tape

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	±20	V
Continue Drain Current	I_D	60	A
Pulsed Drain Current (Note1)	I_{DM}	110	A
Power Dissipation	P_D	34.7	W
Single Pulse Avalanche Energy (Note1)	E_{AS}	57	mJ
Operating Temperature Range	T_J	150	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	3.6	°C/W
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62	°C/W

Note1:Pulse test: 300 μs pulse width, 2 % duty cycle

Electrical Characteristics at Tc=25°C unless otherwise specified

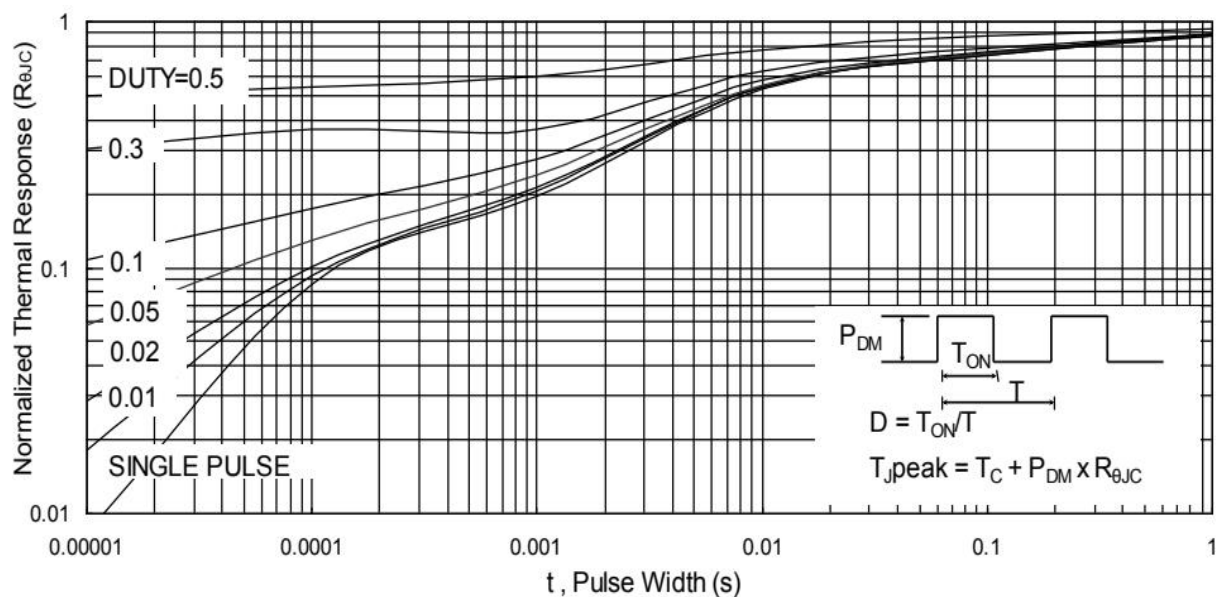
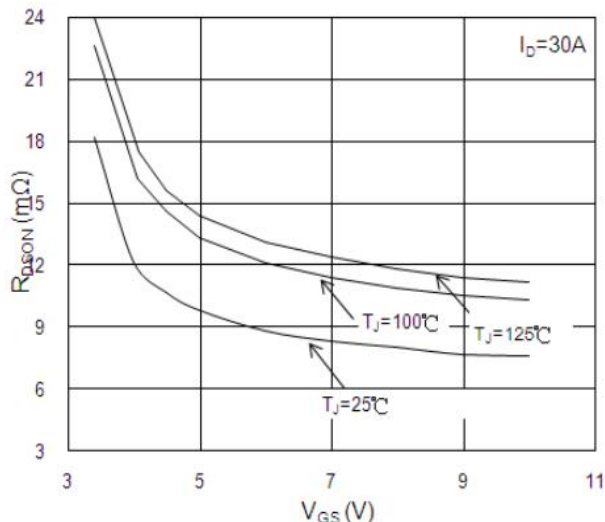
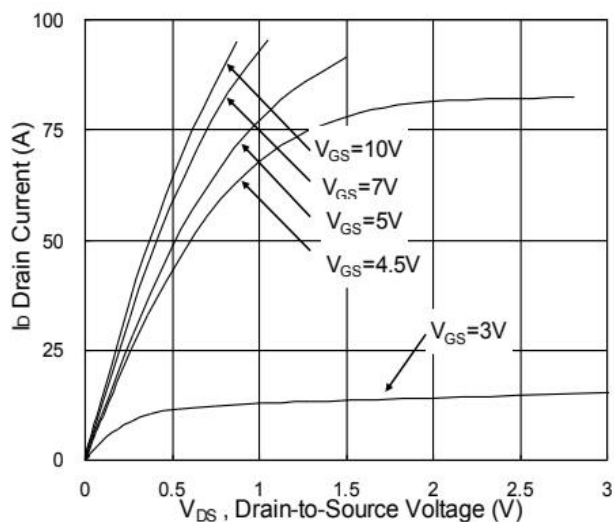
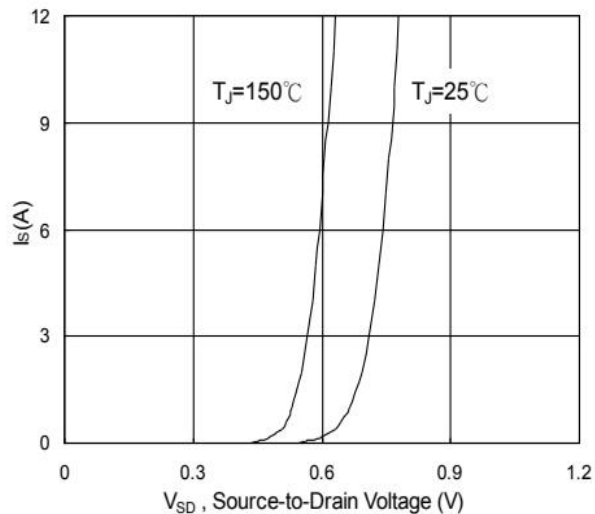
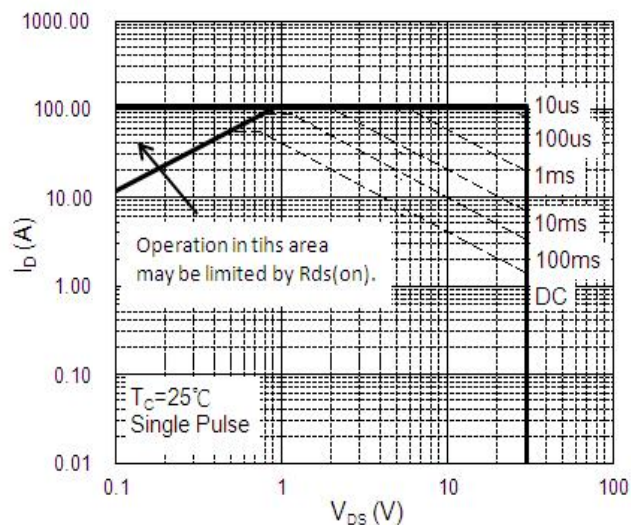
Characteristics	Test Condition	Symbol	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	BV_{DSS}	30	-	-	V
Drain-Source Leakage Current	$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$	I_{DSS}	-	-	1	μA
Gate Leakage Current	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	I_{GSS}	-	-	±100	nA
Gate-Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	$V_{GS(th)}$	1	-	2.5	V
Drain-Source On-State Resistance	$V_{GS} = 10\text{ V}, I_D = 30\text{ A}$	$R_{DS(on)}$	-	6.8	8.5	mΩ
Forward Transconductance	$V_{DS} = 5\text{ V}, I_D = 30\text{ A}$	g_{fs}	-	36	-	S
Input Capacitance	$V_{DS}=15\text{ V}, V_{GS}=0\text{ V}, f=1\text{ MHz}$	C_{iss}	-	1314	-	pF
Output Capacitance		C_{oss}	-	162	-	pF
Reverse Transfer Capacitance		C_{rss}	-	128	-	pF
Turn-on Delay Time(Note2)	$V_{DD}=15\text{ V}, V_{GS}=10\text{ V}, R_G=3.3\text{ }\Omega, I_D=15\text{ A}$	$t_{d(ON)}$	-	4.5	-	ns
Rise Time(Note2)		t_r	-	11.8	-	ns
Turn-Off Delay Time(Note2)		$t_{d(OFF)}$	-	25	-	ns
Fall Time(Note2)		t_f	-	7	-	ns
Total Gate Charge(Note2)	$V_{DS}=15\text{ V}, V_{GS}=4.5\text{ V}, I_D=15\text{ A}$	Q_G	-	13	-	nC
Gate to Source Charge(Note2)		Q_{GS}	-	4	-	nC
Gate to Drain Charge(Note2)		Q_{GD}	-	5	-	nC

Source-Drain Diode Characteristics at Ta=25°C unless otherwise specified

Characteristics	Test Condition	Symbol	Min.	Typ.	Max.	Unit
Maximun Body-Diode Continuous Current		I_S	-	-	58	A
Maximun Body-Diode Pulsed Current(Note2)		I_{SM}	-	-	120	A
Drain-Source Diode Forward Voltage	$V_{GS}=0\text{ V}, I_S=1\text{ A}, T_J=25^\circ\text{C}$	V_{SD}	-	-	1.2	V

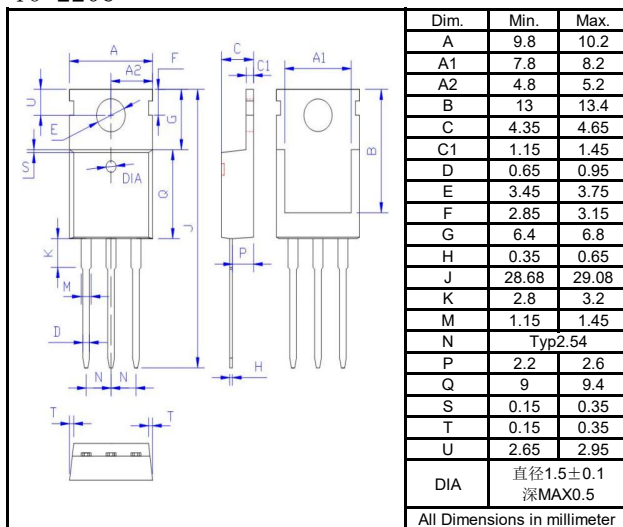
Note2:Pulse test: 300 μs pulse width, 2 % duty cycle

RATINGS AND CHARACTERISTIC CURVES

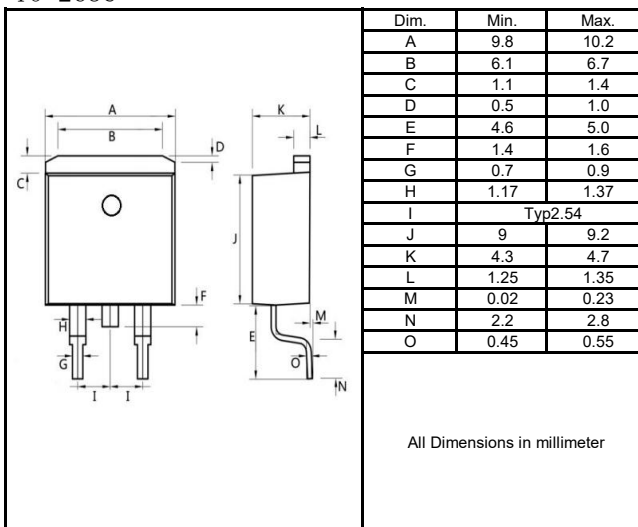


Package Outline Dimensions millimeters

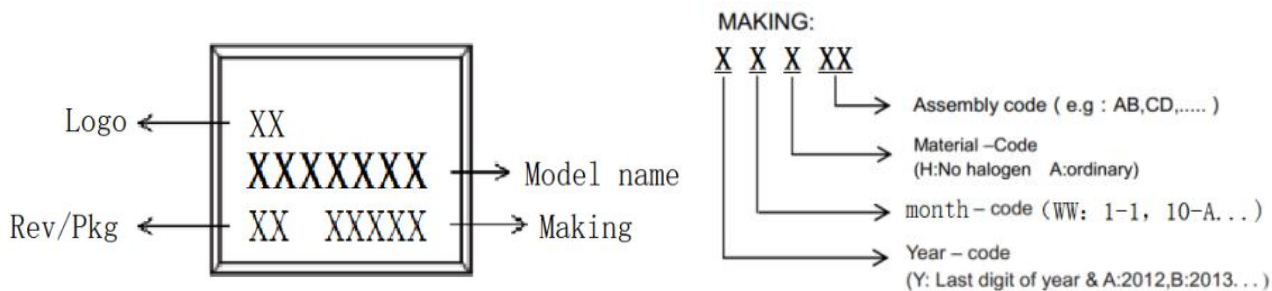
TO-220C



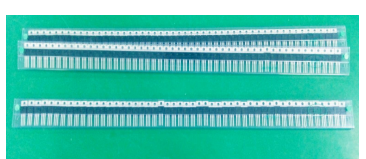
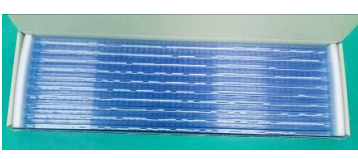
TO-263C



Marking on the body



packing instruction

PKG	最小包装	内盒	外箱
TO-220C TO-263C	 50PCS/管	 1000pcs/盒	 5000pcs/箱
TO-263C	 800pcs/盘	 1600pcs/盒	 8000pcs/箱

Notice

All product,product specifications and data are subject to change without notice to improve.The right to explain is owned by LINGXUN electronics company.

Confirm that operation temperature is within the specified range described in the product specification. Avoid applying poer exceeding normal rated poer; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.

LINGXUN electronics shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.