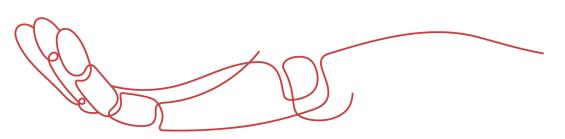




## **PRODUCT DATA SHEET**



To learn more about JGSEMI, please visit our website at







Datasheet

ources Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



#### **General Description**

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	19m $\Omega$	5.8A

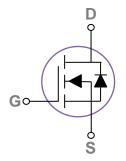
#### **Features**

- 30V, 5.8 A,  $RDS(ON) = 19m\Omega@VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for 2.5V Gate Drive Applications

#### **Applications**

- Notebook
- Load Switch
- LED applications





#### **Absolute Maximum Ratings** Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	30	V
V <sub>G</sub> s	Gate-Source Voltage	±12	V
1_	Drain Current – Continuous (T <sub>A</sub> =25°C)	5.8	А
ID	Drain Current – Continuous (T <sub>A</sub> =70°C)	4.2	А
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	21.2	А
D-	Power Dissipation (T <sub>A</sub> =25°C)	1.56	W
P <sub>D</sub>	Power Dissipation – Derate above 25°C	0.012	W/°C
Т <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 125	°C

#### **Thermal Characteristics**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient		80	°C/W



## **Electrical Characteristics** (T<sub>J</sub>=25 °C, unless otherwise noted)

#### **Off Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA				V
△BV <sub>DSS</sub> /△T <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.06		V/°C
IDSS	Drain Source Leakage Current	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C			10	uA
Igss	Gate-Source Leakage Current	V <sub>GS</sub> =±12V , V <sub>DS</sub> =0V			±100	nA

#### **On Characteristics**

R <sub>DS(ON)</sub> :		V <sub>GS</sub> =10V , I <sub>D</sub> =5A		19	30	
	·Static Drain-Source On-Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =4A		21	35	mΩ
		Vgs=2.5V , ID=3A		35	55	
$V_{GS(th)}$	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	0.5	0.9	1.2	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS=VDS , ID =250UA		-3		mV/°C
gfs	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>S</sub> =3A		7		S

## **Dynamic and switching Characteristics**

$Q_g$	Total Gate Charge <sup>2,3</sup>		 8.4	
Qgs	Gate-Source Charge <sup>2,3</sup>	$V_{DS}$ =10V , $V_{GS}$ =4.5V , $I_{D}$ =4A	 1	 nC
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		 2.2	
$T_{d(on)}$	Turn-On Delay Time <sup>2,3</sup>		 4.5	
Tr	Rise Time <sup>2, 3</sup>	$V_{DD}$ =10V , $V_{GS}$ =4.5V , $R_{G}$ =25 $\Omega$	 13	 nS
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>	I <sub>D</sub> =1A	 27	 113
Tf	Fall Time <sup>2, 3</sup>		 8.3	
Ciss	Input Capacitance		 695	
Coss	Output Capacitance	V <sub>DS</sub> =10V , V <sub>GS</sub> =0V , F=1MHz	 45	 pF
Crss	Reverse Transfer Capacitance		 36	

### **Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V . Force Current			5.8	Α
Isм	Pulsed Source Current	VG=VD=UV, FOICE Current			21.2	Α
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1.2	V



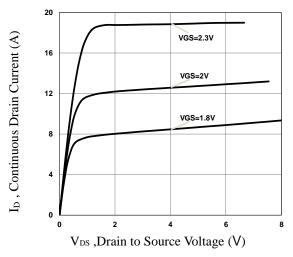


Fig.1 Typical Output Characteristics

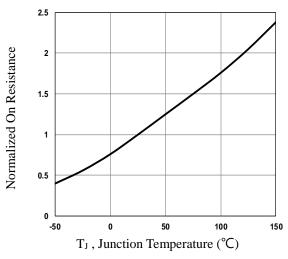


Fig.3 Normalized RDSON vs. T<sub>J</sub>

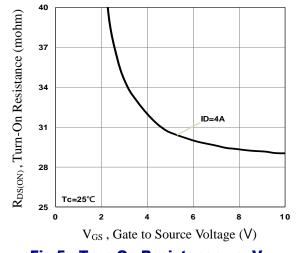


Fig.5 Turn-On Resistance vs. V<sub>GS</sub>

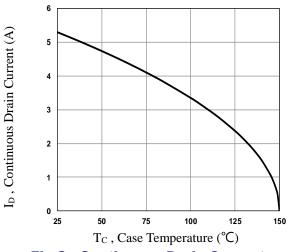


Fig.2 Continuous Drain Current vs. Tc

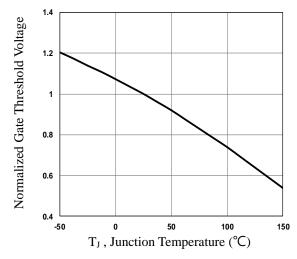


Fig.4 Normalized V<sub>th</sub> vs. T<sub>J</sub>

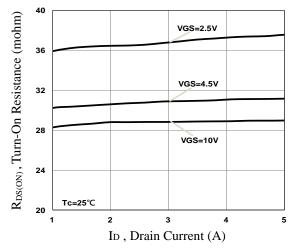
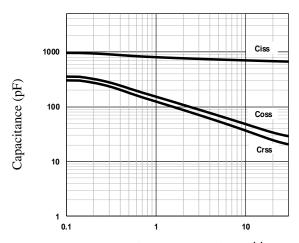


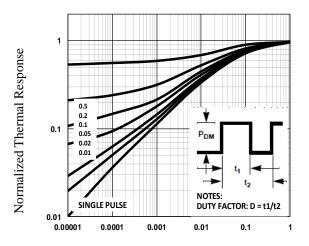
Fig.6 Turn-On Resistance vs. ID





 $V_{DS}$  , Drain to Source Voltage (V)

Fig.7 Capacitance Characteristics



Square Wave Pulse Duration (s)

Fig.9 Normalized Transient Impedance

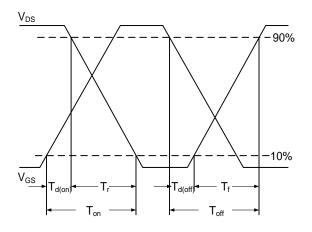


Fig.11 Switching Time Waveform

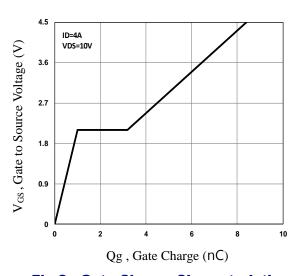


Fig.8 Gate Charge Characteristics

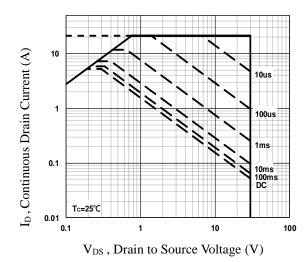


Fig.10 Maximum Safe Operation Area

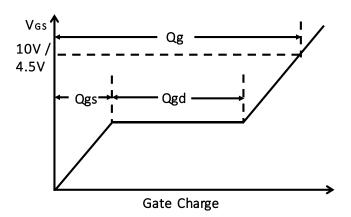
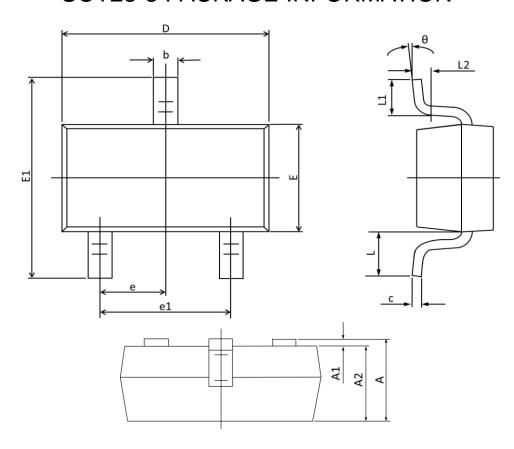


Fig.12 Gate Charge Waveform



# **SOT23-3 PACKAGE INFORMATION**



Symbol	Dimensions	In Millimeters	Dimensio	ns In Inches
Symbol	MAX	MIN	MAX	MIN
Α	1.450		0.057	
<b>A</b> 1	0.100	0.000	0.004	0.000
A2	1.300	0.900	0.051	0.035
b	0.500	0.300	0.020	0.012
С	0.150	0.080	0.006	0.003
D	3.050	2.850	0.120	0.112
E	1.750	1.550	0.069	0.061
E1	3.000	2.600	0.118	0.102
е	0.95 TYP.		0.03	37 TYP.
e1	2.000	1.800	0.079	0.071
L	0.59	REF.	0.02	22 REF.
L1	0.600	0.350	0.024	0.014
L2	0.29	5 TYP.	0.01 TYP.	
θ	12°	0°	12°	0°



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