

TSM70N10CPROG-VB Datasheet

N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A)
100	0.0085 at $V_{GS} = 10$ V	85
	0.0105 at $V_{GS} = 4.5$ V	70

FEATURES

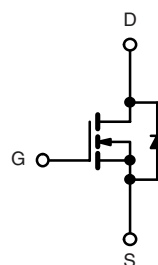
- Trench Power MOSFET
- 100 % R_g Tested
- 100 % UIS Tested


RoHS
 COMPLIANT

APPLICATIONS

- Primary Side Switch
- Isolated DC/DC Converter

TO-252



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless otherwise noted			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	I_D	85	A
		75 ^a	
Pulsed Drain Current	I_{DM}	300	
Avalanche Current	I_{AS}	75	
Single Pulse Avalanche Energy ^b	E_{AS}	280	mJ
Maximum Power Dissipation ^b	P_D	176	W
		3.75	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	R_{thJA}	40	50	°C/W	Steady State
Maximum Junction-to-Case	R_{thJC}	0.85	1.1		

Notes:

a. Package limited.

b. Surface mounted on 1" x 1" FR4 board.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1	μA
		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 175°C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = ≥ 5 V, V _{GS} = 10 V	50			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 20 A		0.0085		Ω
		V _{GS} = 4.5 V, I _D = 20 A		0.0105		
		V _{GS} = 10 V, I _D =20 A, T _J = 125 °C		0.017		
		V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C		0.022		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 20A	25			S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		4000		pF
Output Capacitance	C _{oss}			565		
Reverse Transfer Capacitance	C _{rss}			205		
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 45 A		105	160	nC
Gate-Source Charge ^c	Q _{gs}			17		
Gate-Drain Charge ^c	Q _{gd}			23		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 20 V, R _L = 0.6 Ω I _D ≅ 45 A, V _{GEN} = 10 V, R _g = 2.5 Ω		12	25	ns
Rise Time ^c	t _r			90	135	
Turn-Off DelayTime ^c	t _{d(off)}			55	85	
Fall Time ^c	t _f			130	195	
Source-Drain Diode Ratings and Characteristics T _C = 25 °C ^b						
Continuous Current	I _S				85	A
Pulsed Current	I _{SM}				140	
Forward Voltage ^a	V _{SD}	I _F = 45 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 50 A, dI/dt = 100 A/μs		85	140	ns
Peak Reverse Recovery Current	I _{RM(REC)}			4.5	7	A
Reverse Recovery Charge	Q _{rr}			0.17	0.35	μC

Notes:

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

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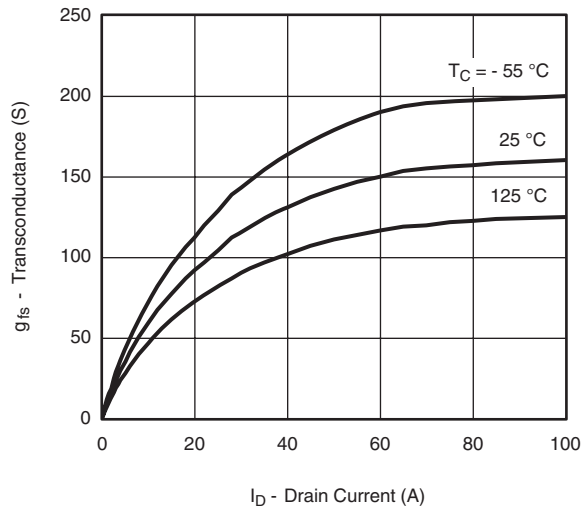
TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, unless otherwise noted



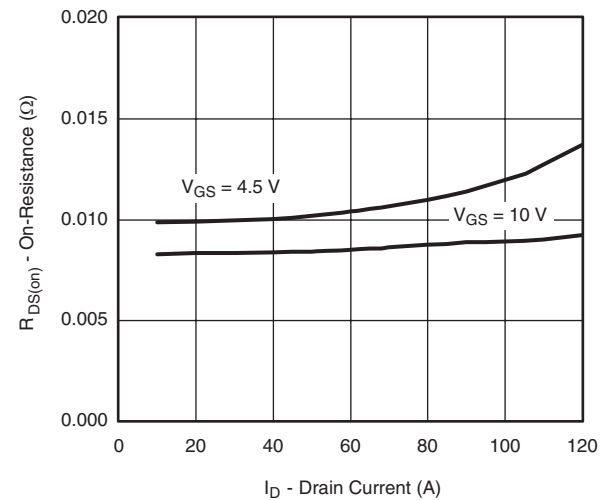
Output Characteristics



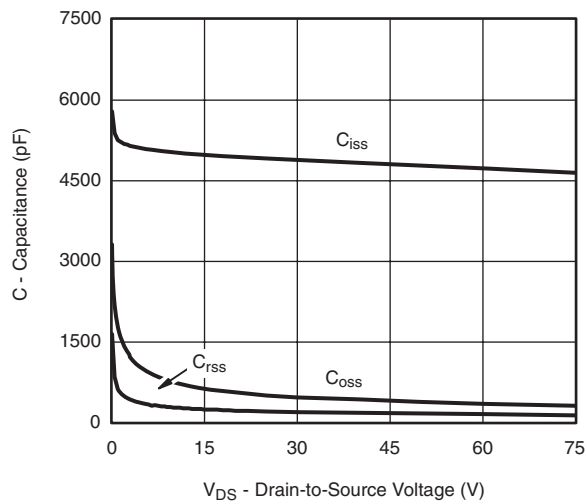
Transfer Characteristics



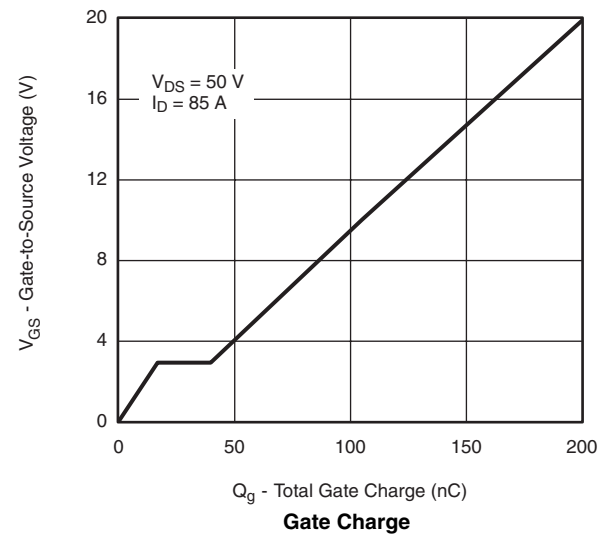
Transconductance



On-Resistance vs. Drain Current



Capacitance

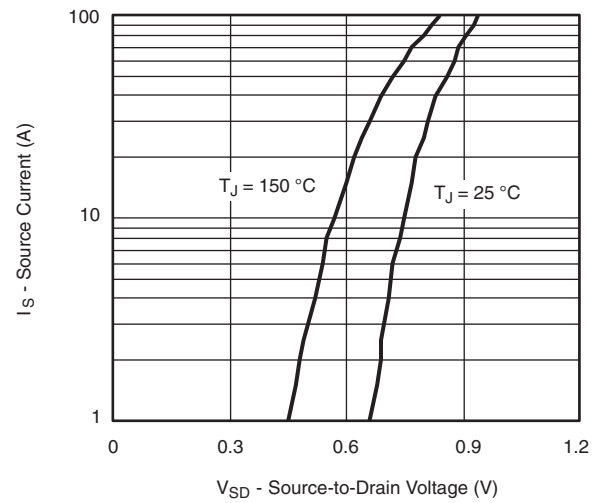


Gate Charge

TYPICAL CHARACTERISTICS $T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted



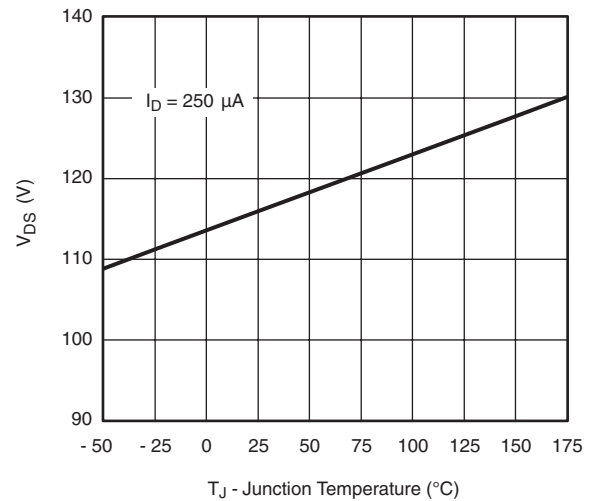
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

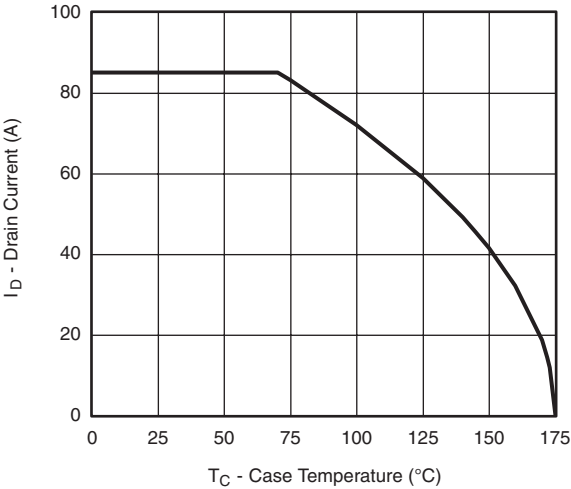


Avalanche Current vs. Time

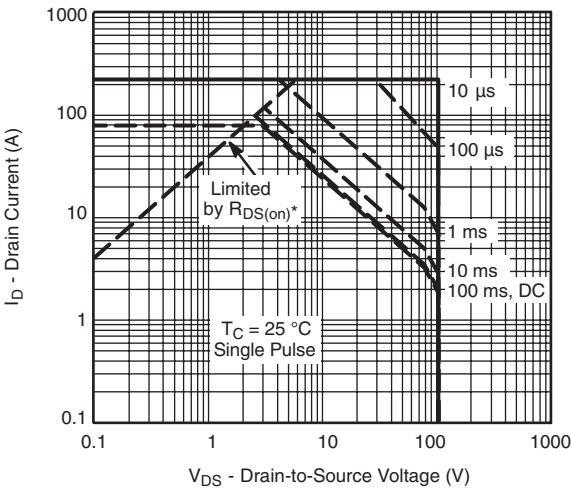


T_J - Drain-Source Breakdown vs. Junction-Temperature

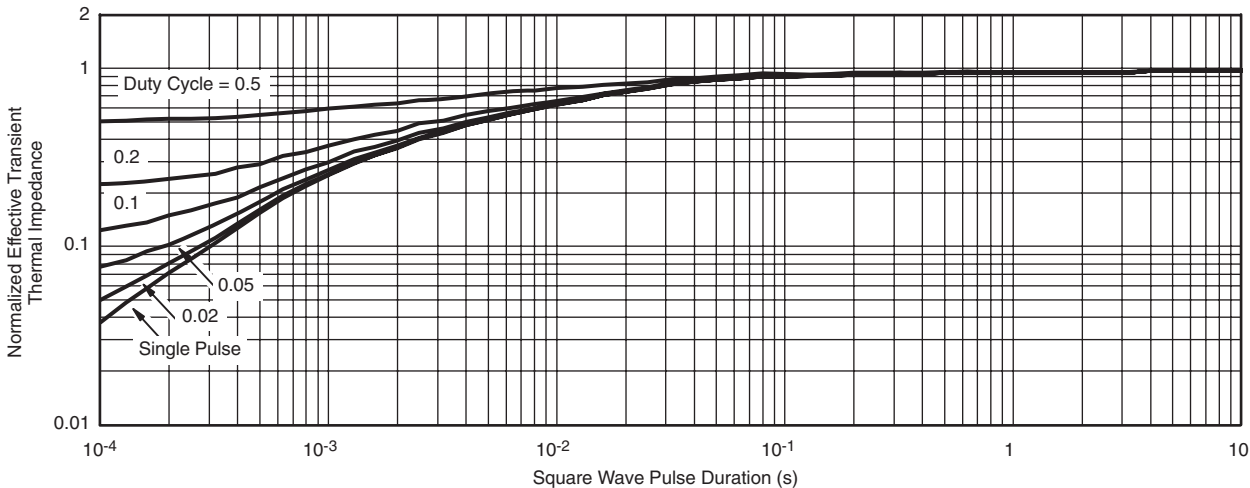
THERMAL RATINGS



**Maximum Avalanche and Drain Current
vs. Case Temperature**

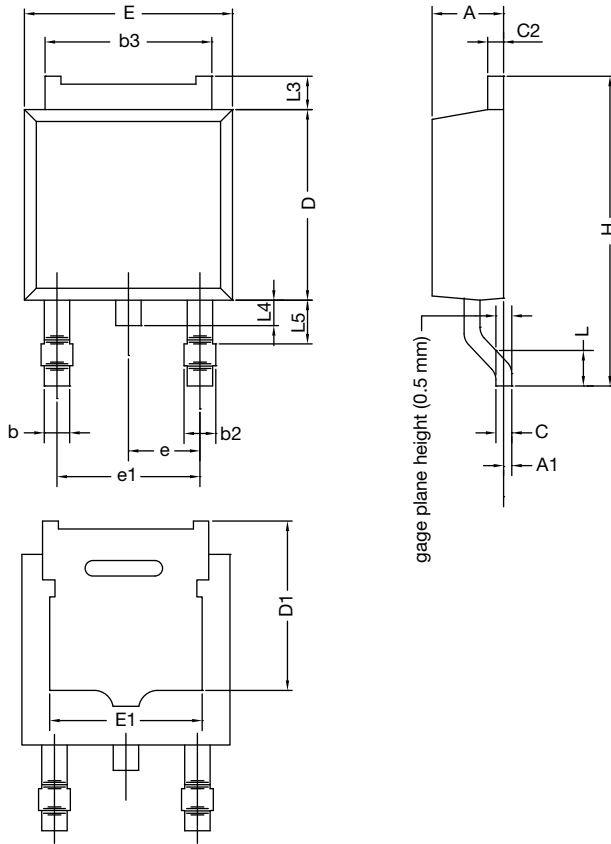


Safe Operating Area
* $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified



Normalized Thermal Transient Impedance, Junction-to-Case

TO-252AA CASE OUTLINE

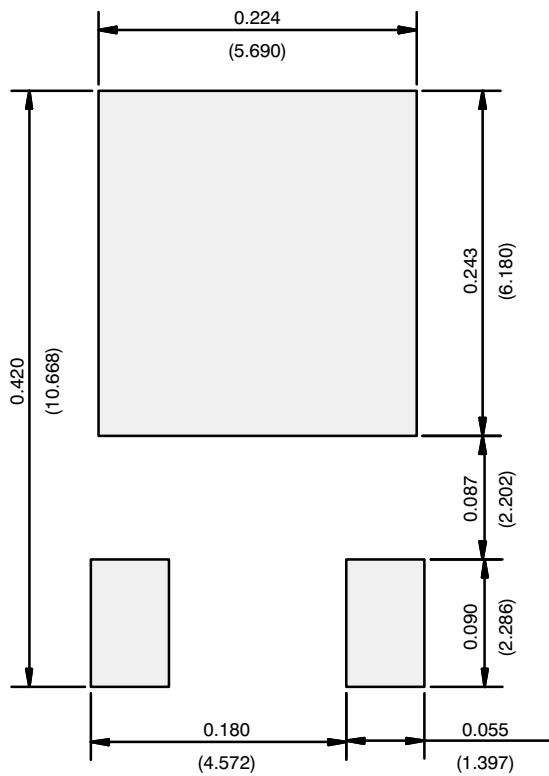


DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.38	0.086	0.094
A1	-	0.127	-	0.005
b	0.64	0.88	0.025	0.035
b2	0.76	1.14	0.030	0.045
b3	4.95	5.46	0.195	0.215
C	0.46	0.61	0.018	0.024
C2	0.46	0.89	0.018	0.035
D	5.97	6.22	0.235	0.245
D1	5.21	-	0.205	-
E	6.35	6.73	0.250	0.265
E1	4.32	-	0.170	-
H	9.40	10.41	0.370	0.410
e	2.28 BSC		0.090 BSC	
e1	4.56 BSC		0.180 BSC	
L	1.40	1.78	0.055	0.070
L3	0.89	1.27	0.035	0.050
L4	-	1.02	-	0.040
L5	1.14	1.52	0.045	0.060
ECN: X12-0247-Rev. M, 24-Dec-12				
DWG: 5347				

Note

- Dimension L3 is for reference only.

RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads
Dimensions in Inches/(mm)

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