

N-Channel Enhancement Mode Power MOSFET

<p>Description</p> <p>The G1003A uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.</p> <p>General Features</p> <ul style="list-style-type: none"> ● V_{DS} 100V ● I_D (at $V_{GS} = 10V$) 3A ● $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 120mΩ ● $R_{DS(ON)}$ (at $V_{GS} = 4.5V$) < 130mΩ ● 100% Avalanche Tested ● RoHS Compliant ● ESD (HBM) : 5KV <p>Application</p> <ul style="list-style-type: none"> ● Power switch ● DC/DC converters 	<p>Schematic diagram</p> <p>SOT-23-3L</p>
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Ordering Information			
Device	Package	Marking	Packaging
G1003A	SOT-23-3L	G1003A	3000pcs/Reel

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Continuous Drain Current	I_D	3	A
Pulsed Drain Current (note1)	I_{DM}	12	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	1.5	W
Single pulse avalanche energy (note2)	E_{AS}	7	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ\text{C}$

Thermal Resistance			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient, $t \leq 10s$	R_{thJA}	83.3	$^\circ\text{C/W}$

Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	1	μA
Gate-Source Leakage	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}$	--	--	± 30	μA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.0	1.7	2.5	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 3\text{A}$	--	96	120	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 3\text{A}$	--	105	130	
Forward Transconductance	g_{FS}	$V_{\text{GS}} = 5\text{V}, I_D = 3\text{A}$	--	5	--	S
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 50\text{V}, f = 1.0\text{MHz}$	--	532	--	pF
Output Capacitance	C_{oss}		--	30	--	
Reverse Transfer Capacitance	C_{rss}		--	22	--	
Total Gate Charge	Q_g	$V_{\text{DD}} = 50\text{V}, I_D = 3\text{A}, V_{\text{GS}} = 10\text{V}$	--	17	--	nC
Gate-Source Charge	Q_{gs}		--	2	--	
Gate-Drain Charge	Q_{gd}		--	3	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 50\text{V}, I_D = 3\text{A}, R_G = 3\Omega$	--	5	--	ns
Turn-on Rise Time	t_r		--	4	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	17	--	
Turn-off Fall Time	t_f		--	4	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I_S	$T_C = 25^\circ\text{C}$	--	--	3	A
Body Diode Voltage	V_{SD}	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 3\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	Q_{rr}	$I_F = 3\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A/us}$	--	12	--	nC
Reverse Recovery Time	T_{rr}		--	17	--	ns

Notes

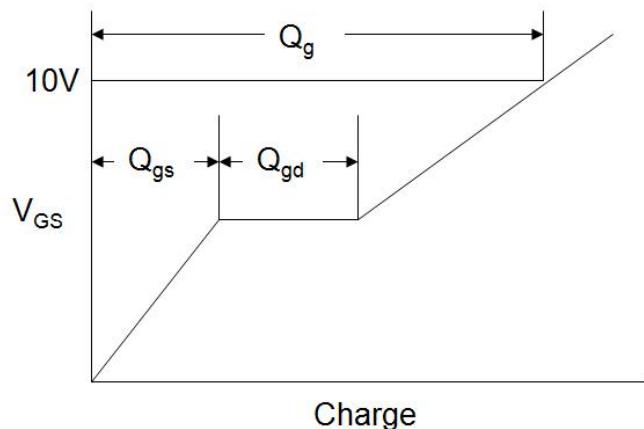
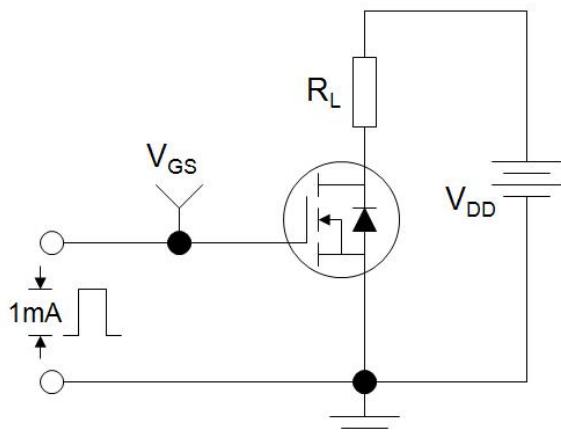
1. Repetitive Rating: Pulse width limited by maximum junction temperature

2. EAS condition : $T_J=25^\circ\text{C}$, $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.5\text{mH}$, $R_G=25\Omega$

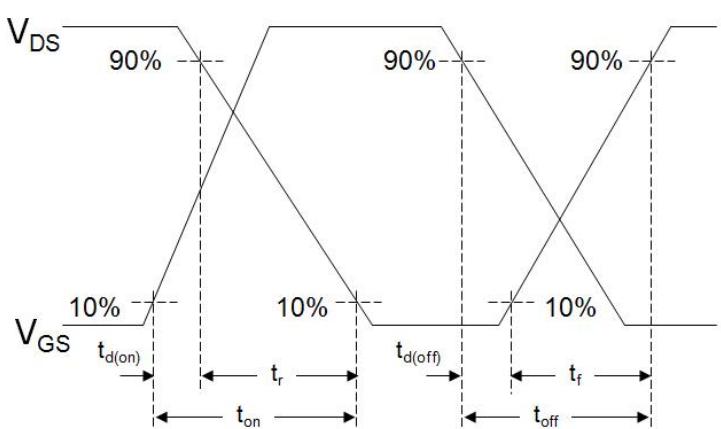
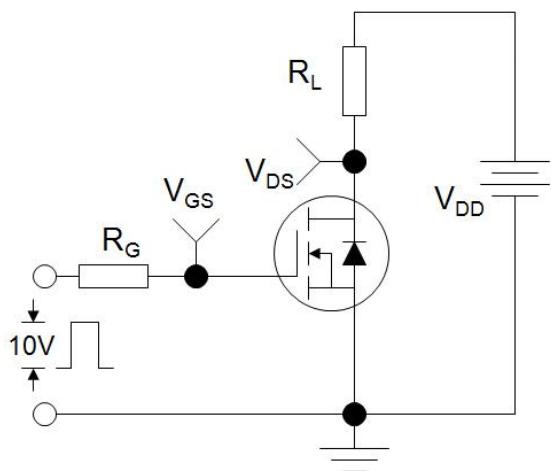
The table shows the minimum avalanche energy, which is 20mJ when the device is tested until failure

3. Identical low side and high side switch with identical R_G

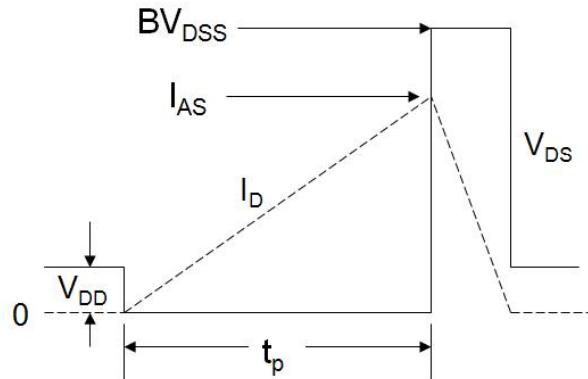
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

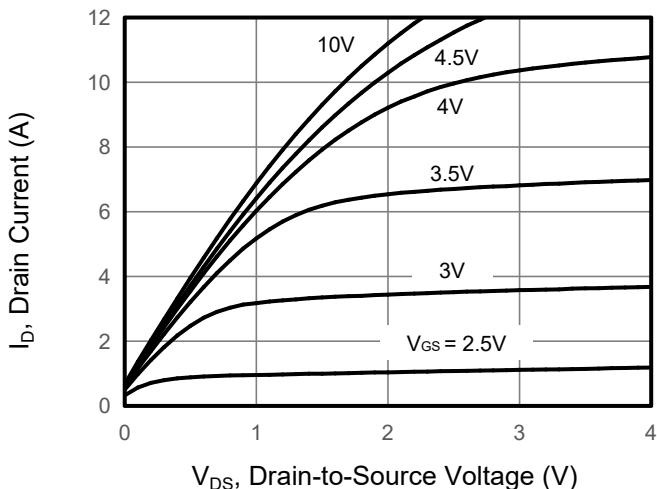


Figure 2. Transfer Characteristics

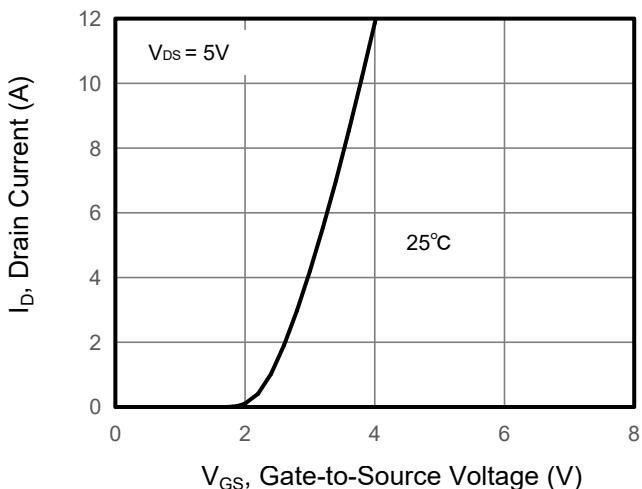


Figure 3. Drain Source On Resistance

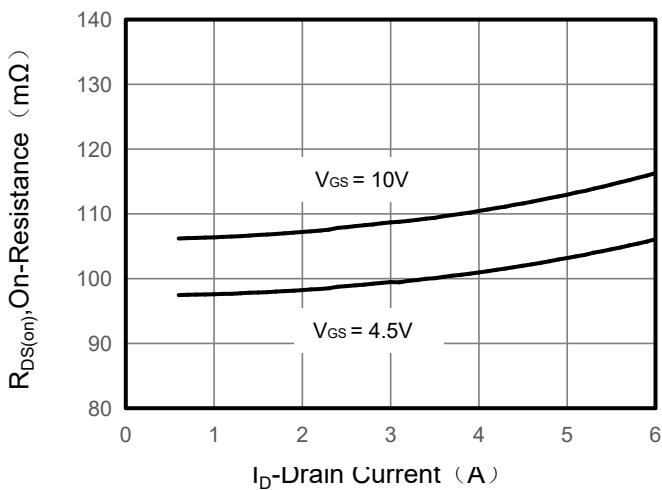


Figure 4. Gate Charge

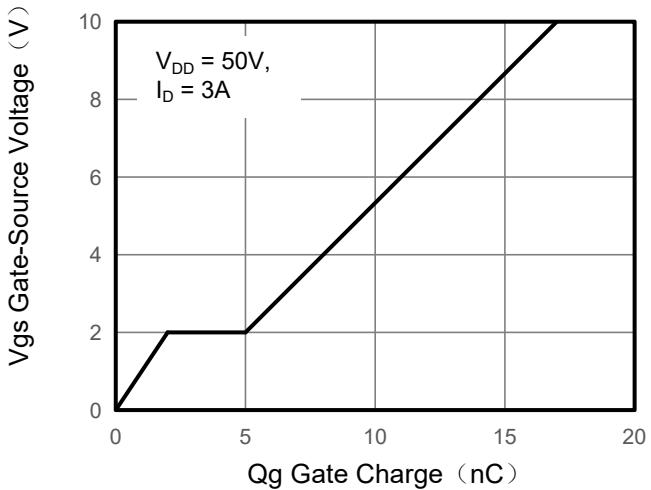


Figure 5. Capacitance

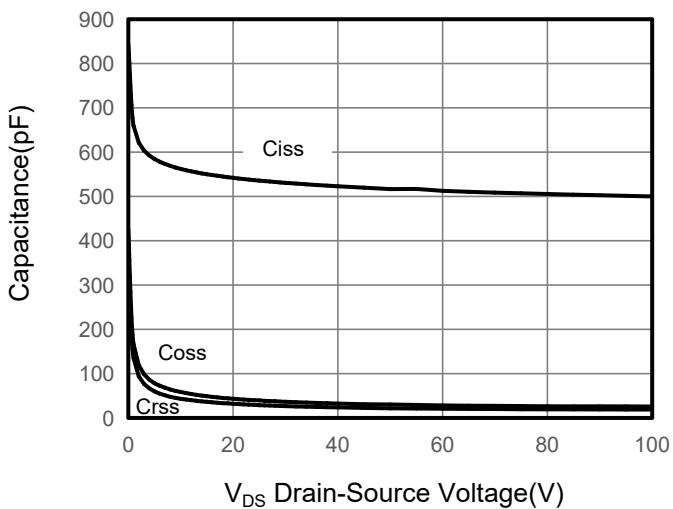
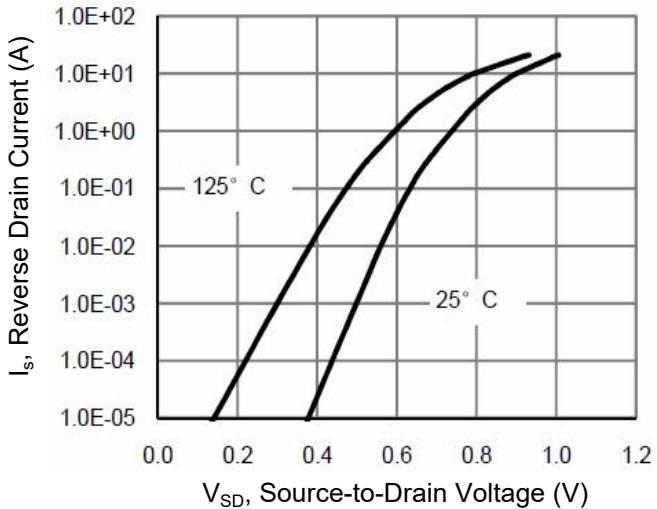


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

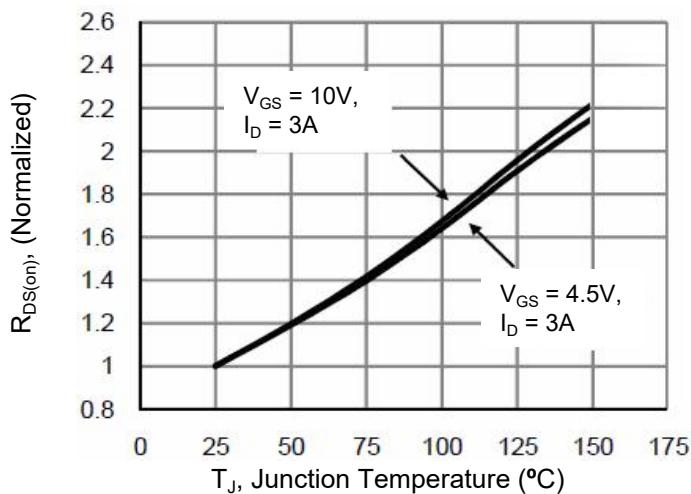


Figure 8. Safe Operation Area

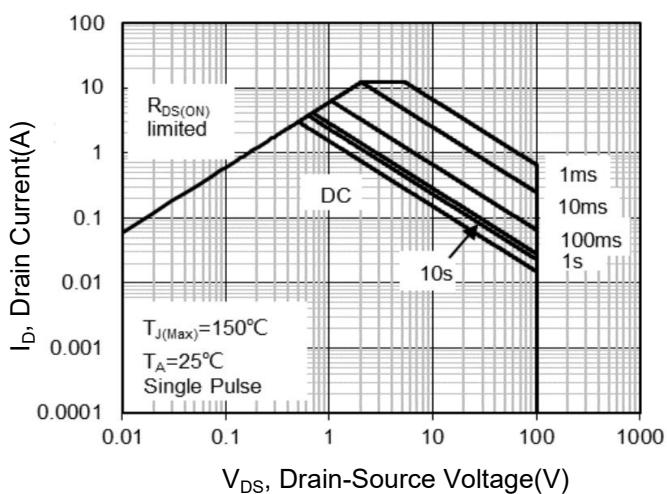
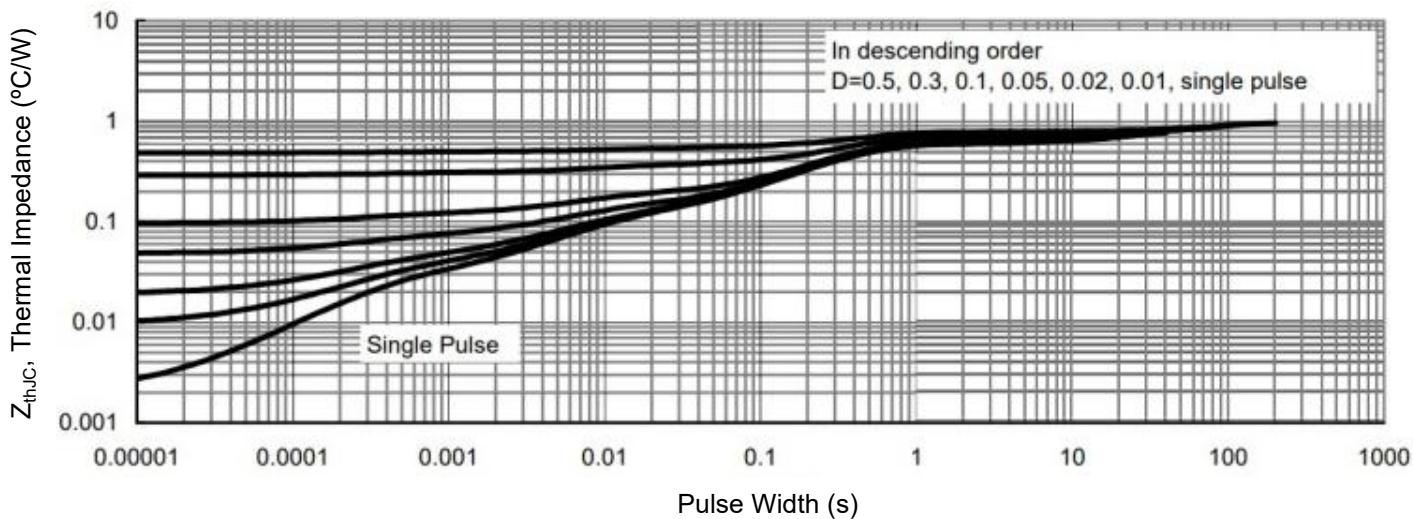
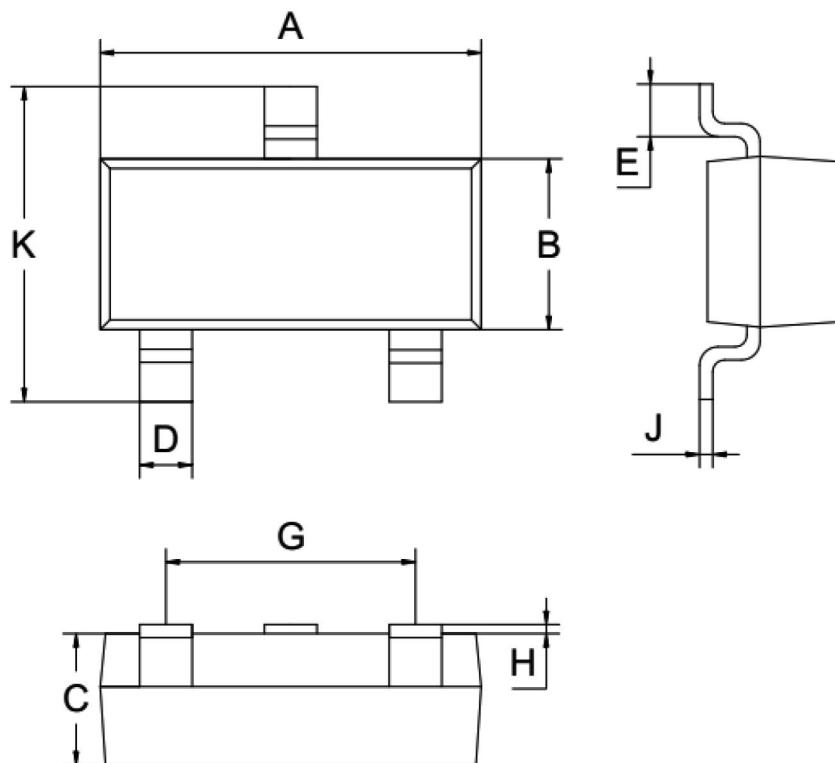


Figure 9. Normalized Maximum Transient Thermal Impedance



SOT-23-3L Package Information

Symbol	Dimensions in Millimeters		
	MIN.	NOM.	MAX.
A	2.80	2.90	3.00
B	1.50	1.60	1.70
C	1.00	1.10	1.20
D	0.30	0.40	0.50
E	0.25	0.40	0.55
G		1.90	
H	0.00	-	0.10
J	0.047	0.127	0.207
K	2.60	2.80	3.00

All Dimensions in mm