



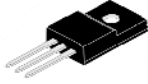
Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



Sensitive Gate SCR

BT151X



TO-220FP



SYMBOL

TO-220FP
Fully Isolated
Plastic Package
RoHS compliant

GENERAL DESCRIPTION:

The BT151X SCR is suitable to fit all models of control found in applications such as motor control, Industrial and Domestic lighting, heating and static switching, voltage regulation circuits.

FEATURES:

1. High blocking voltage
2. Low on-state voltage and high I_{TSM}
3. High heat dissipation and durability

APPLICATIONS: Its application is in solid state relay, motorcycle, power charger, T-tools etc

ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	VALUE	UNIT
Repetitive Peak Off-State Voltage	V_{DRM}	BT151X - 650/800	650/800	V
Repetitive Peak Reverse Voltage	V_{RRM}		650/800	
Average on-state current	$I_{T(AV)}$	half sine wave, $T_{amb} \leq 109^\circ C$	7.5	A
On-State RMS Current	$I_{T(RMS)}$	All conduction angles	12	A
Non Repetitive Surge Peak On-State Current	I_{TSM}	full Sine wave, $T_J = 25^\circ C$, $t = 10ms$	110	A
I ² t Value for Fusing	I^2t	$t = 10ms$	50	A ² s
Repetitive Rate of Rise of On-State Current after Triggering	di/dt	$I_{TM} = 20A$, $I_G = 0.2A$, $dI_G/dt = 0.2A/ms$	50	A/ μs
Peak Gate Current	I_{GM}		2	A
Peak Gate Voltage	V_{GM}			V
Peak reverse gate current	V_{RGM}			V
Peak gate power	P_{GM}			W
Average Gate Power	$P_{G(AV)}$	Over any 20ms period		W
Storage Temperature	T_{stg}		-40 to +150	°C
Operation Junction Temperature	T_J		125	°C

THERMAL RESISTANCES

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Thermal Resistance, Junction to Case	R_{thJ-C}			1.3	K/W
Thermal Resistance, Junction to Ambient	R_{thJ-A}		60		K/W

BT151X

Rev01 06102020E



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ELECTRICAL CHARACTERISTICS at ($T_a = 25^\circ\text{C}$ Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	VALUE			UNIT
			MIN	TYP	MAX	
Peak Repetitive Forward Blocking Current	I_{DRM}	$V_{\text{DM}}=V_{\text{DRM}(\text{MAX})};$ $T_j=125^\circ\text{C}$		0.1	0.5	mA
Peak Repetitive Reverse Blocking Current	I_{RRM}	$V_{\text{RM}}=V_{\text{RRM}(\text{MAX})};$ $T_j=125^\circ\text{C}$		0.1	0.5	mA
Peak On-State Voltage	V_{TM}	$I_{\text{TM}}=23\text{A}$		1.45	1.8	V
Gate Trigger Current	I_{GT}	$V_{\text{DM}}=12\text{V}, I_{\text{T}}=0.1\text{A}$	1	4	15	mA
Gate Trigger Voltage	V_{GT}	$V_{\text{DM}}=12\text{V}, I_{\text{T}}=0.1\text{A}$		0.7	1.5	V
Holding Current	I_{H}	$V_{\text{DM}}=12\text{V}, I_{\text{GT}}=0.1\text{A}$		7	20	mA
Latching Current	I_{L}	$V_{\text{DM}}=12\text{V}, I_{\text{GT}}=0.1\text{A}$		10	40	mA
Rise of Off-State Voltage	dV/dt	$V_{\text{DM}}=67\% V_{\text{DRM}(\text{MAX})};$ $T_j=125^\circ\text{C}$ Gate Open	50	130		V/ μs
Gate controlled turn-on time	t_{gt}	$T_{\text{M}}=40\text{A}, V_{\text{DM}}=V_{\text{DRM}(\text{MAX})};$ $I_{\text{G}}=0.1\text{A}, dI_{\text{G}}/dt=5\text{A}/\mu\text{S}$		2		μs

TYPICAL CHARACTERISTICS CURVES

Fig 1: Maximum On-state dissipation P_{tot} Versus
Average On-state Current, $I_{T(AV)}$

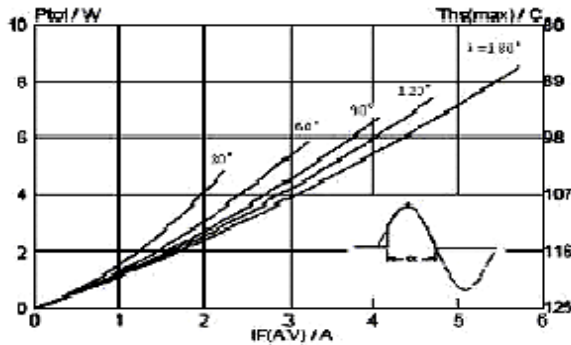


Fig 2: Typical and Maximum On-state
Characteristics

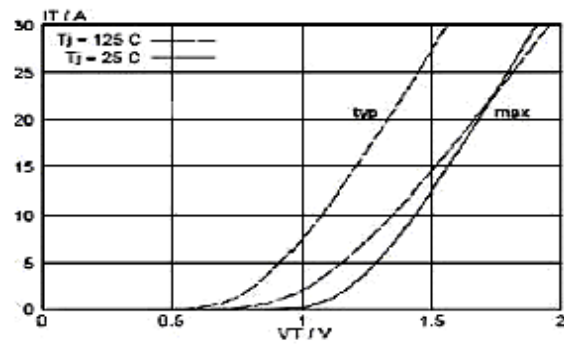
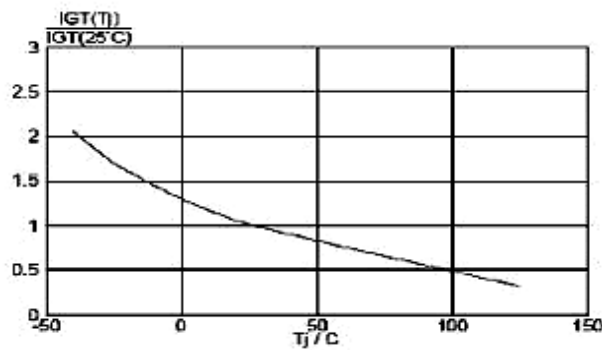


Fig 3 : Normalized gate trigger current $I_{GT}(T_J)$, $I_{GT}(25^\circ\text{C})$,
Versus Junction Temperature T_J





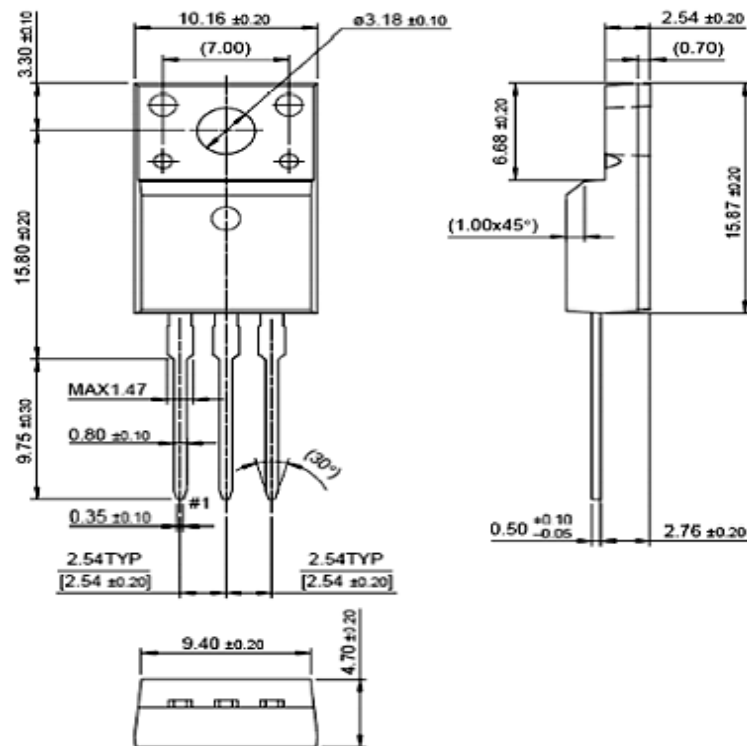
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PACKAGE DETAILS

TO-220FP Plastic Package



PIN CONFIGURATION

1. CATHODE
2. ANODE
3. GATE



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Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



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Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

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