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A Product Line of
Diodes Incorporated

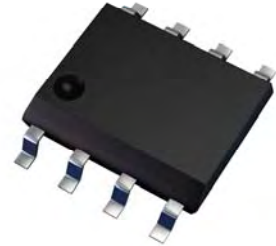
ZETEX

ZXMP3F36N8

30V SO8 P-channel enhancement mode MOSFET

Summary

$V_{(BR)DSS}$ (V)	$R_{DS(on)}$ (Ω)	I_D (A)
-30	0.020 @ $V_{GS}=-10V$	-12.6
	0.028 @ $V_{GS}=-4.5V$	



Description

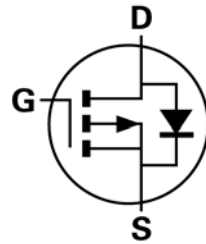
This new generation Trench MOSFET from Zetex has been designed to minimize the on-state resistance ($R_{DS(on)}$) and yet maintain superior switching performance.

Features

- Low on-resistance
- SO8 package

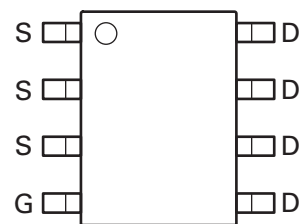
Applications

- Battery Protection
- Battery disconnect
- Power management functions



Ordering information

Device	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXMP3F36N8TA	7	12	500



Top view

Device marking

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Absolute maximum ratings

Parameter	Symbol	Limit	Unit
Drain-Source voltage	V_{DSS}	-30	V
Gate-Source voltage	V_{GS}	± 20	V
Continuous Drain current @ $V_{GS} = -10V$; $T_A = 25^\circ C$ ^(b) @ $V_{GS} = -10V$; $T_A = 70^\circ C$ ^(b) @ $V_{GS} = -10V$; $T_A = 25^\circ C$ ^(a) @ $V_{GS} = -10V$; $T_L = 25^\circ C$ ^(d)	I_D	-9.6 -7.7 -7.2 -12.6	V
Pulsed Drain current ^(c)	I_{DM}	-45	A
Continuous Source current (Body diode) ^(b)	I_S	-4.7	A
Pulsed Source current (Body diode) ^(c)	I_{SM}	-45	A
Power dissipation at $T_A = 25^\circ C$ ^(a) Linear derating factor	P_D	1.56 12.5	W mW/°C
Power dissipation at $T_A = 25^\circ C$ ^(b) Linear derating factor	P_D	2.8 22.2	W mW/°C
Power dissipation at $T_L = 25^\circ C$ ^(d) Linear derating factor	P_D	4.7 37.9	W mW/°C
Operating and storage temperature range	T_j, T_{stg}	-55 to 150	°C

Thermal resistance

Parameter	Symbol	Value	Unit
Junction to ambient ^(a)	$R_{\theta JA}$	80	°C/W
Junction to ambient ^(b)	$R_{\theta JA}$	45	°C/W
Junction to lead ^(d)	$R_{\theta JL}$	26.4	°C/W

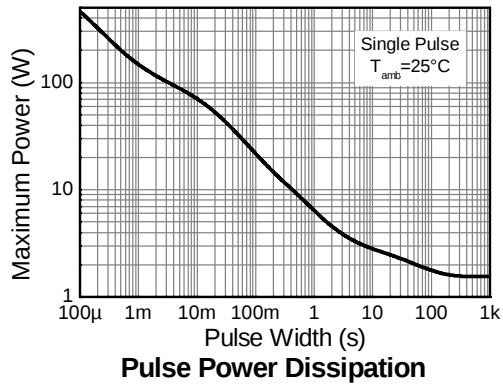
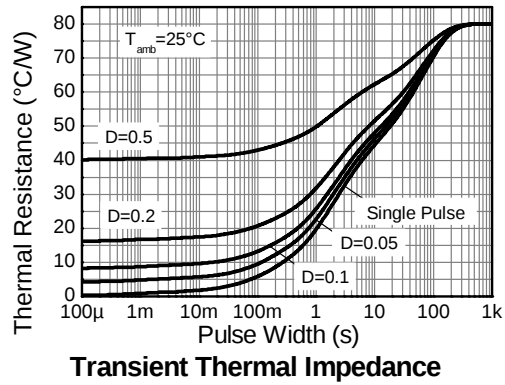
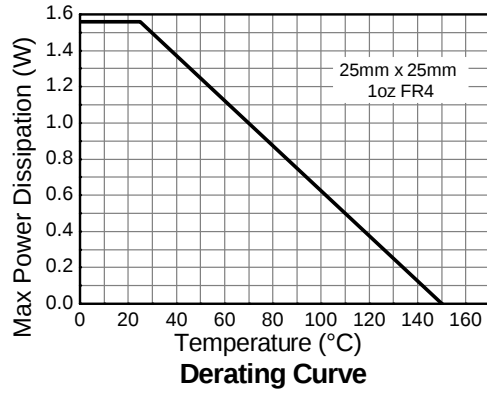
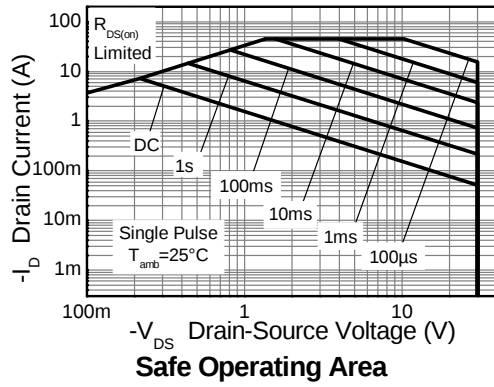
NOTES:

- (a) For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.
- (b) Mounted on FR4 PCB measured at $t \leq 10$ sec.
- (c) Repetitive rating on 25mm x 25mm FR4 PCB, $D=0.02$, pulse width 300us – pulse width limited by maximum junction temperature.
- (d) Thermal resistance from junction to solder-point (at the end of the drain lead).

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Thermal characteristics



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Electrical characteristics (at $T_{amb} = 25^{\circ}\text{C}$ unless otherwise stated)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Static						
Drain-Source breakdown voltage	V _{(BR)DSS}	-30			V	I _D = -250μA, V _{GS} =0V
Zero Gate voltage Drain current	I _{DSS}			-1.0	μA	V _{DS} =-30V, V _{GS} =0V
Gate-Body leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V
Gate-Source threshold voltage	V _{GS(th)}	-1.3		-2.5	V	I _D = -250μA, V _{DS} =V _{GS}
Static Drain-Source on-state resistance (*)	R _{DS(on)}			0.020 0.028	Ω	V _{GS} = -10V, I _D = -10A V _{GS} = -4.5V, I _D = -8.0A
Forward Transconductance (*) (†)	g _{fs}		29		S	V _{DS} = -15V, I _D = -10A
Dynamic (†)						
Input capacitance	C _{iss}		2265		pF	V _{DS} = -15V, V _{GS} =0V f=1MHz
Output capacitance	C _{oss}		424		pF	
Reverse transfer capacitance	C _{rss}		266		pF	
Switching (‡) (†)						
Turn-on-delay time	t _{d(on)}		3.1		ns	V _{DD} = -15V, V _{GS} = -10V I _D = -1A R _G ≐ 6.0Ω,
Rise time	t _r		5		ns	
Turn-off delay time	t _{d(off)}		75		ns	
Fall time	t _f		40		ns	
Gate charge						
Total Gate charge	Q _g		43.9		nC	V _{DS} = -15V, V _{GS} = -10V I _D = -10A
Gate-Source charge	Q _{gs}		6		nC	
Gate-Drain charge	Q _{gd}		9.8		nC	
Source–Drain diode						
Diode forward voltage (*)	V _{SD}		-0.73	-1.2	V	I _S = -1.7A,V _{GS} =0V
Reverse recovery time (‡)	t _{rr}		17.7		ns	I _S = -2.9A,di/dt=100A/μs
Reverse recovery charge(‡)	Q _{rr}		11.7		nC	

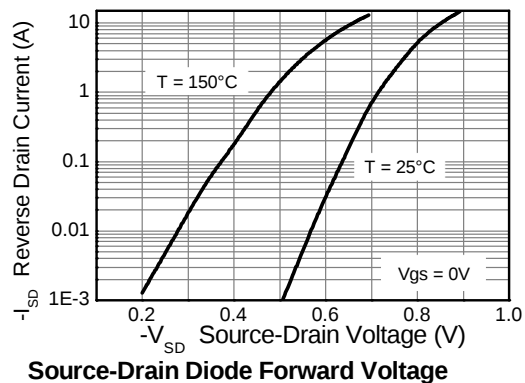
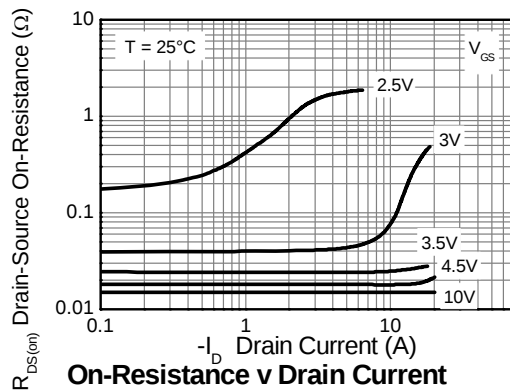
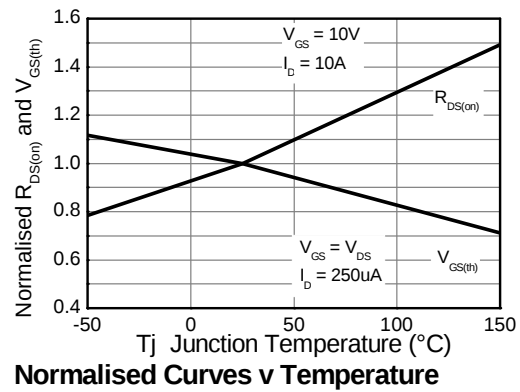
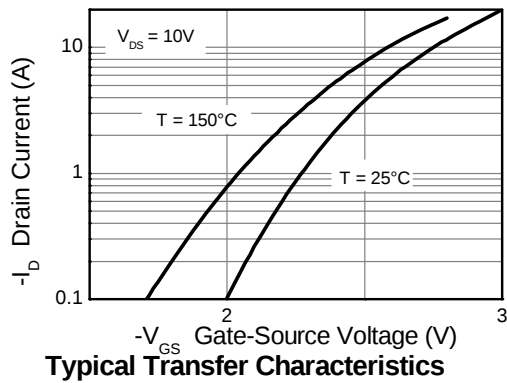
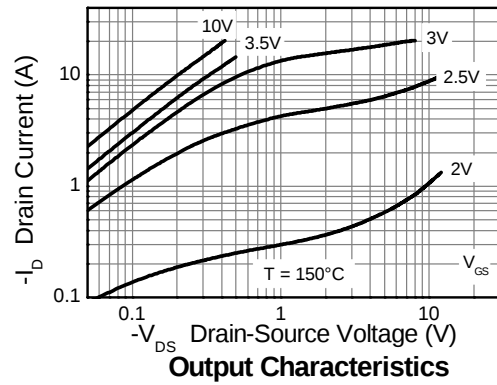
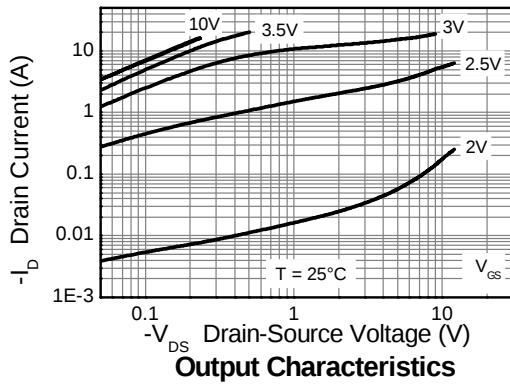
NOTES:

(*) Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.

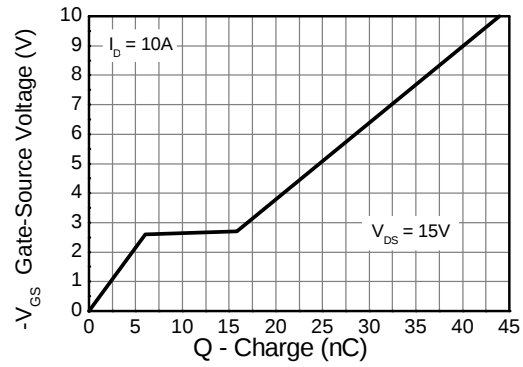
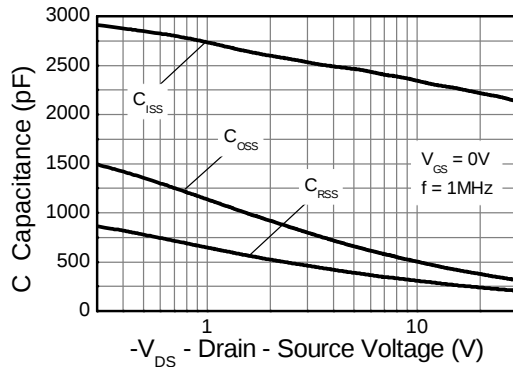
(†) Switching characteristics are independent of operating junction temperature.

(‡) For design aid only, not subject to production testing

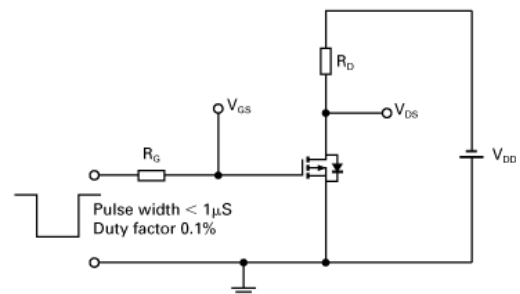
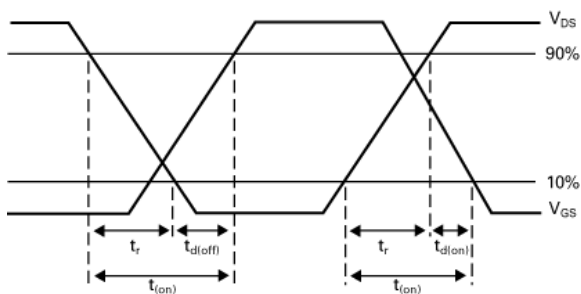
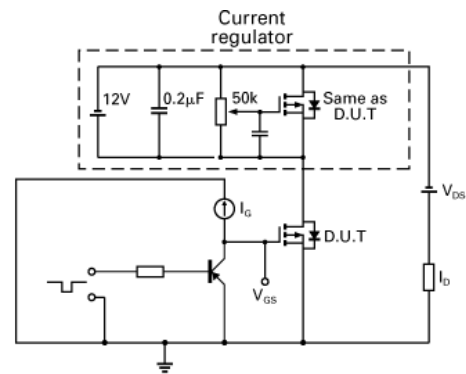
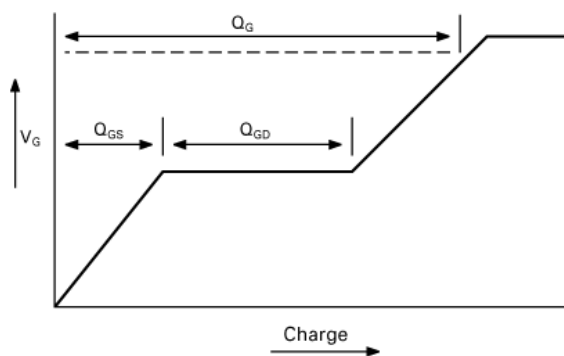
Typical characteristics



Typical characteristics



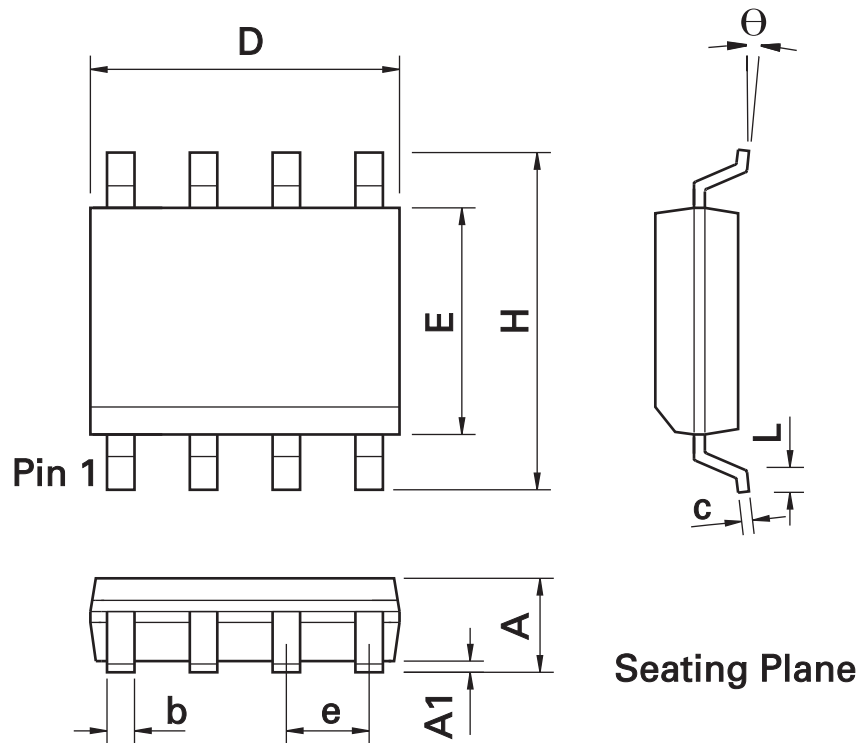
Test circuits



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Package outline SO8



SO8 Package Information

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.053	0.069	1.35	1.75	e	0.050 BSC		1.27 BSC	
A1	0.004	0.010	0.10	0.25	b	0.013	0.020	0.33	0.51
D	0.189	0.197	4.80	5.00	c	0.008	0.010	0.19	0.25
H	0.228	0.244	5.80	6.20	U	0°	8°	0°	8°
E	0.150	0.157	3.80	4.00	h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27	-	-	-	-	-

Note: Controlling dimensions are in inches. Approximate dimensions are provided in millimeters

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Semiconductor devices are susceptible to damage by ESD. Suitable precautions should be taken when handling and transporting devices. The possible damage to devices depends on the circumstances of the handling and transporting, and the nature of the device. The extent of damage can vary from immediate functional or parametric malfunction to degradation of function or performance in use over time. Devices suspected of being affected should be replaced.

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"Preview"	Future device intended for production at some point. Samples may be available
"Active"	Product status recommended for new designs
"Last time buy (LTB)"	Device will be discontinued and last time buy period and delivery is in effect
"Not recommended for new designs"	Device is still in production to support existing designs and production
"Obsolete"	Production has been discontinued

Datasheet status key:

"Draft version"	This term denotes a very early datasheet version and contains highly provisional information, which may change in any manner without notice.
"Provisional version"	This term denotes a pre-release datasheet. It provides a clear indication of anticipated performance. However, changes to the test conditions and specifications may occur, at any time and without notice.
"Issue"	This term denotes an issued datasheet containing finalized specifications. However, changes to specifications may occur, at any time and without notice.

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